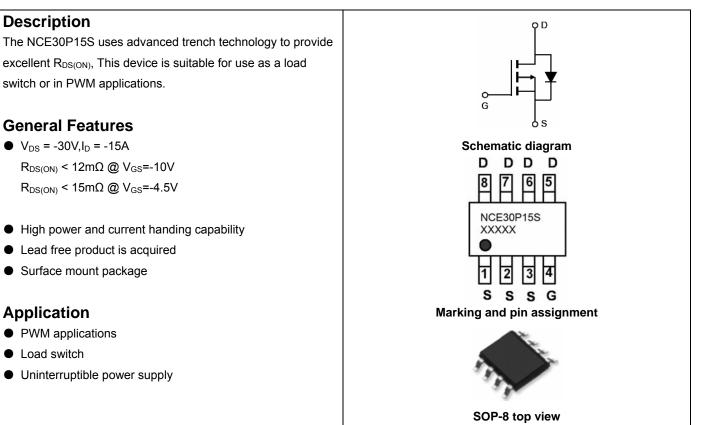


NCE P-Channel Enhancement Mode Power MOSFET



Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
NCE30P15S	NCE30P15S	SOP-8	Ø330mm	12mm	4000 units

Absolute Maximum Ratings (T_A=25℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit				
Drain-Source Voltage	Vds	-30	V				
Gate-Source Voltage	Vgs	±20	V				
Drain Current-Continuous	I _D	-15	А				
Drain Current-Pulsed (Note 1)	I _{DM}	-60	А				
Single pulse avalanche energy (Note 5)	E _{AS}	420	mJ				
Maximum Power Dissipation	PD	3.1	W				
Operating Junction and Storage Temperature Range	T _J ,T _{STG}	-55 To 150	°C				
Thermal Characteristic							
Thermal Resistance, Junction-to-Ambient (Note 2)	R _{eJA}	40	°C/W				
Thermal Resistance, Junction-to-Lead (Note 2)	R _{θJL}	24	°C/W				

Electrical Characteristics (T_A=25[°]C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250µA	-30	-33	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-30V,V _{GS} =0V	-	-	-1	μA



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Gate-Body Leakage Current	I _{GSS}	I _{GSS} V _{GS} =±20V,V _{DS} =0V		-	±100	nA	
On Characteristics (Note 3)							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1.0	-1.5	-2.2	V	
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-10V, I _D =-15A	-	8.5	12	mΩ	
Drain-Source On-State Resistance		V _{GS} =-4.5V, I _D =-10A	-	11.5	15	mΩ	
Forward Transconductance	g fs	V _{DS} =-5V,I _D =-15A	30	-	-	S	
Dynamic Characteristics (Note4)							
Input Capacitance	C _{lss}	(-15)()(-0)(-	2900	-	PF	
Output Capacitance	Coss	- V _{DS} =-15V,V _{GS} =0V, - F=1.0MHz	-	410	-	PF	
Reverse Transfer Capacitance	Crss		-	280	-	PF	
Switching Characteristics (Note 4)							
Turn-on Delay Time	t _{d(on)}		-	15	-	nS	
Turn-on Rise Time	tr	V _{DD} =-15V, ID=-10A,	-	11	-	nS	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{GEN} =3 Ω	-	44	-	nS	
Turn-Off Fall Time	t _f		-	21	-	nS	
Total Gate Charge	Qg		-	48	-	nC	
Gate-Source Charge	Q _{gs}	V _{DS} =-15V,I _D =-10A,V _{GS} =-10V	-	12	-	nC	
Gate-Drain Charge	Q _{gd}		-	14	-	nC	
Drain-Source Diode Characteristics							
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-2A	-	-	-1.2	V	

Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, t \leq 10 sec. The $R_{\theta JA}$ is the sum of the thermal impedence from junction to lead $R_{\theta JL}$ and lead to ambient.

3. Pulse Test: Pulse Width \leq 300µs, Duty Cycle \leq 2%.

4. Guaranteed by design, not subject to production

5.EAS condition: Tj=25 $^\circ C$,V_{DD}=-15V,V_G=-10V,L=0.5mH,Rg=25\Omega



Typical Electrical and Thermal Characteristics

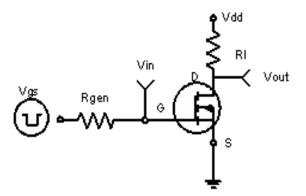
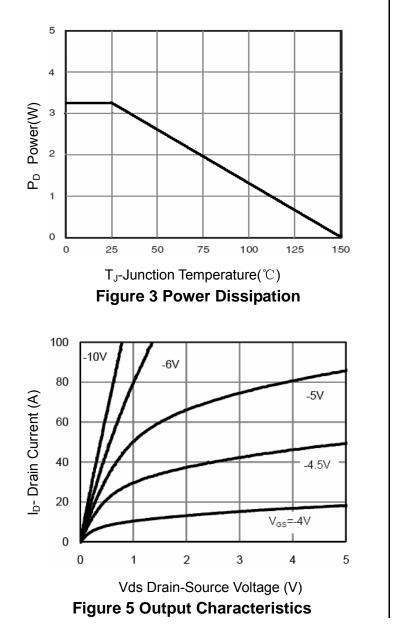
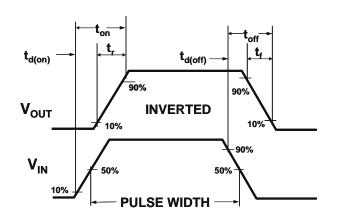
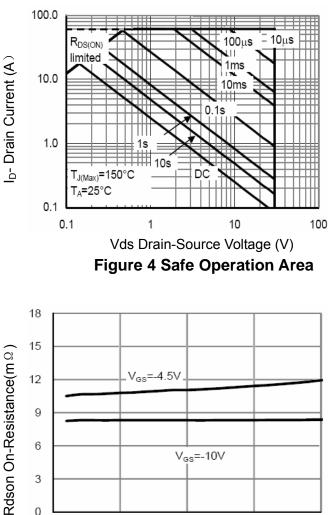


Figure 1 Switching Test Circuit









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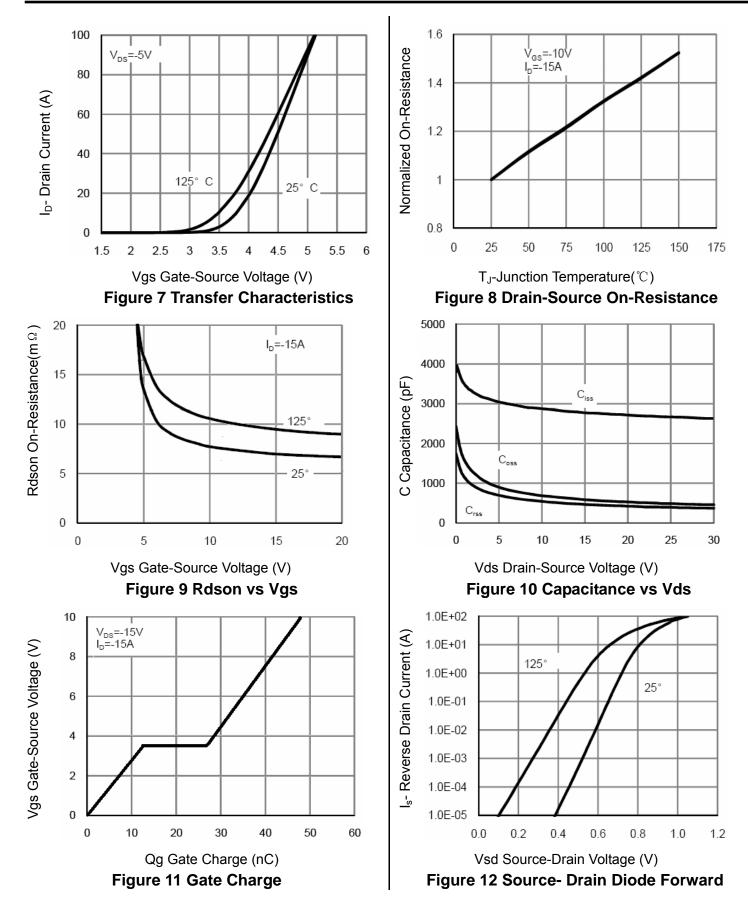
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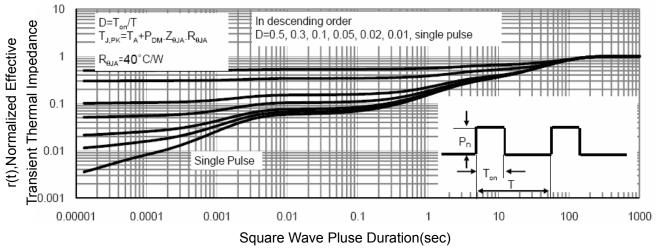
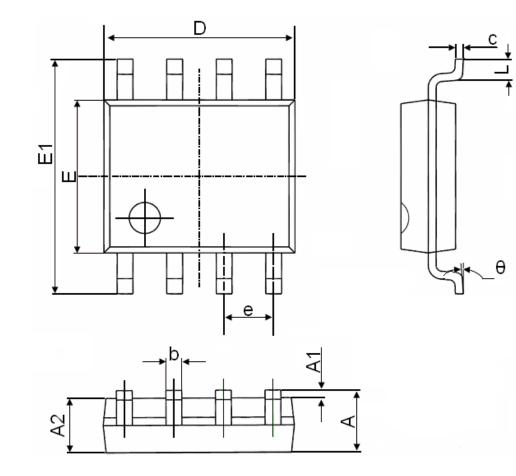


Figure 13 Normalized Maximum Transient Thermal Impedance



SOP-8 Package Information



Symbol	Dimensions	n Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	1.350	1.750	0.053	0.069	
A1	0.100	0.250	0.004	0.010	
A2	1.350	1.550	0.053	0.061	
b	0.330	0.510	0.013	0.020	
С	0.170	0.250	0.006	0.010	
D	4.700	5.100	0.185	0.200	
E	3.800	4.000	0.150	0.157	
E1	5.800	6.200	0.228	0.244	
e	1.270	(BSC)	0.050	(BSC)	
L	0.400	1.270	0.016	0.050	
θ	0°	8°	0°	8°	



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