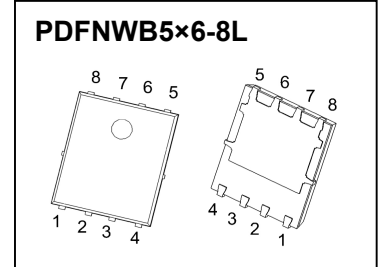


**CJAC50P03 P-Channel Power MOSFET**

$V_{(BR)DSS}$	$R_{DS(on)TYP}$	I_D
-30V	4.4mΩ@-10V	-50A

**DESCRIPTION**

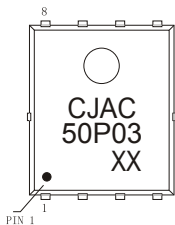
The CJAC50P03 uses advanced trench technology and design to provide excellent $R_{DS(ON)}$ with low gate charge. It can be used in a wide variety of applications

FEATURES

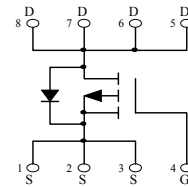
- High density cell design for ultra low $R_{DS(ON)}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high E_{AS}
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

APPLICATIONS

- Battery and loading switching

MARKING

CJAC50P03 = Part No. Solid
dot = Pin1 indicator.
XX = Code.

EQUIVALENT CIRCUIT**ABSOLUTE MAXIMUM RATINGS ($T_a=25^{\circ}\text{C}$ unless otherwise noted)**

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	$I_D^{①}$	-50	A
Pulsed Drain Current	$I_{DM}^{②}$	-200	A
Single Pulsed Avalanche Energy	$E_{AS}^{③}$	300	mJ
Maximum Power Dissipation	$P_D^{①}$	65	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}^{⑥}$	62.5	°C/W
Thermal Resistance from Junction to Case	$R_{\theta JC}^{①}$	1.92	°C/W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55~+150	°C

MOSFET ELECTRICAL CHARACTERISTICS

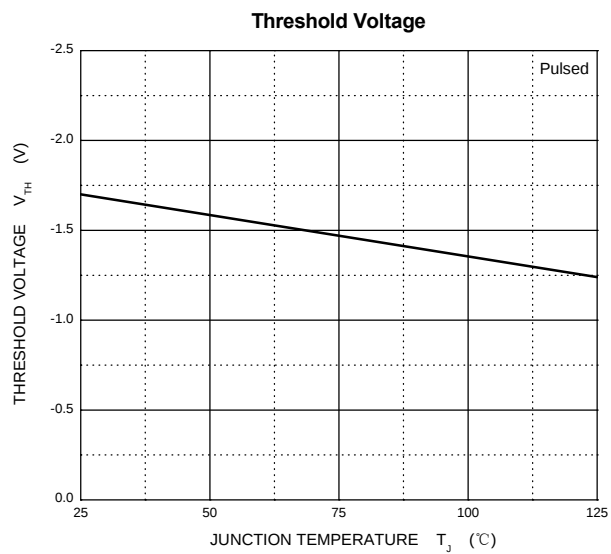
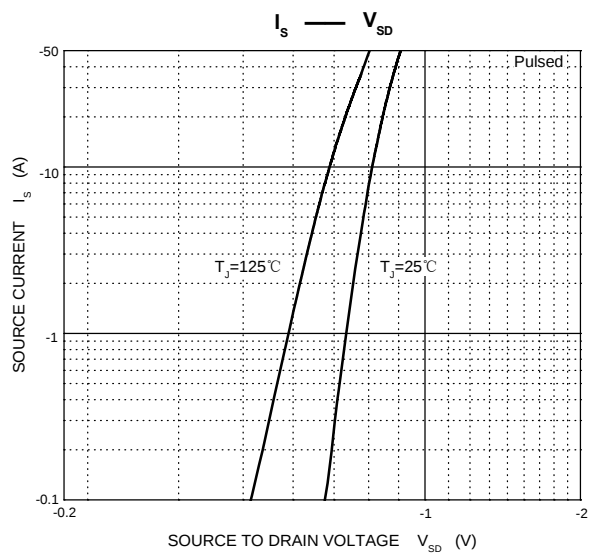
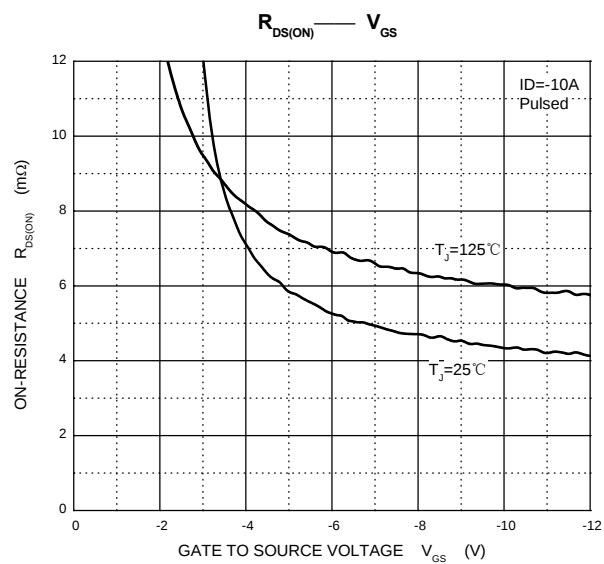
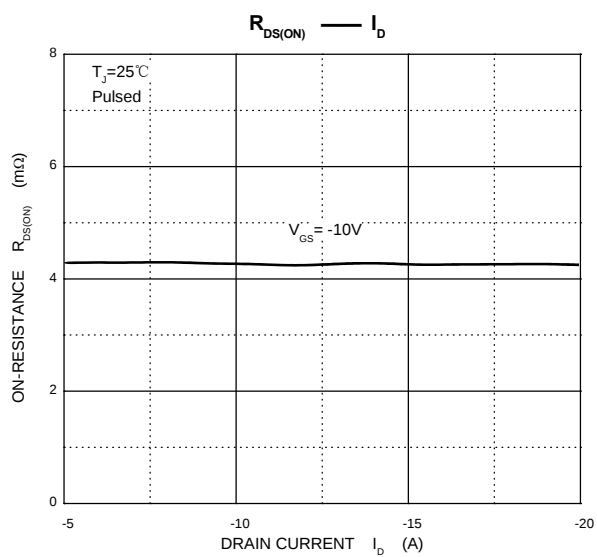
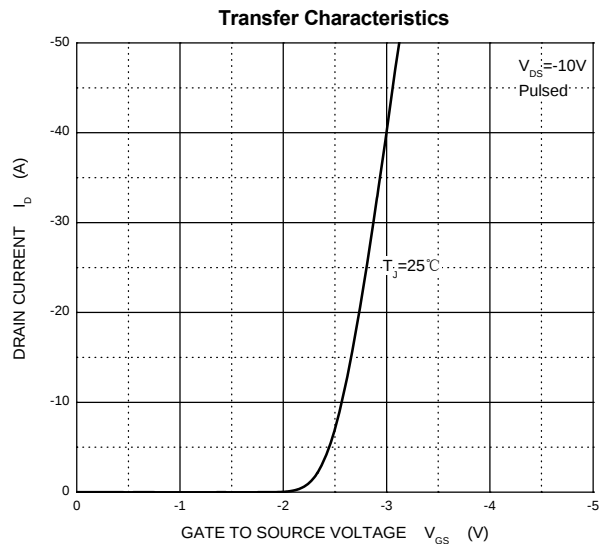
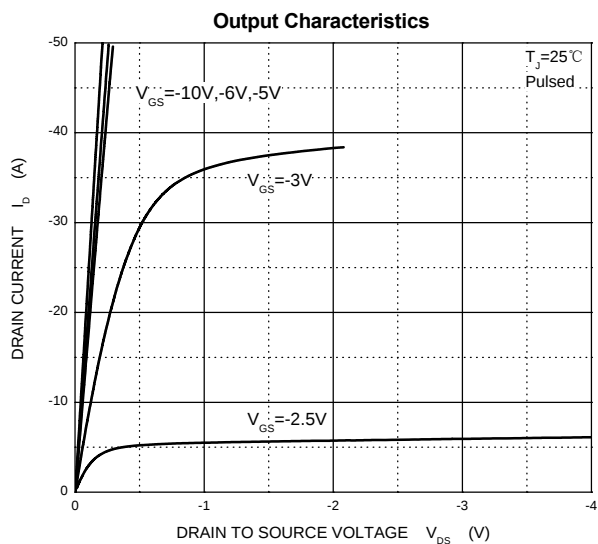
$T_a=25\text{ }^{\circ}\text{C}$ unless otherwise specified

Parameter	Symbol	Test Condition		Min	Typ	Max	Unit
Off characteristics							
Drain-source breakdown voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = -250\mu A$		-30			V
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -24V,$	$T_J = 25\text{ }^{\circ}\text{C}$			-1	μA
		$V_{GS} = 0V$	$T_J = 125\text{ }^{\circ}\text{C}$			-200	
Gate-body leakage current	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$				± 100	nA
On characteristics ^④							
Gate-threshold voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$		-1.0	-1.7	-2.5	V
Static drain-source on-sate resistance	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -10A$			4.4	7	m Ω
Forward transconductance	g_{FS}	$V_{DS} = -10V, I_D = -15A$			20		S
Dynamic characteristics ^{④ ⑤}							
Input capacitance	C_{iss}	$V_{DS} = -15V, V_{GS} = 0V,$ $f = 1MHz$			3590	7180	pF
Output capacitance	C_{oss}				695	1390	
Reverse transfer capacitance	C_{rss}				665	1330	
Switching characteristics ^{④ ⑤}							
Total gate charge	Q_g	$V_{GS} = -10V,$ $V_{DS} = -15V, I_D = -10A$			84	168	nC
Gate-source charge	Q_{gs}				11.7	23	
Gate-drain charge	Q_{gd}				25	50	
Turn-on delay time	$t_{d(on)}$	$V_{DD} = -15V, I_D = -10A,$ $V_{GS} = -10V, R_G = 6\Omega$			13		ns
Turn-on rise time	t_r				12		
Turn-off delay time	$t_{d(off)}$				50		
Turn-off fall time	t_f				14		
Drain-Source Diode Characteristics							
Drain-source diode forward voltage	V_{SD} ^④	$V_{GS} = 0V, I_S = -10A$				-1.2	V
Continuous drain-source diode forward current	I_S ^①					-50	A
Pulsed drain-source diode forward current	I_{SM} ^②					-200	A

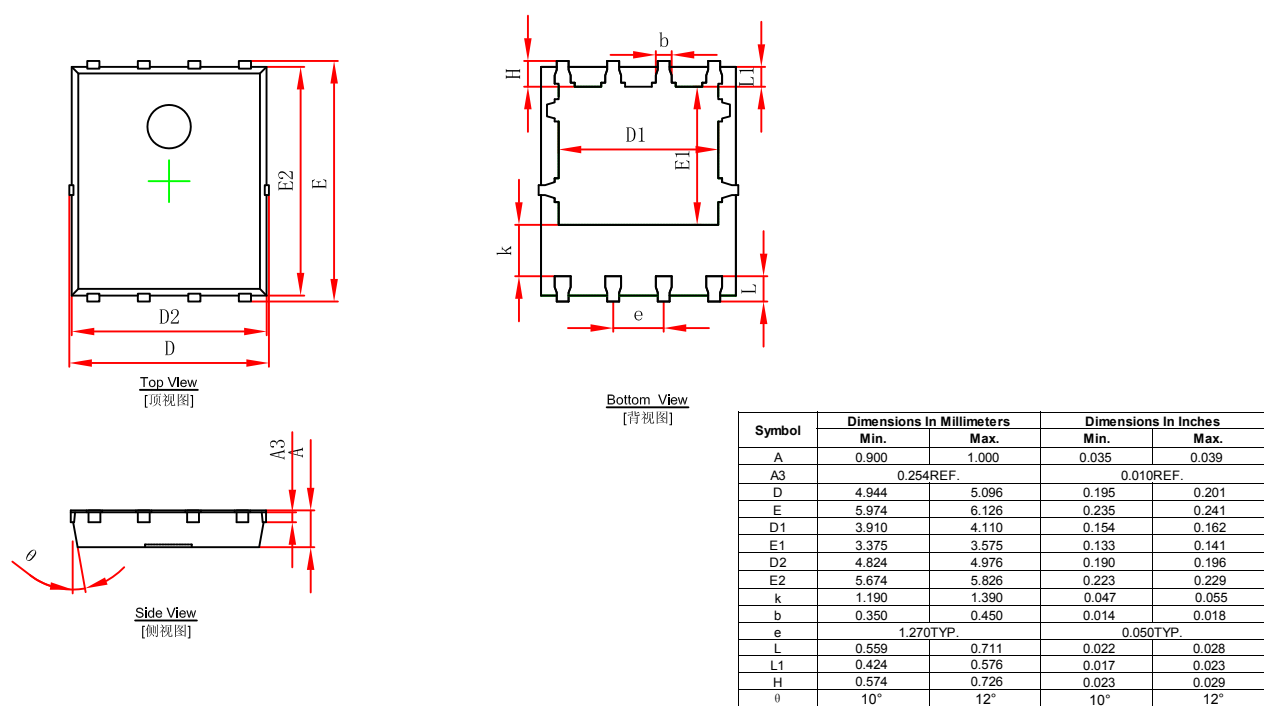
Notes:

1. $T_C = 25\text{ }^{\circ}\text{C}$ Limited only by maximum temperature allowed.
2. $P_W \leq 10\mu s$, Duty cycle $\leq 1\%$.
3. EAS condition: $V_{DD} = -15V, V_{GS} = -10V, L = 0.5mH, R_G = 25\Omega$ Starting $T_J = 25\text{ }^{\circ}\text{C}$.
4. Pulse Test : Pulse Width $\leq 300\mu s$, duty cycle $\leq 2\%$.
5. Guaranteed by design, not subject to production.
6. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in 2 FR-4 board with 2oz. Copper, in a still air environment with $T_a = 25\text{ }^{\circ}\text{C}$.

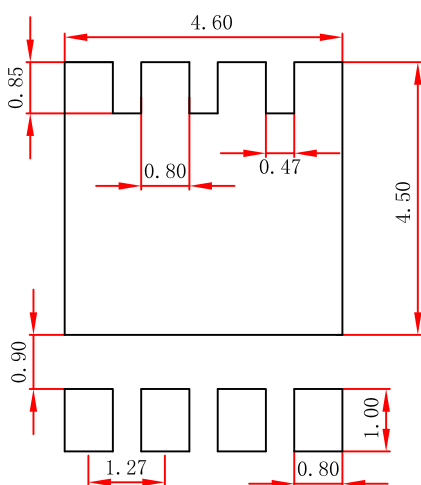
Typical Characteristics



PDFNWB5x6-8L Package Outline Dimensions



PDFNWB5x6-8L Suggested Pad Layout



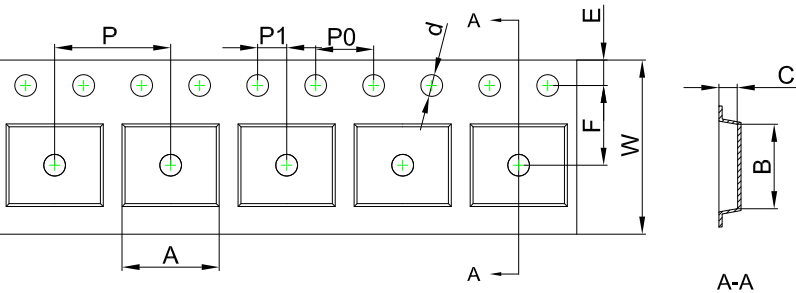
Note:
1.Controlling dimension:in millimeters.
2.General tolerance:±0.05mm.
3.The pad layout is for reference purposes only.

NOTICE

JCET reserve the right to make modifications,enhancements, improvements, corrections or other changes without further notice to any product herein.JCET does not assume any liability arising out of the application or use of any product described herein.

PDFNWB5×6 Tape and Reel

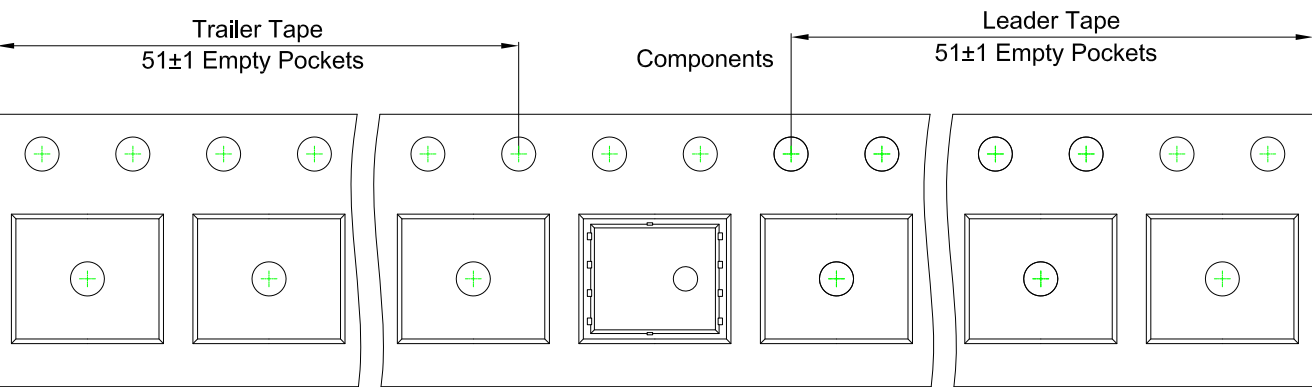
PDFNWB5×6-8L Embossed Carrier Tape



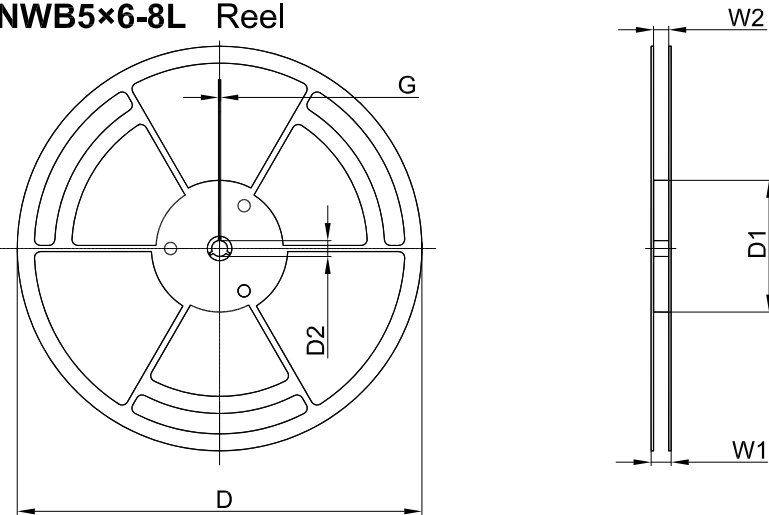
Packaging Description:
PDFNWB5×6-8L parts are shipped in tape. The carrier tape is made from a dissipative (carbon filled) polycarbonate resin. The cover tape is a multilayer film (Heat Activated Adhesive in nature) primarily composed of polyester film, adhesive layer, sealant, and anti-static sprayed agent. These reeled parts in standard option are shipped with 5,000 units per 13" or 33.0 cm diameter reel. The reels are clear in color and is made of polystyrene plastic (anti-static coated).

Dimensions are in millimeter										
Pkg type	A	B	C	d	E	F	P0	P	P1	W
PDFNWB5×6-8L	6.30	5.30	1.10	Ø1.50	1.75	5.50	4.00	8.00	2.00	12.00

PDFNWB5×6-8L Tape Leader and Trailer



PDFNWB5×6-8L Reel



Dimensions are in millimeter						
Reel Option	D	D1	D2	G	W1	W2
13"Dia	Ø330.00	100.00	13.00	1.90	17.60	12.40

REEL	Reel Size	Box	Box Size(mm)	Carton	Carton Size(mm)
5,000 pcs	13 inch	5,000 pcs	340×336×29	50,000 pcs	353×346×365