

isc N-Channel MOSFET Transistor

IPD50R800CE, IIPD50R800CE

• FEATURES

- Static drain-source on-resistance:
 $R_{DS(on)} \leq 800\text{m}\Omega$
- Enhancement mode:
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• DESCRIPTION

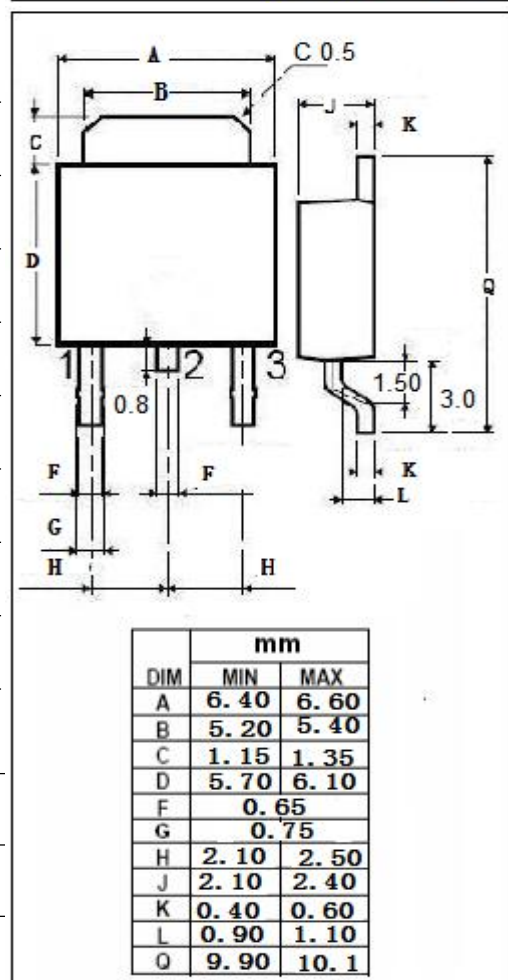
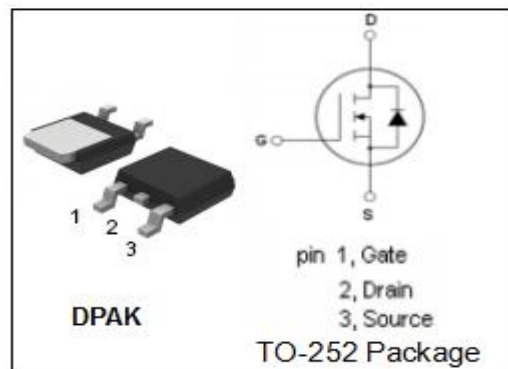
- Fast switching

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DS}	Drain-Source Voltage	500	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current-Continuous	7.6	A
I_{DM}	Drain Current-Single Pulsed	15.5	A
P_D	Total Dissipation @ $T_c=25^\circ\text{C}$	60	W
T_j	Max. Operating Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature	-55~150	$^\circ\text{C}$

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(j-c)}$	Channel-to-case thermal resistance	2.07	$^\circ\text{C/W}$
$R_{th(j-a)}$	Channel-to-ambient thermal resistance	62	$^\circ\text{C/W}$



isc N-Channel MOSFET Transistor**IPD50R800CE,IIPD50R800CE****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V; I _D =1mA	500			V
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} ; I _D =0.13 μA	2.5		3.5	V
R _{DS(on)}	Drain-Source On-Resistance	V _{GS} =13V; I _D =1.3A			800	mΩ
I _{GSS}	Gate-Source Leakage Current	V _{GS} =20V			0.1	μA
I _{DSS}	Drain-Source Leakage Current	V _{DS} =500V; V _{GS} = 0V			1	μA
V _{SD}	Diode forward voltage	I _F =1.9A, V _{GS} = 0V		0.83		V

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