

isc Silicon PNP Power Transistor

2SA771

DESCRIPTION

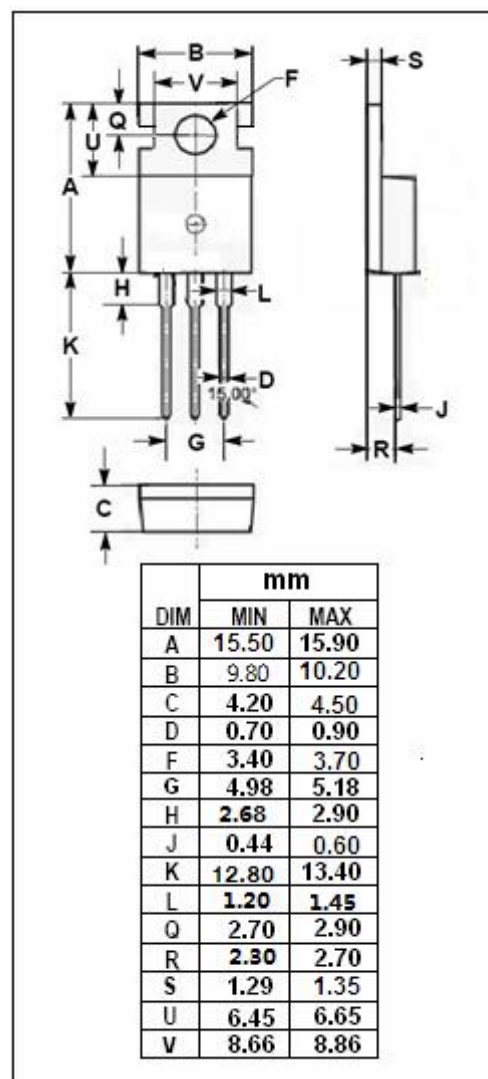
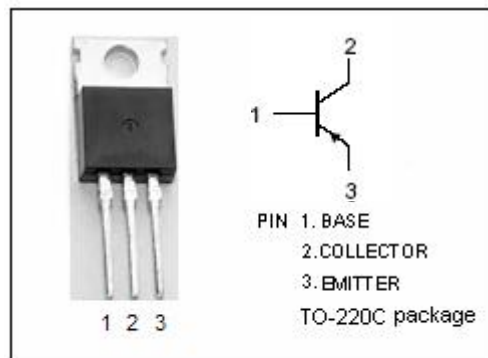
- Collector-Emitter Breakdown Voltage-
: $V_{(BR)CEO} = -80(V)(Min.)$
- Complement to Type 2SC1986
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for audio and general purpose applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^{\circ}C$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-Base Voltage	-80	V
V_{CEO}	Collector-Emitter Voltage	-80	V
V_{EBO}	Emitter-Base Voltage	-6	V
I_C	Collector Current-Continuous	-6	A
I_B	Base Collector Current-Continuous	-3	A
P_C	Total Power Dissipation @ $T_C=25^{\circ}C$	40	W
T_J	Junction Temperature	150	$^{\circ}C$
T_{stg}	Storage Temperature Range	-55~150	$^{\circ}C$



isc Silicon PNP Power Transistor**2SA771****ELECTRICAL CHARACTERISTICS****T_C=25°C unless otherwise specified**

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage	I _C = -25mA; I _B = 0	-80			V
V _{CE(sat)}	Collector-Emitter Saturation Voltage	I _C = -3A; I _B = -0.3A			-1.0	V
I _{CBO}	Collector Cutoff Current	V _{CB} = -80V; I _E = 0			-1.0	mA
I _{EBO}	Emitter Cutoff Current	V _{EB} = -6V; I _C = 0			-1.0	mA
h _{FE}	DC Current Gain	I _C = -1A; V _{CE} = -4V	40			
f _T	Current-Gain—Bandwidth Product	I _E = 0.5A; V _{CE} = -12V		10		MHz

Switching Times

t _r	Rise Time	I _C = -3A, R _L = 3 Ω, I _{B1} = -I _{B2} = -0.4A, V _{CC} = -9V		0.9		μ s
t _{stg}	Storage Time			1.0		μ s
t _f	Fall Time			0.1		μ s

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