

# **ISC Silicon PNP Power Transistor**

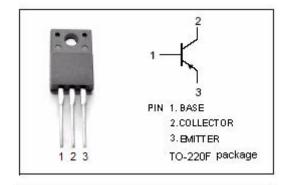
#### **DESCRIPTION**

- · Collector-Emitter Breakdown Voltage-
  - : V<sub>(BR)CEO</sub>= -30V(Min)
- · Low Collector Saturation Voltage-
  - :  $V_{CE(sat)} = -0.4V(Max)@ (I_C = -3A, I_B = -0.1A)$
- Minimum Lot-to-Lot variations for robust device performance and reliable operation



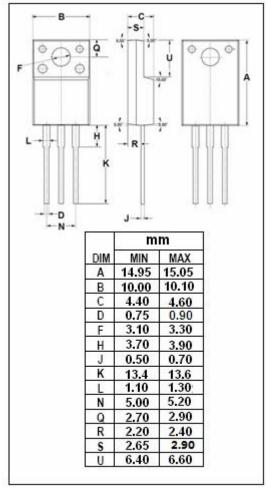
### **APPLICATIONS**

 Designed for switching regulator, driver and power switching applications.



## ABSOLUTE MAXIMUM RATINGS(Ta=25℃)

SYMBOL	PARAMETER	VALUE	UNIT	
V <sub>CBO</sub>	Collector-Base Voltage	-30	V	
Vceo	Collector-Emitter Voltage	-30	V	
$V_{EBO}$	Emitter-Base Voltage	-5	٧	
Ic	Collector Current-Continuous	-7	А	
lΒ	Base Current-Continuous	-1	А	
Pc	Collector Power Dissipation @Tc=25℃	40	W	
TJ	Junction Temperature	150	$^{\circ}$ C	
T <sub>stg</sub>	Storage Temperature	-55~150	$^{\circ}$ C	





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2SA1640

### **ELECTRICAL CHARACTERISTICS**

Tj=25℃ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V <sub>(BR)CEO</sub>	Collector-Emitter Breakdown Voltage	I <sub>C</sub> = -10mA; I <sub>B</sub> = 0	-30			V
V <sub>(BR)EBO</sub>	Emitter-Base Breakdown Voltage	I <sub>E</sub> = -1mA; I <sub>C</sub> = 0	-5			V
V <sub>(BR)CBO</sub>	Collector-Base Breakdown Voltage	I <sub>C</sub> = -1mA; I <sub>E</sub> = 0	-30			V
V <sub>CE(sat)</sub>	Collector-Emitter Saturation Voltage	I <sub>C</sub> = -3A; I <sub>B</sub> = -0.1A			-0.4	V
V <sub>BE</sub> (sat)	Base-Emitter Saturation Voltage	I <sub>C</sub> = -3A; I <sub>B</sub> = -0.1A			-1.0	V
Ісво	Collector Cutoff Current	V <sub>CB</sub> = -30V; I <sub>E</sub> = 0			-10	μА
I <sub>EBO</sub>	Emitter Cutoff Current	V <sub>EB</sub> = -5V; I <sub>C</sub> = 0			-10	μА
h <sub>FE</sub>	DC Current Gain	I <sub>C</sub> = -0.2A; V <sub>CE</sub> = -2V	100			
f⊤	Current-Gain—Bandwidth Product	I <sub>C</sub> = -0.5A; V <sub>CE</sub> = -10V	20			MHz

### NOTICE:

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