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Vishay Dale

AUTOMOTIVE

RoHS COMPLIANT

HALOGEN

FREE

GREEN

(5-2008)

Low-Profile, High-Current Coupled Inductor



DESIGN SUPPORT TOOLS click logo to get started





STANDARD ELECTRICAL SPECIFICATIONS					
	L ₀ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (μH)	DCR NOM. 25 °C (mΩ)	DCR MAX. 25 °C (mΩ)	CURRENT	SATURATION CURRENT DC TYP. (A) ⁽²⁾
L ₁₋₂	15	109.0	116.6	3.3	6.0
L ₃₋₄	15	111.0	118.6	3.5	6.0
L ₁₋₄ (L ₂₋₃ shorted)	61	220.0	235.0	2.3	2.7
L ₁₋₃ (L ₂₋₄ shorted)	1.0	220.0	235.0	2.3	See note (3)
L _{Common Mode} (1-3 and 2-4 shorted)	15	51.0	53.0	5.5	6.3
L _{Differential Mode} (1-4 and 2-3 shorted)	0.0	51.0	53.0	5.5	See note (3)

Notes

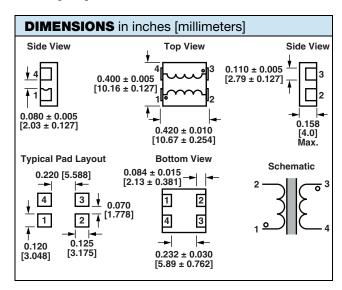
- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +155 °C
- The part temperature (ambient + temp. rise) should not exceed 155 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 50 V
- DC current (A) that will cause an approximate ΔT of 40 °C DC current (A) that will cause L₀ to drop approximately 20 %
- In this configuration, current flowing opposite directions through coils cancels and the 1.0 µH inductance is very stable with varying current. Observe the heat rating current to avoid excessive temperature rise in this configuration

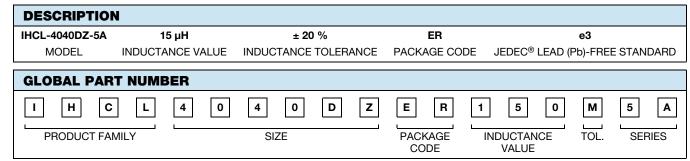
FEATURES

- High temperature, up to 155 °C
- Shielded construction
- Frequency range up to 5.0 MHz
- Lowest DCR/µH in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite
- construction
- Coupling is > 90 % optimized for SEPIC converters
- AEC-Q200 qualified
- PATENT(S): www.vishav.com/patents
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- SEPIC converters
- DC/DC converters
- Common mode applications
- LED lighting



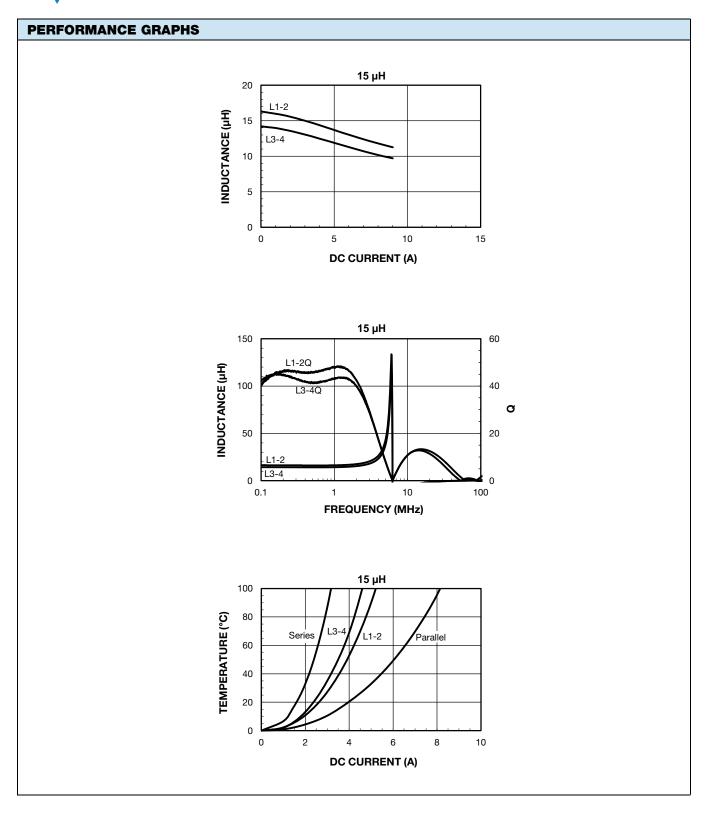


PATENT(S): www.vishay.com/patents

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