

J559-VB Datasheet P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R _{DS(on)} (Ω)	I _D (A)	Q _g (TYP.) (nC)			
	0.450 at V _{GS} = -10 V	-0.40				
-20	0.500 at V _{GS} = -4.5 V	-0.36	1			
	0.600 at V _{GS} = -2.5 V	-0.32				

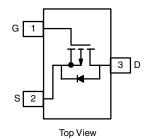
FEATURES

- Trench power MOSFET
- 100 % R tested
- Fast switching speed



APPLICATIONS

- Load / power switch for portable devices
- Drivers: relays, solenoids, displays
- Battery operated systems



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C, unless otherwise noted)					
PARAMETER	SYMBOL	LIMIT	UNIT		
Drain-Source Voltage		V _{DS}	-20		
Gate-Source Voltage		V _{GS}	± 20	V	
Continuous Dunis Comment /T 150 °C)	T _A = 25 °C		-0.40 b, c		
Continuous Drain Current (T _J = 150 °C)	T _A = 70 °C	I _D	-0.31 ^{b, c}		
Pulsed Drain Current (t = 300 μs)		I _{DM}	-1.6	A	
Continuous Source-Drain Diode Current	T _A = 25 °C	Is	-0.16 ^{b, c}	0.16 ^{b, c}	
Maximum Bower Dissination	T _A = 25 °C	В	0.19 ^{b, c}	w	
Maximum Power Dissipation	T _A = 70 °C	P _D	0.12 ^{b, c}		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT		
Maximum Junction-to-Ambient ^{a, b}	t ≤ 5 s	В	440	530	°C/W	
iviaximum sunction-to-ambient 4, 2	Steady State	R_{thJA}	540	650	C/VV	

Notes

- a. Maximum under steady state conditions is 650 $^{\circ}\text{C/W}.$
- b. Surface mounted on 1" x 1" FR4 board.
- $c. \ t=5 \ s.$

服务热线:400-655-8788

1



PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static	STWIDOL	TEST CONDITIONS	IVIIIV.	115.	WAX.	ONIT	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0, I _D = -250 μA	-20	_		l v	
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	1 d3 0, 1, 1 = 0 p. 1	-	-12	-	+ '	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	——In = -250 µA		1.8	_	mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = -250 \mu\text{A}$	- 0.6	-	-1.5	V	
auto course missione renage	* G3(III)	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	-	_	± 30	· ·	
Gate-Source Leakage	I_{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$	-	_	± 1		
		V _{DS} = -20 V, V _{GS} = 0 V	_	_	-1	μΑ	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -20 V, V _{GS} = 0 V, T _J = 85 °C	_	_	-10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = \ge 5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-1.5	_	-	Α	
	B(OH)	V _{GS} = -10 V, I _D = -0.4 A	_	0.450	-		
Drain-Source On-State Resistance a	R _{DS(on)}	V _{GS} = -4.5 V, I _D = -0.2 A	-	0.500	-	Ω	
	23(6.1,	V _{GS} = -2.5 V, I _D = -0.1 A	-	0.600	-		
Forward Transconductance	9 _{fs}	V _{DS} = -10 V, I _D = 0.4 A	-	1	-	S	
Dynamic ^b	0.2						
Input Capacitance	C _{iss}		-	45	-	pF	
Output Capacitance	Coss	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	15	-		
Reverse Transfer Capacitance	C _{rss}		-	10	-		
Total Cata Chausa	Qg	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -0.4 \text{ A}$	-	1.65	2.50)	
Total Gate Charge			-	1	2	-0	
Gate-Source Charge	Q _{gs}	$V_{DS} = -0 \text{ V}, V_{GS} = -2.5 \text{ V}, I_D = -0.4$	-	0.2	-	nC	
Gate-Drain Charge	Q _{gd}		-	0.26	-		
Gate Resistance	R_g	f = 1 MHz	2.4	12	24	Ω	
Turn-On Delay Time	t _{d(on)}		-	9	18		
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_L = 33.3 \Omega$	-	10	20		
Turn-Off DelayTime	t _{d(off)}	$I_D\cong$ -0.3 A, $V_{GEN}=$ -4.5 V, $R_g=$ 1 Ω	-	10	20		
Fall Time	t _f		-	8	16	1	
Turn-On Delay Time	t _{d(on)}		-	1	2	ns	
Rise Time	t _r	$V_{DD} = -10 \text{ V}, R_L = 33.3 \Omega$	-	8	16		
Turn-Off DelayTime	t _{d(off)}	$I_D\cong$ -0.3 A, V_{GEN} = -8 V, R_g = 1 Ω	-	9	18		
Fall Time	t _f		-	5	10		
Drain-Source Body Diode Characteris	tics						
Pulse Diode Forward Current ^a	I _{SM}		-	-	-1.5	Α	
Body Diode Voltage	V_{SD}	$I_{S} = -0.3 \text{ A}$	-	-0.8	-1.2	V	
Body Diode Reverse Recovery Time	t _{rr}		-	16	24	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = -0.3 A, dI/dt = 100 A/μs		8	16	nC	
Reverse Recovery Fall Time	ta	if = -0.3 A, αι/αι = 100 A/μs	-	11	-		
Reverse Recovery Rise Time	t _b		-	5	-	ns	

Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

1.5

1.2

0.9

0.6

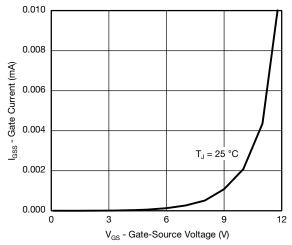
0.3

0

I_D - Drain Current (A)



TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Gate Current vs. Gate-Source Voltage

 $V_{GS} = 5 \text{ V thru } 3 \text{ V}$

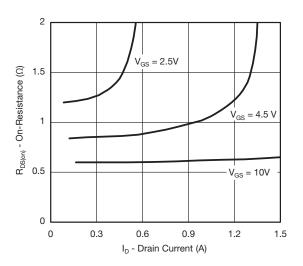


 $V_{GS} = 2.5 \, \underline{V}$

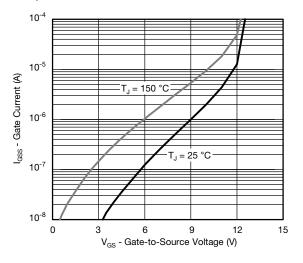
Output Characteristics

V_{GS} = 1.5 V

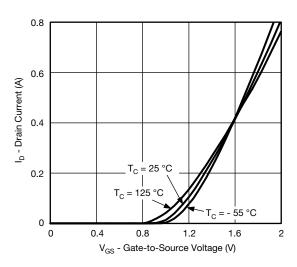
V_{DS} - Drain-to-Source Voltage (V)



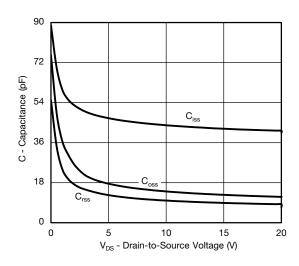
On-Resistance vs. Drain Current



Gate Current vs. Gate-Source Voltage



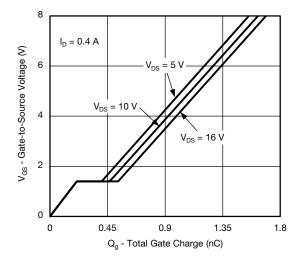
Transfer Characteristics



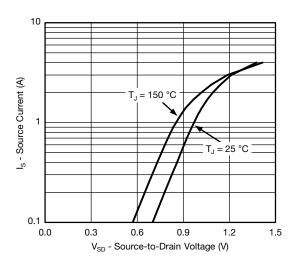
Capacitance



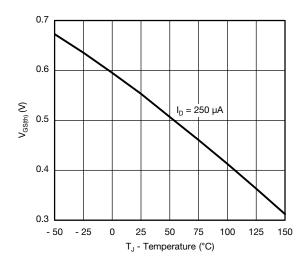
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



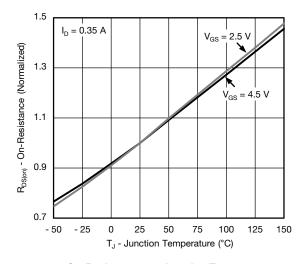
Gate Charge



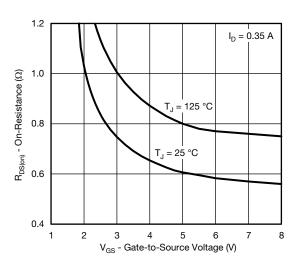
Source-Drain Diode Forward Voltage



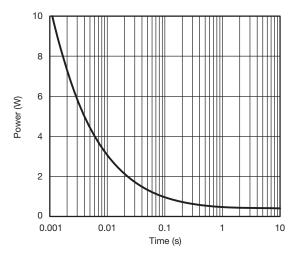
Threshold Voltage



On-Resistance vs. Junction Temperature



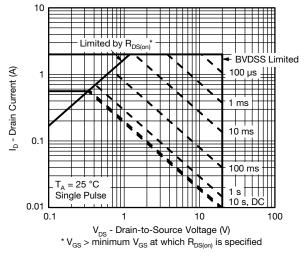
On-Resistance vs. Gate-to-Source Voltage

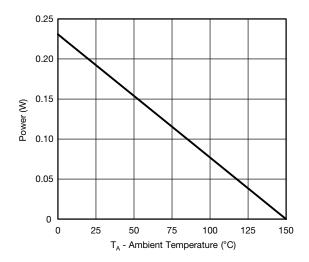


Single Pulse Power, Junction-to-Ambient



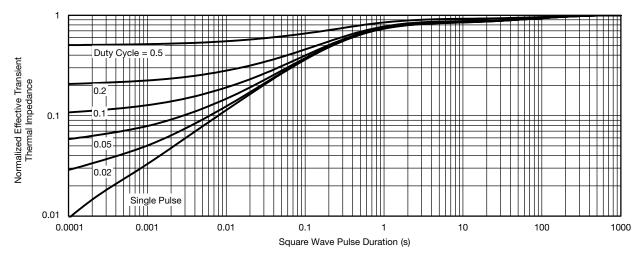
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





Safe Operating Area, Junction-to-Ambient

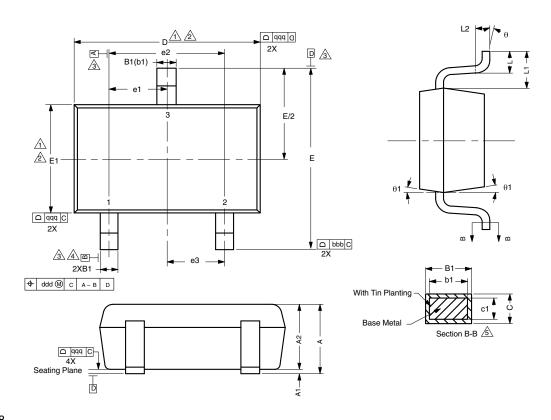
Power Derating, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient



SC-75A: 3 Leads



DWG: 5868

Notes

Dimensions in millimeters will govern.

- Dimension D does not include mold flash, protrusions or gate burrs. Mold flash protrusions or gate burrs shall not exceed 0.10 mm per end. Dimension E1 does not include Interlead flash or protrusion. Interlead flash or protrusion shall not exceed 0.10 mm per side.
- Dimensions D and E1 are determined at the outmost extremes of the plastic body exclusive of mold flash, tie bar burrs, gate burrs and interlead flash, but including any mismatch between the top and bottom of the plastic body.
- ADatums A, B and D to be determined 0.10 mm from the lead tip.
- A Terminal positions are shown for reference only.

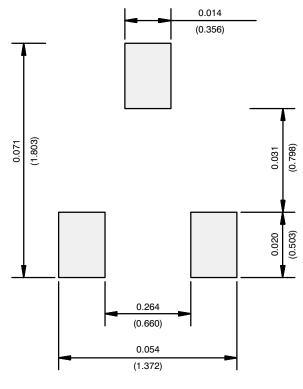
 These dimensions apply to the flat section of the lead between 0.08 mm and 0.15 mm from the lead tip.

DIMENSIONS	TOLERANCES		
aaa	0.10		
bbb	0.10		
ccc	0.10		
ddd	0.10		

5114	N	NOTE		
DIM.	MIN.	NOM.	MAX.	NOTE
Α	-	=	0.80	
A1	0.00	=	0.10	
A2	0.65	0.70	0.80	
B1	0.19	=	0.24	5
b1	0.17	=	0.21	
С	0.13	=	0.15	5
c1	0.10	-	0.12	5
D	1.48	1.575	1.68	1, 2
E	1.50	1.60	1.70	
E1	0.66	0.76	0.86	1, 2
e1	0.50 BSC			
e2	1.00 BSC			
e3	0.50 BSC			
L	0.15	0.205	0.30	
L1	0.40 ref.			
L2	0.15 BSC			
q	0°	-	8°	
q1	4°	-	10°	



RECOMMENDED MINIMUM PADS FOR SC-75A: 3-Lead



Recommended Minimum Pads Dimensions in Inches/(mm)



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