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VBA1101N Datasheet

N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY					
V _{DS} (V)	$R_{DS(on)}$ (Ω) Max.	I _D (A) ^a	Q _g (Typ.)		
	0.0090 at V _{GS} = 10 V	16			
100	0.0105 at $V_{GS} = 7.5 \text{ V}$	15.2	27.9 nC		
	0.0110 at V _{GS} = 6.0 V	14			

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SO-8

Top View

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N-Channel MOSFET

FEATURES

- Trench Power MOSFET
- 100 % R_g and UIS Tested
- Material categorization:

APPLICATIONS

- DC/DC Primary Side Switch
- · Telecom/Server
- Motor Drive Control
- · Synchronous Rectification



RoHS

Parameter		Symbol	Limit	Unit
Drain-Source Voltage		V _{DS}	100	V
Gate-Source Voltage		V _{GS}	± 20	v
	T _C = 25 °C		16	
Outlines Prois Outline (T., 450.00)	T _C = 70 °C	1 .	13	
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	10.2 ^{b, c}	
	T _A = 70 °C		7.4 ^{b, c}	•
Pulsed Drain Current (t = 300 μs)		I _{DM}	70	Α
Continuous Course Dunin Diada Current	T _C = 25 °C		7	
Continuous Source-Drain Diode Current	T _A = 25 °C	- I _S	3.1 ^{b, c}	
Single Pulse Avalanche Current	L = 0.1 mH	I _{AS}	30	
Avalanche Energy		E _{AS}	45	mJ
	T _C = 25 °C		7.8	
Mariana Paran Biasinatian	T _C = 70 °C		5	w
Maximum Power Dissipation	T _A = 25 °C	P _D	3.5 ^{b, c}	VV
	T _A = 70 °C		2.2 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R _{thJA}	29	35	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	13	16	0/11		

Notes:

- a. Based on $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 10 s
- d. Maximum under steady state conditions is 80 °C/W.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	-		,		·		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V, I}_{D} = 250 \mu\text{A}$	100			V	
V _{DS} Temperature Coefficient	ΔV _{DS} /T _J	1 050 4		67		140	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	- I _D = 250 μA		- 6.4		mV/°C	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_{D} = 250 \mu A$	1.0		3.0	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
7 0	I _{DSS}	V _{DS} = 100 V, V _{GS} = 0 V			1	μΑ	
Zero Gate Voltage Drain Current		V _{DS} = 100 V, V _{GS} = 0 V, T _J = 55 °C			10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
		V _{GS} = 10 V, I _D = 15 A		0.0090			
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 7.5 V, I _D = 12 A		0.0105		Ω	
		$V_{GS} = 6.0 \text{ V}, I_D = 10 \text{ A}$		0.0110			
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A		54		S	
Dynamic ^b			I	<u> </u>	<u>I</u>		
Input Capacitance	C _{iss}			3410			
Output Capacitance	C _{oss}	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		790		pF	
Reverse Transfer Capacitance	C _{rss}	1		160			
T		V _{DS} = 50 V, V _{GS} = 10 V, I _D = 10 A		45.6	69		
Total Gate Charge	Q _g		27.9	42	-		
Gate-Source Charge	Q _{gs}	$V_{DS} = 50 \text{ V}, V_{GS} = 6 \text{ V}, I_{D} = 10 \text{ A}$		8.5		nC	
Gate-Drain Charge	Q _{gd}	1		9.2			
Output Charge	Q _{oss}	V _{DS} = 50 V, V _{GS} = 0 V		63	95		
Gate Resistance	R _g	f = 1 MHz	0.4	1.3	2.6	Ω	
Turn-On Delay Time	t _{d(on)}			16	32		
Rise Time	t _r	$V_{DD} = 50 \text{ V}, R_L = 5 \Omega$		11	22		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 7.5 \text{ V}, R_g = 1 \Omega$		35	70	1	
Fall Time	t _f			10	20		
Turn-On Delay Time	t _{d(on)}			14	28	ns	
Rise Time	t _r	$V_{DD} = 50 \text{ V}, R_L = 5 \Omega$		10	20		
Turn-Off Delay Time	t _{d(off)}	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		36	70		
Fall Time	t _f	1		10	20		
Drain-Source Body Diode Characteristic	cs						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			7		
Pulse Diode Forward Current ^a	I _{SM}				70	A	
Body Diode Voltage	V _{SD}	I _S = 5 A		0.75	1.1	٧	
Body Diode Reverse Recovery Time	t _{rr}			49	95	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	1 10 A di/dt 100 A/m T 05 00		58	115	nC	
Reverse Recovery Fall Time	t _a	t_a $t_b = 100 \text{ A}, \text{ di/dt} = 1000 \text{ A/} \mu \text{S}, t_J = 25 \text{ C}$		21		ns	
Reverse Recovery Rise Time	t _b			28			

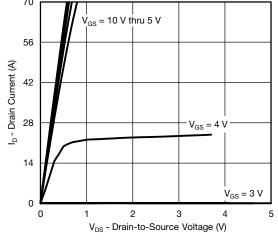
Notes:

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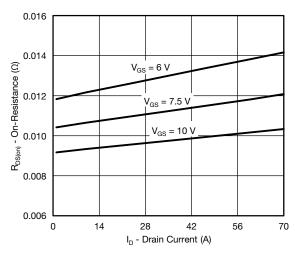
- a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 % b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

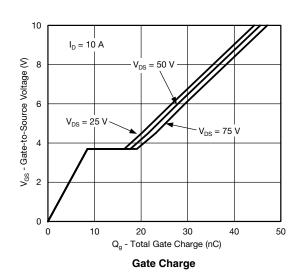


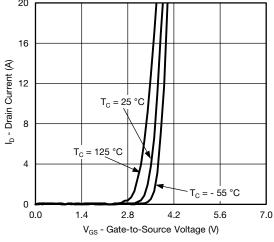


Output Characteristics

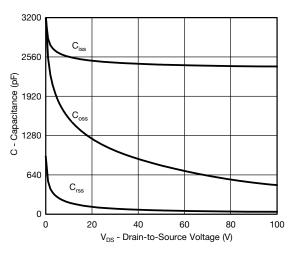


On-Resistance vs. Drain Current

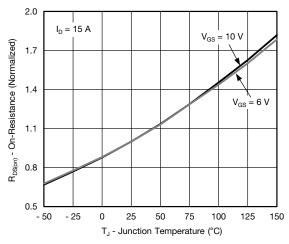




Transfer Characteristics

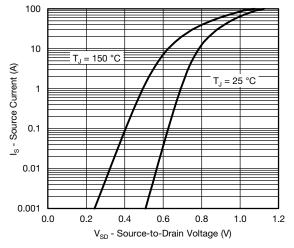


Capacitance

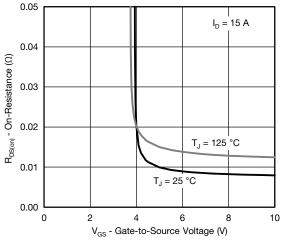


On-Resistance vs. Junction Temperature

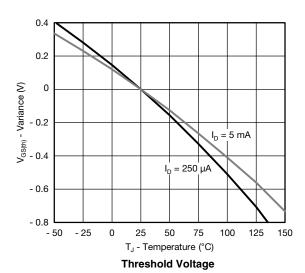




Source-Drain Diode Forward Voltage



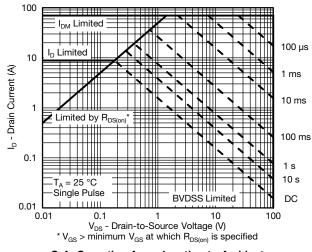
On-Resistance vs. Gate-to-Source Voltage



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160 (8) 120 40 0 0.001 0.01 0.1 1 10 Time (s)

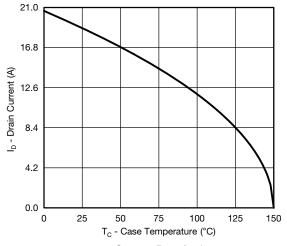
Single Pulse Power, Junction-to-Ambient



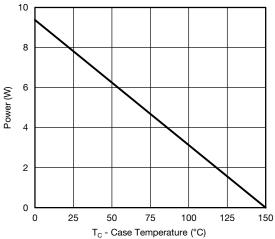
200

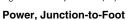
Safe Operating Area, Junction-to-Ambient

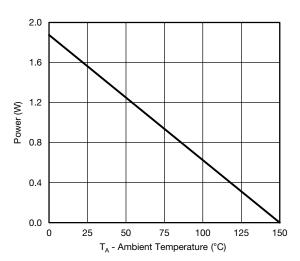




Current Derating*



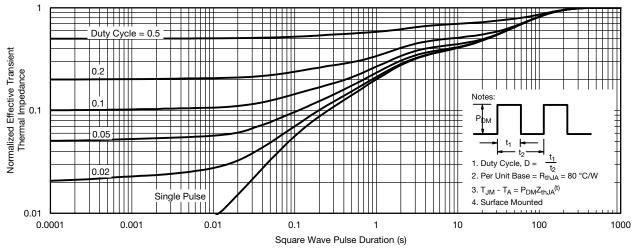




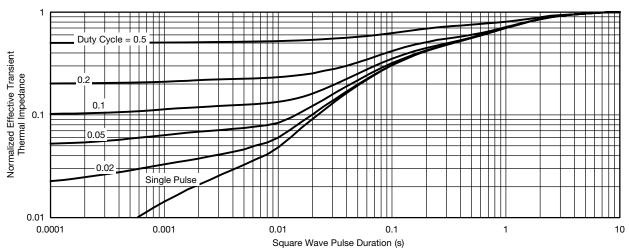
Power, Junction-to-Ambient

^{*} The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.





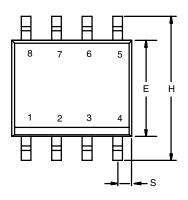
Normalized Thermal Transient Impedance, Junction-to-Ambient

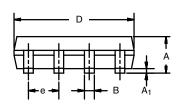


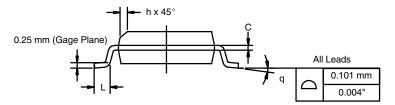
Normalized Thermal Transient Impedance, Junction-to-Foot



SOIC (NARROW): 8-LEADJEDEC Part Number: MS-012







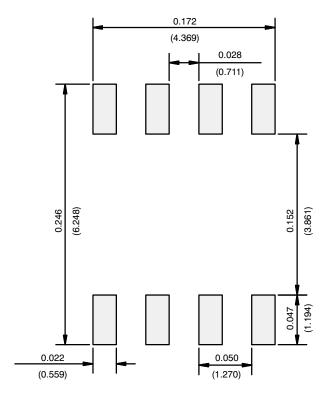
	MILLIM	IETERS	INC	INCHES		
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A ₁	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
E	3.80	4.00	0.150	0.157		
е	1.27 BSC		0.050	0.050 BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
FCN: C-06527-Rev 11-Sep-06						

ECN: C-06527-Rev. I, 11-Sep-06

DWG: 5498



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)



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