

#### Discription

The SD15C-01FTG protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD. It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.

# Features

- ★ Small Body Outline Dimensions
- ★ Low Body Height
- ★ Peak Power up to 200 Watts @ 8 x 20 \_s Pulse
- ★ Low Leakage current
- ★ Response Time is Typically < 1 ns</p>
- ★ ESD Rating of Class 3 (> 16 kV) per Human Body Model
- ★ IEC61000-4-2 Level 4 ESD Protection
- ★ IEC61000-4-4 Level 4 EFT Protection

### **Ordering information**





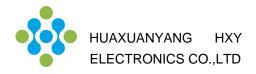


Circuit Diagram

Product ID	Pack	Qty(PCS)		
SD15C-01FTG	SOD-323	3000		

### Absolute Ratings (T<sub>amb</sub>=25°C)

Symbol	Parameter	Value	Units
P <sub>PP</sub>	Peak Pulse Power ( $t_p = 8/20 \ \mu \ s$ )	200	W
TL	Maximum lead temperature for soldering during 10s	260	°C
T <sub>stg</sub>	Storage Temperature Range	-55 to +155	°C
T <sub>op</sub>	Operating Temperature Range	-40 to +125	°C
Tj	Maximum junction temperature	150	°C
	IEC61000-4-2 (ESD) air discharge	±30	КV
	contact discharge	±30	



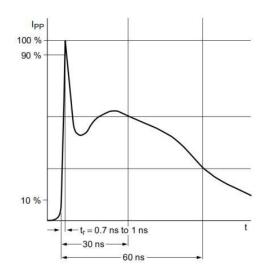
V <sub>RWM</sub> (V)	I <sub>R</sub> (uA) @V <sub>RWM</sub>	V <sub>BR</sub> (V)@ I <sub>T</sub> (Note 1)	Ι <sub>Τ</sub>	V <sub>C</sub> (V) @ I <sub>PP</sub> =1 A*	V <sub>c</sub> (V) @ Max I <sub>PP</sub> *	І <sub>РР</sub> (А)*	Р <sub>РК</sub> (W)*	С (рF)
Мах	Мах	Min	mA	Тур	Max	Мах	Max	Мах
15	1.0	17	1	24	33	8	260	35

Electrical Characteristics Ratings at 25°C ambient temperature unless otherwise specified.VF = 0.9V at IF = 10mA

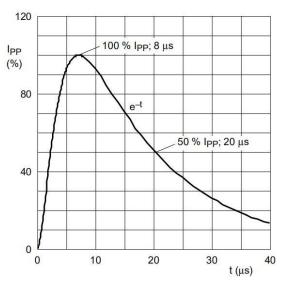
\*Surge current waveform per Figure 1.

1.  $V_{BR}$  is measured with a pluse test current  $I_T$  at an ambient temperature of  $25\,^\circ\! {\rm C}$  .

# **Typical Characteristics**



IEC61000-4-2 Waveform



IEC 61000-4-5 Waveform( 8/20µs pulse)



# **Outline And Dimensions**

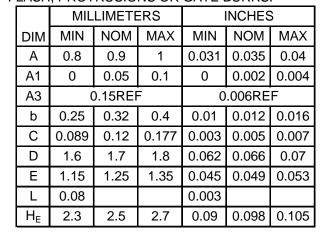
Notes:

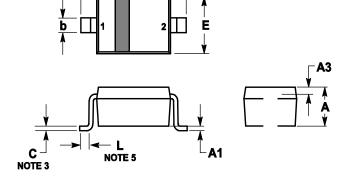
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

2. CONTROLLING DIMENSION: MILLIMETERS.

3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

4. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

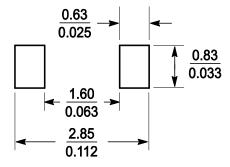




HE

D

**Soldering Footprint** 





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