
600 mA PWM/VFM Step-Down DC/DC Converter with Synchronous Rectifier

NO.EA-259-170620

OUTLINE

The RP504x is a low supply current CMOS-based PWM/VFM step-down DC/DC converter with synchronous rectifier featuring 600 mA^{*1} output current. Internally, a single converter consists of an oscillator, a reference voltage unit, an error amplifier, a switching control circuit, a mode control circuit (RP504xxx1A/D), a soft-start circuit, a Latch-type protection circuit, an under voltage lockout (UVLO) circuit and switching transistors.

The RP504x is employing synchronous rectification for improving the efficiency of rectification by replacing diodes with built-in switching transistors. Using synchronous rectification not only increases circuit performance but also allows a design to reduce parts count.

Power controlling method can be selected from forced PWM control type or PWM/VFM auto switching control type by inputting a signal to the MODE pin. In low output current, forced PWM control switches at fixed frequency rate in order to reduce noise. Likewise, in low output current, PWM/VFM auto switching control automatically switches from PWM mode to VFM mode in order to achieve high efficiency.

Output voltage is internally fixed type which allows output voltages that range from 0.8 V to 3.3 V in 0.1 V step. The output voltage accuracy is as high as $\pm 1.5\%$ or ± 18 mV.

Protection circuits included in the RP504x are overcurrent protection circuit and latch type protection circuit. Overcurrent protection circuit supervises the inductor peak current in each switching cycle, and if the current exceeds the I_{LX} current limit (I_{LXLIM}), it turns off P-channel Tr. Latch type protection circuit latches the built-in driver to the OFF state and stops the operation of the step-down DC/DC converter if the overcurrent status continues or V_{OUT} continues being the half of the setting voltage for equal or longer than protection delay time (t_{prot}). To cancel the latch type protection circuit, select the standby mode or the active mode with the CE pin, or drop the power supply voltage below the UVLO detector threshold.

The RP504x is offered in 6-pin DFN(PLP)1216-6F, 6-pin DFN1616-6B and 5-pin SOT-23-5 packages which achieve the smallest possible footprint solution on boards where area is limited.

^{*1} This is an approximate value. The output current is dependent on conditions and external components.

FEATURES

- Supply Current Typ. 25 μ A in VFM mode without any load
- Standby Current..... Max. 5 μ A
- Input Voltage Range 2.3 V to 5.5 V ($V_{OUT} \geq 1.0$ V)
- Output Voltage Range..... 0.8 V to 3.3 V in 0.1 V step
- Output Voltage Accuracy..... $\pm 1.5\%$ ($V_{OUT} \geq 1.2$ V), ± 18 mV ($V_{OUT} < 1.2$ V)
- Temperature-Drift Coefficient of Output Voltage ... Typ. ± 40 ppm/ $^{\circ}$ C
- Oscillator Frequency Typ. 2.25 MHz
- Oscillator Maximum Duty Cycle..... Min. 100%
- Built-in Driver ON Resistance Typ. Pch. 0.34 Ω , Nch. 0.43 Ω ($V_{IN} = 3.6$ V)
- UVLO Detector Threshold..... Typ. 2.0 V
- Soft Start Time Typ. 0.15 ms
- L_x Current Limit..... Typ. 900 mA
- Latch-type Protection Circuit..... Typ. 1.5 ms
- Auto-discharge Function Only for RP504xxxxD
- Power Controlling Method..... forced PWM control or PWM/VFM auto switching control
- MODE Pin^{*1} “H”: forced PWM control,
“L”: PWM/VFM auto switching control
- Package^{*1} DFN1616-6B, DFN(PLP)1216-6F, SOT-23-5

^{*1} DFN(PLP)1216-6F, DFN1616-6B: forced PWM control by pulling MODE pin “H” or PWM/VFM auto switching control by pulling MODE pin “L”

SOT-23-5: forced PWM control for RP504xxxxC and PWM/VFM auto switching control for RP504xxxxB

APPLICATIONS

- Power source for battery-powered equipment.
- Power source for hand-held communication equipment, cameras, VCRs, camcorders.
- Power source for HDD, portable equipment.

SELECTION GUIDE

The set output voltage, the package type, the MODE control pin function and the auto-discharge^{*1} function are user-selectable options.

Product Name	Package	Quantity per Reel	Pb Free	Halogen Free
RP504Kxx1\$-E2	DFN(PLP)1216-6F	5,000 pcs	Yes	Yes
RP504Lxx1\$-TR	DFN1616-6B	5,000 pcs	Yes	Yes
RP504Nxx1\$-TR-FE	SOT-23-5	3,000 pcs	Yes	Yes

xx: Specify the set output voltage (V_{SET}) within the range of 0.8 V(08) to 3.3 V(33) in 0.1 V steps.
Refer to the section of *PACKAGE INFORMATION* for detailed information.

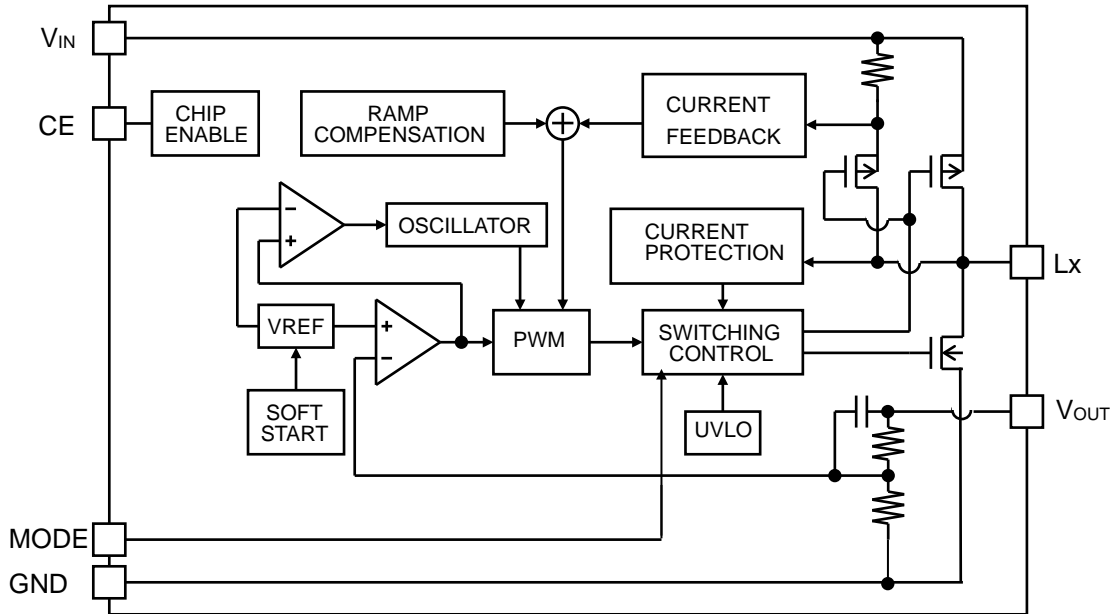
\$. Specify the package type, the MODE control pin function and the auto-discharge function.

\$	Package	MODE Control Pin Function		Auto-discharge Function
		MODE Pin	Power Controlling Method	
A	DFN1616-6B	Yes	"H": forced PWM "L": PWM/VFM auto switching control	No
	DFN(PLP)1216-6F			
B	SOT-23-5	No	PWM/VFM auto switching control	No
C	SOT-23-5	No	forced PWM control	No
D	DFN1616-6B	Yes	"H": forced PWM control "L": PWM/VFM auto switching control	Yes
	DFN(PLP)1216-6F			

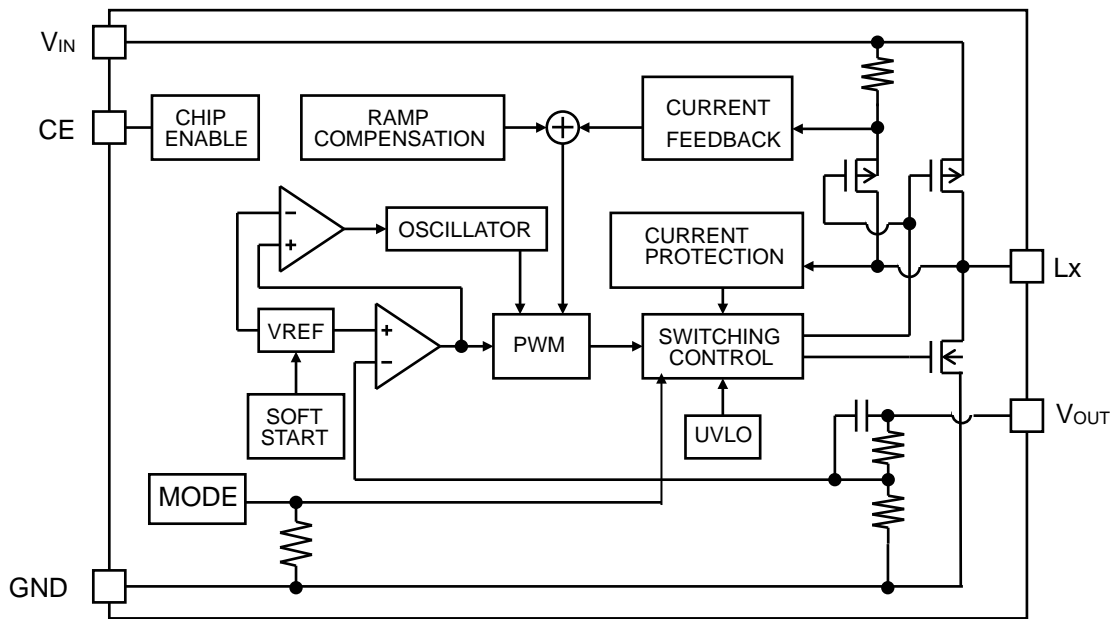
^{*1} Auto-discharge function quickly lowers the output voltage to 0 V, when the chip enable signal is switched from the active mode to the standby mode, by releasing the electrical charge accumulated in the external capacitor.

^{*2} 0.05 V step is also available as a custom code.

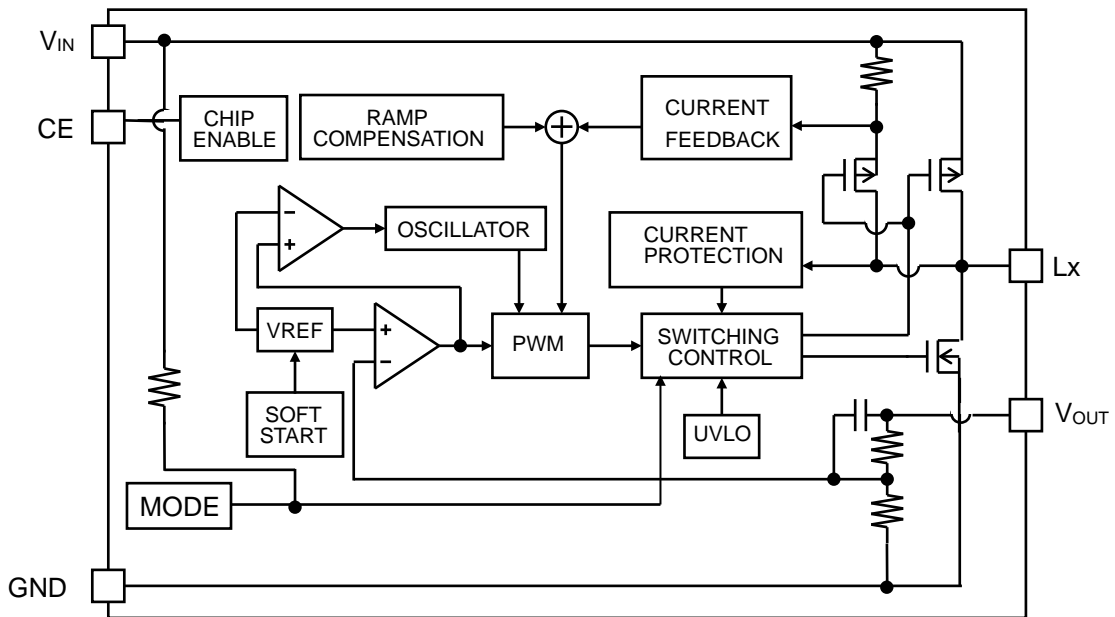
BLOCK DIAGRAMS



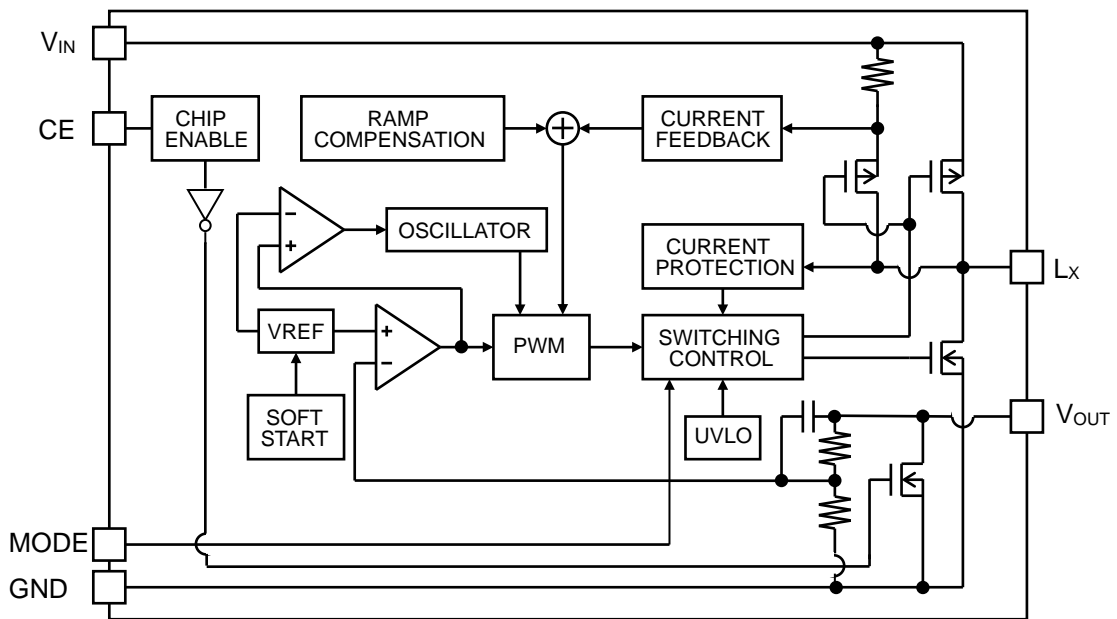
RP504xxxxA Block Diagram



RP504xxxxB Block Diagram

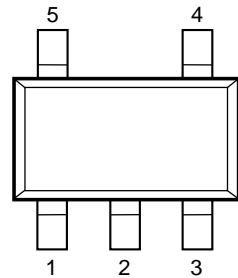
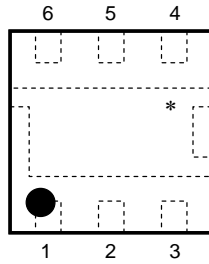
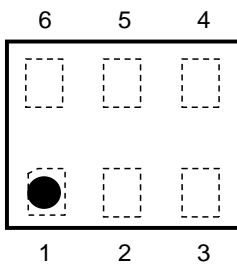


RP504xxxxC Block Diagram



RP504xxxxD Block Diagram

PIN DESCRIPTION



DFN(PLP)1216-6F Pin Configurations DFN1616-6B Pin Configurations SOT-23-5 Pin Configurations

RP504Kxx1A, RP504Kxx1D: DFN(PLP)1216-6F Pin Description

Pin No.	Symbol	Description
1	V _{IN}	Input Pin
2	MODE	Mode Control Pin ("H": forced PWM control, "L": PWM/VFM auto switching control)
3	CE	Chip Enable Pin (Active-high)
4	V _{OUT}	Output Pin
5	GND	Ground Pin
6	L _x	L _x Switching Pin

RP504Lxx1A, RP504Lxx1D: DFN1616-6B Pin Description

Pin No.	Symbol	Description
1	CE	Chip Enable Pin (Active-high)
2	MODE	Mode Control Pin ("H": forced PWM control, "L": PWM/VFM auto switching control)
3	V _{IN}	Input Pin
4	L _x	L _x Switching Pin
5	GND	Ground Pin
6	V _{OUT}	Output Pin

* The tab on the bottom of the package enhances thermal performance and is electrically connected to GND (substrate level). It is recommended that the tab be connected to the ground plane on the board. If not, the tab can be left open.

RP504Nxx1B, RP504Nxx1C: SOT-23-5 Pin Description

Pin No.	Symbol	Description
1	V _{OUT}	Output Pin
2	GND	Ground Pin
3	L _x	L _x Switching Pin
4	V _{IN}	Input Pin
5	CE	Chip Enable Pin (Active-high)

ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings

(GND = 0 V)

Symbol	Item	Rating	Unit
V _{IN}	V _{IN} Input Voltage	-0.3 to 6.5	V
V _{LX}	L _X Pin Voltage	-0.3 to V _{IN} +0.3	V
V _{CE}	CE Pin Input Voltage	-0.3 to 6.5	V
V _{MODE}	Mode Control Pin Voltage	-0.3 to 6.5	V
V _{OUT}	V _{OUT} Pin Voltage	-0.3 to 6.5	V
I _{LX}	L _X Pin Output Current	900	mA
P _D	Power Dissipation (Standard Land Pattern)* ¹	DFN(PLP)1216-6F	385
		DFN1616-6B	640
		SOT-23-5	420
T _j	Junction Temperature Range	-40 to 125	°C
T _{stg}	Storage Temperature Range	-55 to 125	°C

*¹ Refer to *POWER DISSIPATION* for detailed information.

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause the permanent damages and may degrade the life time and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

RECOMMENDED OPERATING CONDITIONS

Recommended Operating Conditions

Symbol	Item	Rating	Unit
V _{IN}	Operating Input Voltage	2.3 to 5.5 (V _{OUT} ≥ 1.0)	V
		2.3 to 4.5 (V _{OUT} < 1.0)	V
T _a	Operating Temperature Range	-40 to 85	°C

RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if when they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

RP504x

NO.EA-259-170620

ELECTRICAL CHARACTERISTICS**RP504xxx1A, RP504xxx1D Electrical Characteristics**

(Ta = 25°C)

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit		
V _{OUT}	Output Voltage	V _{IN} = V _{CE} = 3.6 V or V _{SET} +1 V	V _{OUT} ≥ 1.2 V	x0.985	x1.015	V		
			V _{OUT} < 1.2 V	-0.018	+0.018			
ΔV _{OUT} /ΔTa	Output Voltage Temperature Coefficient	-40°C ≤ Ta ≤ 85°C			±40	ppm/°C		
fosc	Oscillator Frequency	V _{IN} = V _{CE} = 3.6 V or V _{SET} +1 V			1.95	2.25	2.55	MHz
I _{DD1}	Supply Current 1	V _{IN} = V _{CE} = 5.5 V, V _{OUT} = V _{SET} × 0.8			400	800	μA	
I _{DD2}	Supply Current 2	V _{IN} = V _{CE} = V _{OUT} = 5.5 V	V _{MODE} = 0 V	25	40	μA		
			V _{MODE} = 5.5 V	400	800			
I _{standby}	Standby Current	V _{IN} = 5.5 V, V _{CE} = 0 V			0	5	μA	
I _{CEH}	CE "H" Input Voltage	V _{IN} = V _{CE} = 5.5 V			-1	0	1	μA
I _{CEL}	CE "L" Input Voltage	V _{IN} = 5.5 V, V _{CE} = 0 V			-1	0	1	μA
I _{MODEH}	Mode "H" Input Current	V _{IN} = V _{MODE} = 5.5 V			-1	0	1	μA
I _{MODEL}	Mode "L" Input Current	V _{IN} = 5.5 V, V _{MODE} = 0 V			-1	0	1	μA
I _{VOUTH}	V _{OUT} "H" Input Current ^{*1}	V _{IN} = V _{OUT} = 5.5 V, V _{CE} = 0 V			-1	0	1	μA
I _{VOUTL}	V _{OUT} "L" Input Current	V _{IN} = 5.5 V, V _{CE} = V _{OUT} = 0 V			-1	0	1	μA
I _{LXLEAKH}	Lx Leakage Current "H"	V _{IN} = V _{LX} = 5.5 V, V _{CE} = 0 V			-1	0	5	μA
I _{LXLEAKL}	Lx Leakage Current "L"	V _{IN} = 5.5 V, V _{CE} = V _{LX} = 0 V			-5	0	1	μA
V _{CEH}	CE "H" Input Voltage	V _{IN} = 5.5 V			1.0			V
V _{CEL}	CE "L" Input Voltage	V _{IN} = 2.3 V					0.4	V
V _{MODEH}	Mode "H" Input Voltage	V _{IN} = 5.5 V			1.0			V
V _{MODEL}	Mode "L" Input Voltage	V _{IN} = 2.3 V					0.4	V
R _{LOW}	Nch On Resistance ^{*2}	V _{IN} = 3.6 V, V _{CE} = 0 V				30		Ω
R _{ONP}	On Resistance of Pch Tr.	V _{IN} = 3.6 V, I _{LX} = -100 mA				0.34		Ω
R _{ONN}	On Resistance of Nch Tr.	V _{IN} = 3.6 V, I _{LX} = -100 mA				0.43		Ω
Maxduty	Oscillator Maximum Duty Cycle				100			%
tstart	Soft-start Time	V _{IN} = V _{CE} = 3.6 V or V _{SET} +1 V				150	310	μs
I _{LXLIM}	Lx Current Limit	V _{IN} = V _{CE} = 3.6 V or V _{SET} +1 V			700	900		mA
tprot	Protection Delay Time	V _{IN} = V _{CE} = 3.6 V or V _{SET} +1 V			0.5	1.5	5	ms
V _{UVLO1}	UVLO Detector Threshold	V _{IN} = V _{CE}			1.9	2.0	2.1	V
V _{UVLO2}	UVLO Released Voltage	V _{IN} = V _{CE}			2.0	2.1	2.2	V

All test items listed under *ELECTRICAL CHARACTERISTICS* are done under the pulse load condition (T_J ≈ Ta = 25°C) except Output Voltage Temperature Coefficient.

Test circuit is "OPEN LOOP" and AGND = PGND = 0 V unless otherwise specified.

*1 Only for RP504xxx1A/B/C with no auto-discharge

*2 Only for RP504xxx1D with auto-discharge

RP504xxxxB, RP504xxxxC Electrical Characteristics

(Ta = 25°C)

Symbol	Item	Conditions	Min.	Typ.	Max.	Unit
V _{OUT}	Output Voltage	V _{IN} = V _{CE} = 3.6 V or V _{SET} +1 V	V _{OUT} ≥ 1.2 V	x0.985	x1.015	V
			V _{OUT} < 1.2 V	-0.018	+0.018	
ΔV _{OUT} /ΔTa	Output Voltage Temperature Coefficient	-40°C ≤ Ta ≤ 85°C		±40		ppm/°C
fosc	Oscillator Frequency	V _{IN} = V _{CE} = 3.6 V or V _{SET} +1 V	1.95	2.25	2.55	MHz
I _{DD1}	Supply Current 1	V _{IN} = V _{CE} = 5.5 V, V _{OUT} = V _{SET} × 0.8		400	800	μA
I _{DD2}	Supply Current 2	V _{IN} = V _{CE} = V _{OUT} = 5.5 V	RP504xxx1B	25	40	μA
			RP504xxx1C	400	800	
I _{standby}	Standby Current	V _{IN} = 5.5 V, V _{CE} = 0 V		0	5	μA
I _{CEH}	CE "H" Input Voltage	V _{IN} = V _{CE} = 5.5 V	-1	0	1	μA
I _{CEL}	CE "L" Input Voltage	V _{IN} = 5.5 V, V _{CE} = 0 V	-1	0	1	μA
I _{VOUTH}	V _{OUT} "H" Input Current	V _{IN} = V _{OUT} = 5.5 V, V _{CE} = 0 V	-1	0	1	μA
I _{VOU TL}	V _{OUT} "L" Input Current	V _{IN} = 5.5 V, V _{CE} = V _{OUT} = 0 V	-1	0	1	μA
I _{LXLEAKH}	Lx Leakage Current "H"	V _{IN} = V _{LX} = 5.5 V, V _{CE} = 0 V	-1	0	5	μA
I _{LXLEAKL}	Lx Leakage Current "L"	V _{IN} = 5.5 V, V _{CE} = V _{LX} = 0 V	-5	0	1	μA
V _{CEH}	CE "H" Input Voltage	V _{IN} = 5.5 V	1.0			V
V _{CEL}	CE "L" Input Voltage	V _{IN} = 2.3 V			0.4	V
R _{ONP}	On Resistance of Pch Tr.	V _{IN} = 3.6 V, I _{LX} = -100 mA		0.34		Ω
R _{ONN}	On Resistance of Nch Tr.	V _{IN} = 3.6 V, I _{LX} = -100 mA		0.43		Ω
Maxduty	Oscillator Maximum Duty Cycle		100			%
t _{start}	Soft-start Time	V _{IN} = V _{CE} = 3.6 V or V _{SET} +1 V		150	310	μs
I _{LXLIM}	Lx Current Limit	V _{IN} = V _{CE} = 3.6 V or V _{SET} +1 V	700	900		mA
t _{prot}	Protection Delay Time	V _{IN} = V _{CE} = 3.6 V or V _{SET} +1 V	0.5	1.5	5	ms
V _{UVLO1}	UVLO Detector Threshold	V _{IN} = V _{CE}	1.9	2.0	2.1	V
V _{UVLO2}	UVLO Released Voltage	V _{IN} = V _{CE}	2.0	2.1	2.2	V

All test items listed under *ELECTRICAL CHARACTERISTICS* are done under the pulse load condition (T_j ≈ Ta = 25°C) except Output Voltage Temperature Coefficient.

Test circuit is "OPEN LOOP" and AGND = PGND = 0 V unless otherwise specified.

OPERATING DESCRIPTIONS

OPERATION OF STEP-DOWN CONVERTER AND OUTPUT CURENT

The step-down DC/DC converter charges energy in the inductor when L_x Tr. turns "ON", and discharges the energy from the inductor when L_x Tr. turns "OFF" and operates with less energy loss, so that a lower output voltage (V_{OUT}) than the input voltage (V_{IN}) can be obtained. The operation of the step-down DC/DC converter is explained in the following figures.

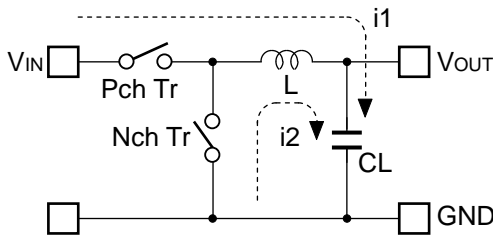


Figure 1. Basic Circuit

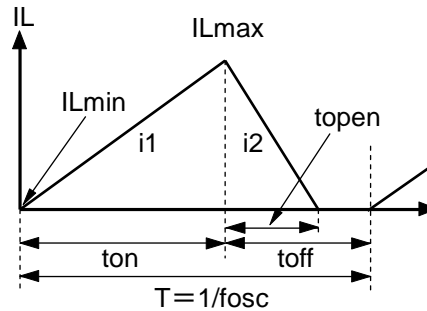


Figure 2. Inductor Current (IL) flowing through Inductor

- Step1.** P-channel Tr. turns "ON" and IL (i_1) flows, L is charged with energy. At this moment, i_1 increases from the minimum inductor current (IL_{min}), which is 0 A, and reaches the maximum inductor current (IL_{max}) in proportion to the on-time period (ton) of P-channel Tr.
- Step2.** When P-channel Tr. turns "OFF", L tries to maintain IL at IL_{max} , so L turns N-channel Tr. "ON" and IL (i_2) flows into L.
- Step3.** i_2 decreases gradually and reaches IL_{min} after the open-time period ($topen$) of N-channel Tr., and then N-channel Tr. turns "OFF". This is called discontinuous current mode.
As the output current (I_{OUT}) increases, the off-time period ($toff$) of P-channel Tr. runs out before IL reaches IL_{min} . The next cycle starts, and P-channel Tr. turns "ON" and N-channel Tr. turns "OFF", which means IL starts increasing from IL_{min} . This is called continuous current mode.

In the case of PWM mode, V_{OUT} is maintained by controlling ton . During the PWM mode, the oscillator frequency ($fosc$) is constantly maintained.

As shown in Figure 2, when the step-down DC/DC operation is constant, IL_{min} and IL_{max} during ton of P-channel Tr. would be the same as IL_{min} and IL_{max} during $toff$ of the P-channel Tr.

The current differential between IL_{max} and IL_{min} is described as ΔI .

$$\Delta I = IL_{max} - IL_{min} = V_{OUT} \times topen / L = (V_{IN} - V_{OUT}) \times ton / L \dots \dots \dots \text{Equation 1}$$

However,

$$T = 1 / fosc = ton + toff$$

$$\text{Duty (\%)} = ton / T \times 100 = ton \times fosc \times 100$$

$$topen \leq toff$$

In Equation 1, " $V_{OUT} \times topen / L$ " shows the amount of current change in "OFF" state. Also, " $(V_{IN} - V_{OUT}) \times ton / L$ " shows the amount of current change at "ON" state.

DISCONTINUOUS MODE AND CONTINUOUS MODE

As illustrated in Figure 3, when I_{OUT} is relatively small, $t_{open} < t_{off}$. In this case, the energy charged into L during t_{on} will be completely discharged during t_{off} , as a result, $I_{LMIN} = 0$. This is called discontinuous mode.

When I_{OUT} is gradually increased, eventually $t_{open} = t_{off}$ and when I_{OUT} is increased further, eventually $I_{LMIN} > 0$. This is called continuous mode.

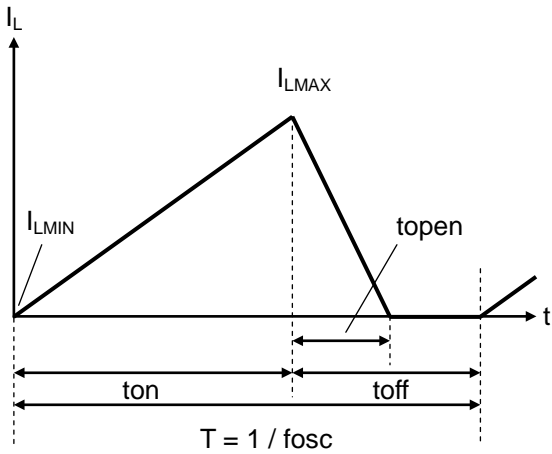


Figure 3. Discontinuous Mode

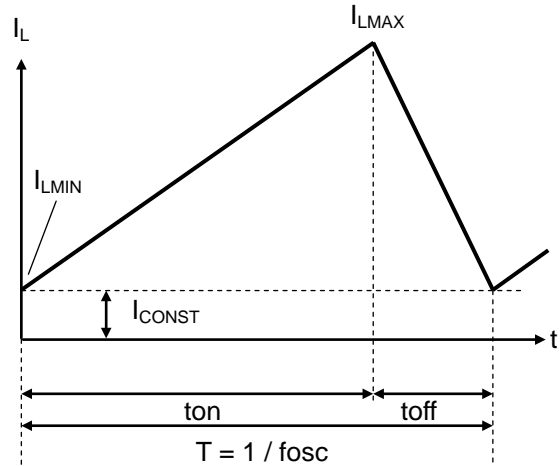


Figure 4. Continuous Mode

In the continuous mode, the solution of Equation 1 is described as t_{onc} .

$$t_{onc} = T \times V_{OUT} / V_{IN} \dots\dots\dots \text{Equation 2}$$

When $t_{on} < t_{onc}$, it indicates discontinuous mode, and when $t_{on} = t_{onc}$, it indicates continuous mode.

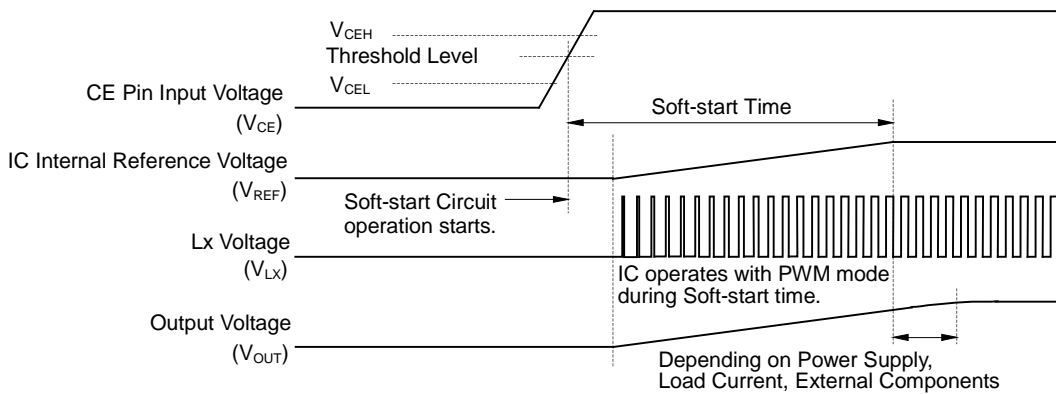
TIMING CHART

1. Soft-start Time

Starting-up with CE Pin

The IC starts to operate when the CE pin voltage (V_{CE}) exceeds the threshold voltage. The threshold voltage is preset between CE "H" input voltage (V_{CEH}) and CE "L" input voltage (V_{CEL}).

After the start-up of the IC, soft-start circuit starts to operate. Then, after a certain period of time, the reference voltage (V_{REF}) in the IC gradually increases up to the specified value.

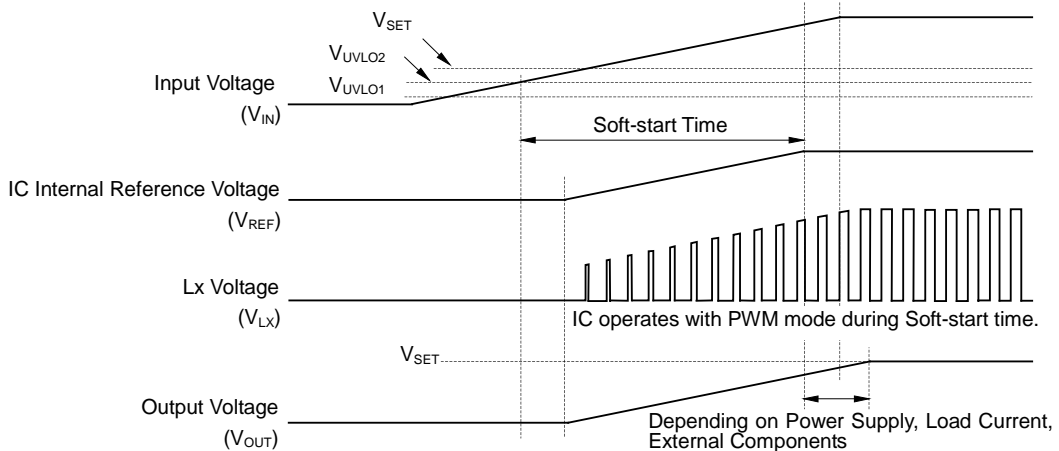


Soft-start time starts when soft-start circuit is activated, and ends when the reference voltage reaches the specified voltage.

Soft start time is not always equal to the turn-on speed of the step-down DC/DC converter. Please note that the turn-on speed could be affected by the power supply capacity, the output current, the inductance value and the C_{OUT} value.

Starting-up with Power Supply

After the power-on, when V_{IN} exceeds the UVLO released voltage (V_{UVLO2}), the IC starts to operate. Then, soft-start circuit starts to operate and after a certain period of time, V_{REF} gradually increases up to the specified value. Soft-start time starts when soft-start circuit is activated, and ends when V_{REF} reaches the specified voltage.



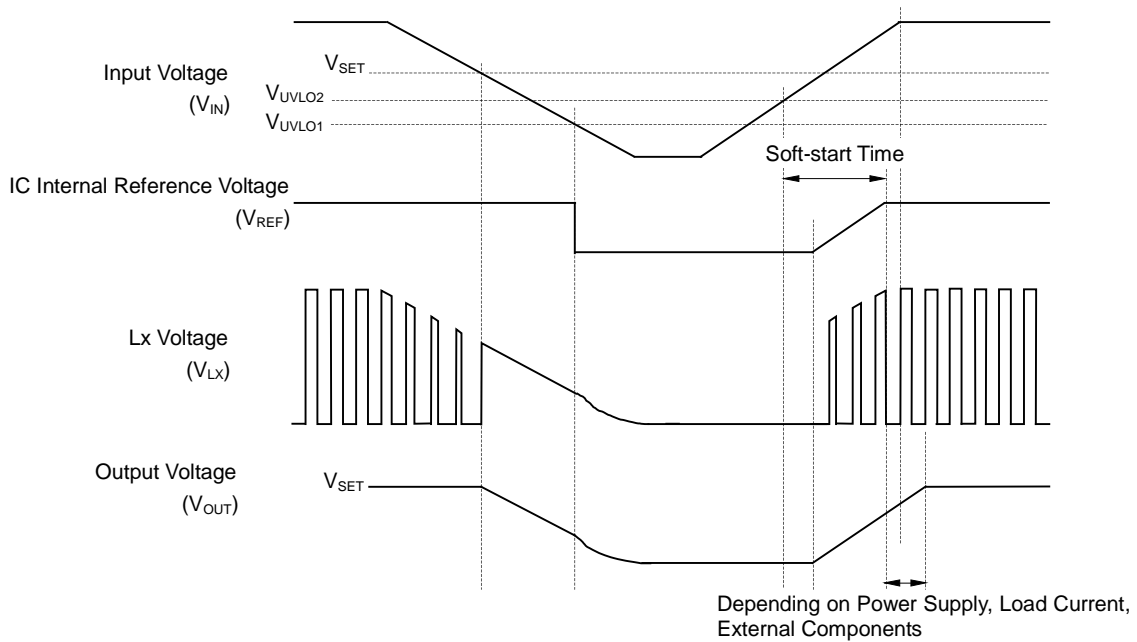
Please note that the turn-on speed of V_{OUT} could be affected by the power supply capacity, the output current, the inductance value, the C_{OUT} value and the turn-on speed of V_{IN} determined by C_{IN} .

2. Under Voltage Lockout (UVLO) Circuit

If V_{IN} becomes lower than V_{SET} , the step-down DC/DC converter stops the switching operation and ON duty becomes 100%, and then V_{OUT} gradually drops according to V_{IN} .

If the V_{IN} becomes lower than the UVLO detector threshold (V_{UVLO1}), the UVLO circuit starts to operate, V_{REF} stops, and P-channel and N-channel built-in switch transistors turn "OFF". As a result, V_{OUT} drops according to the C_{OUT} capacitance value and the load.

To restart the operation, V_{IN} needs to be higher than V_{UVLO2} . The timing chart below shows the voltage shifts of V_{REF} , V_{LX} and V_{OUT} when V_{IN} value is varied.



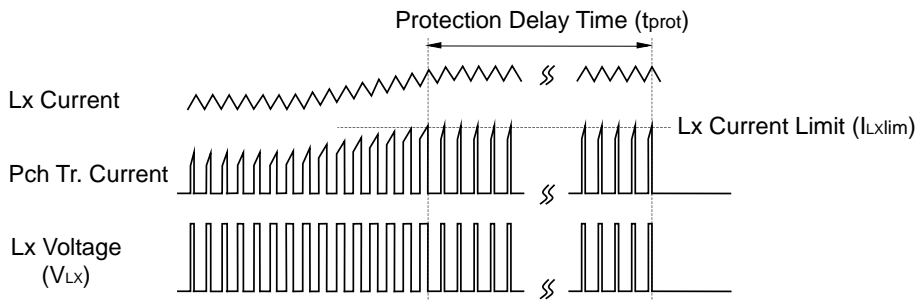
Falling edge (operating) and rising edge (releasing) waveforms of V_{OUT} could be affected by the initial voltage of C_{OUT} and the output current of V_{OUT} .

3. Overcurrent Protection Circuit, Latch Type Protection Circuit

Overcurrent protection circuit supervises the inductor peak current (the peak current flowing through Pch Tr.) in each switching cycle, and if the current exceeds the Lx current limit (I_{LXLIM}), it turns off Pch Tr. I_{LXLIM} of the RP504x is set to $Ty.900\text{ mA}$.

Latch type protection circuit latches the built-in driver to the OFF state and stops the operation of the step-down DC/DC converter if the overcurrent status continues or V_{OUT} continues being the half of the setting voltage for equal or longer than protection delay time (t_{prot}).

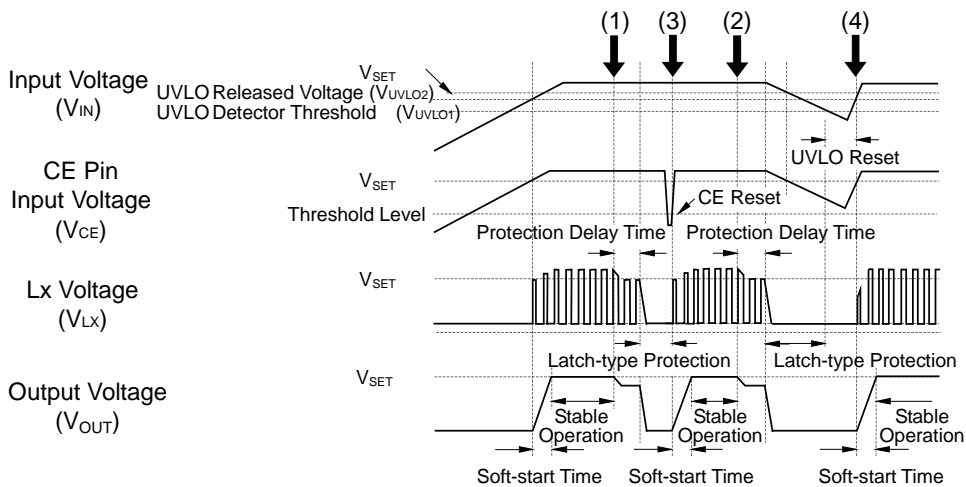
Please note that I_{LXLIM} and t_{prot} could be easily affected by self-heating or ambient environment. If the V_{IN} drops dramatically or becomes unstable due to short-circuit, protection operation and t_{prot} could be affected.



To release the latch type protection circuit, restart the IC by inputting "L" signal to the CE pin, or restart the IC with power-on or make the supply voltage lower than V_{UVLO1} .

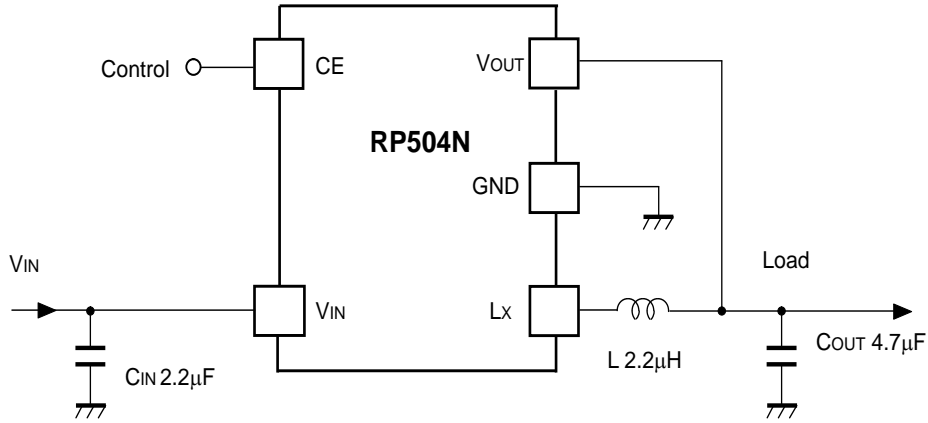
The timing chart below shows the voltage shift of V_{CE} , V_{LX} and V_{OUT} when the IC status is changed by the following orders: V_{IN} rising → stable operation → high load → CE reset → stable operation → V_{IN} falling → V_{IN} recovering (UVLO reset) → stable operation.

- (1)(2) If the large current flows through the circuit or if the IC goes into low V_{OUT} condition due to short-circuit or other reasons, the latch type protection circuit latches the built-in driver to "OFF" state after t_{prot} . Then, V_{LX} becomes "L" and V_{OUT} turns "OFF".
- (3) The latch type protection circuit is released by CE reset, which puts the IC into "L" once with the CE pin and back into "H".
- (4) The latch type protection circuit is released by UVLO reset, which makes V_{IN} lower than V_{UVLO1} .

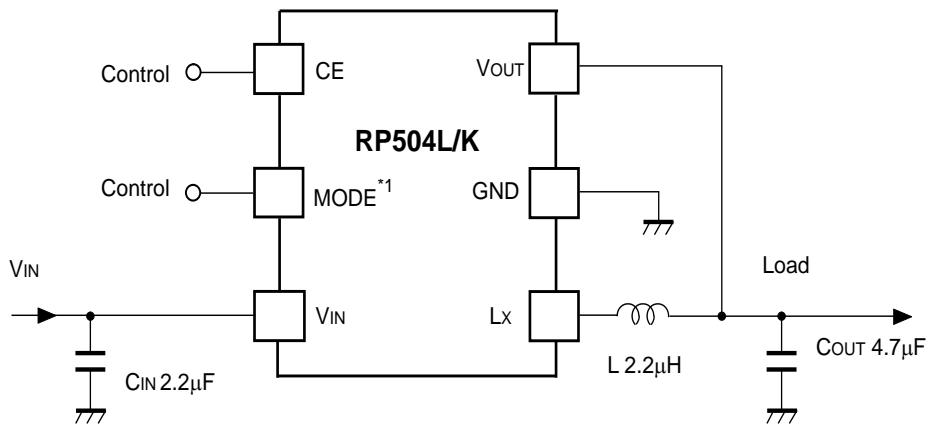


APPLICATION INFORMATION

TYPICAL APPLICATION CIRCUIT



RP504N Typical Application Circuit: MODE Pin not included



RP504K/L Typical Application Circuit: MODE Pin included

*1 MODE = "H": forced PWM control, MODE = "L": PWM/VFM auto switching control

Recommended Components

Symbol	Capacitance	Type	Manufacturer
C _{IN}	2.2 µF	Ceramic Capacitor	C1608JB0J225K(TDK)
	2.2 µF x 2		C1005JB0J225K (TDK) JMK105BJ225MV (Taiyo Yuden)
	4.7 µF		C1005X5R0J475M (TDK) JMK105BJ475MV (Taiyo Yuden)
C _{OUT}	4.7 µF	Ceramic Capacitor	C1608JB0J475K (TDK) GRM188B30J475KE18 (Murata)
L	2.2 µH	Inductor	MIPSZ2520D2R2 (FDK) MIPS2520D2R2 (FDK) MLP2520S2R2M (TDK) VLS252010T-2R2M (TDK)

OUTPUT CURRENT AND SELECTION OF EXTERNAL COMPONENTS

The following equations explain the relationship between output current and peripheral components used in the diagrams in *TYPICAL APPLICATIONS*.

Ripple Current P-P value is described as I_{RP} , ON resistance of P-channel Tr. is described as R_{ONP} , ON resistance of N-channel Tr. is described as R_{ONN} , and DC resistor of the inductor is described as R_L .

$$V_{IN} = V_{OUT} + (R_{ONP} + R_L) \times I_{OUT} + L \times I_{RP} / t_{on} \dots\dots\dots \text{Equation 3}$$

Second, when P-channel Tr. is "OFF" (N-channel Tr. Is "ON"), the following equation is satisfied.

$$L \times I_{RP} / t_{off} = R_{ONN} \times I_{OUT} + V_{OUT} + R_L \times I_{OUT} \dots\dots\dots \text{Equation 4}$$

Put Equation 4 into Equation 3 to solve ON duty of P-channel Tr. ($D_{ON} = t_{on} / (t_{off} + t_{on})$):

$$D_{ON} = (V_{OUT} + R_{ONN} \times I_{OUT} + R_L \times I_{OUT}) / (V_{IN} + R_{ONN} \times I_{OUT} - R_{ONP} \times I_{OUT}) \dots\dots\dots \text{Equation 5}$$

Ripple Current is described as follows:

$$I_{RP} = (V_{IN} - V_{OUT} - R_{ONP} \times I_{OUT} - R_L \times I_{OUT}) \times D_{ON} / f_{osc} / L \dots\dots\dots \text{Equation 6}$$

Peak current that flows through L, and Lx Tr. is described as follows:

$$I_{LXMAX} = I_{OUT} + I_{RP} / 2 \dots\dots\dots \text{Equation 7}$$

Consider I_{LXMAX} when setting conditions of input and output, as well as selecting the external components. The above calculation formulas are based on the ideal operation of the ICS in continuous mode.

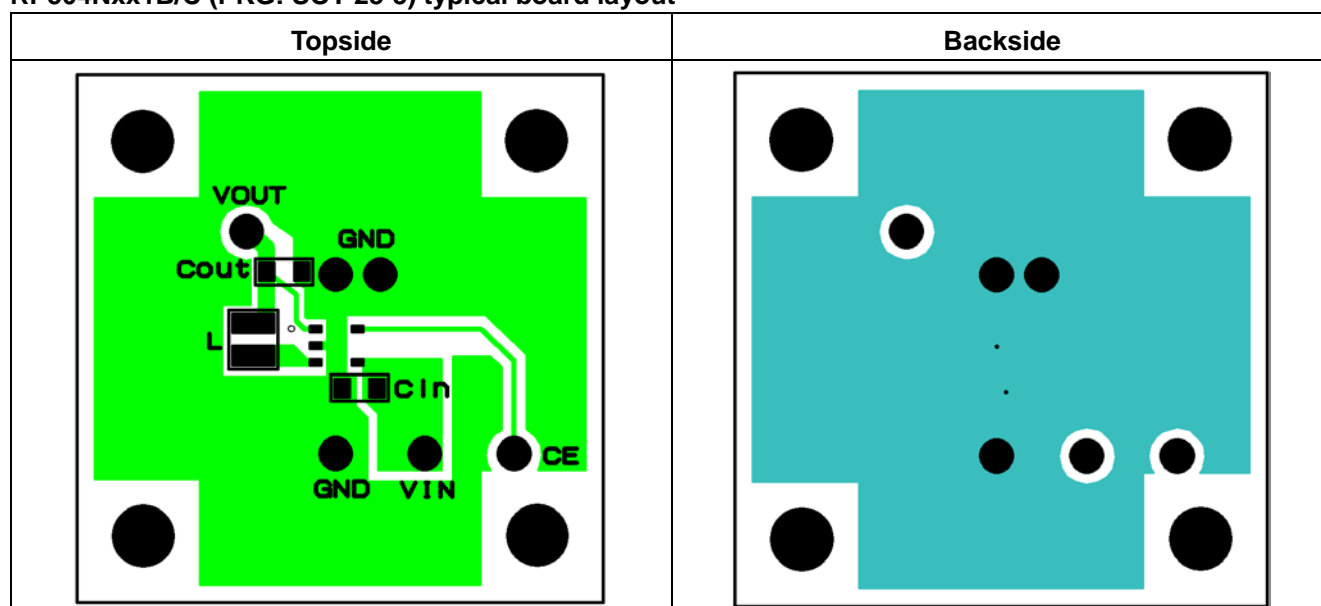
TECHNICAL NOTES

The performance of power supply circuits using this IC largely depends on the peripheral circuits. Please be very careful when setting the peripheral parts. When designing the peripheral circuits of each part, PCB patterns, and this IC, please do not exceed the rated values (Voltage, Current, Power).

- Ensure the V_{IN} and GND lines are sufficiently robust. A large switching current flows through the GND lines, the V_{DD} line, the V_{OUT} line, an inductor, and L_X . If their impedance is too high, noise pickup or unstable operation may result. Set the external components as close as possible to the IC and minimize the wiring between the components and the IC, especially between a capacitor (C_{IN}) and the V_{IN} pin. The wiring between V_{OUT} and load and between L and V_{OUT} should be separated.
- Choose a low ESR ceramic capacitor. The capacitance of C_{IN} should be more than or equal to $2.2 \mu\text{F}$. The capacitance of a capacitor (C_{OUT}) should be between $4.7 \mu\text{F}$ to $10 \mu\text{F}$.
- The Inductance value should be set within the range of $2.2 \mu\text{H}$ to $4.7 \mu\text{H}$. However, the inductance value is limited by output voltage. Refer to the table below. The phase compensation of this IC is designed according to the C_{OUT} and L values. Choose an inductor that has small DC resistance, has enough allowable current and is hard to cause magnetic saturation. If the inductance value of an inductor is extremely small, the peak current of L_X may increase. The increased L_X peak current reaches “ L_X limit current” to trigger overcurrent protection circuit even if the load current is less than 600 mA.
- Overcurrent protection circuit, Latch-type protection circuit may be affected by self-heating and heat radiation environment.

PCB LAYOUT

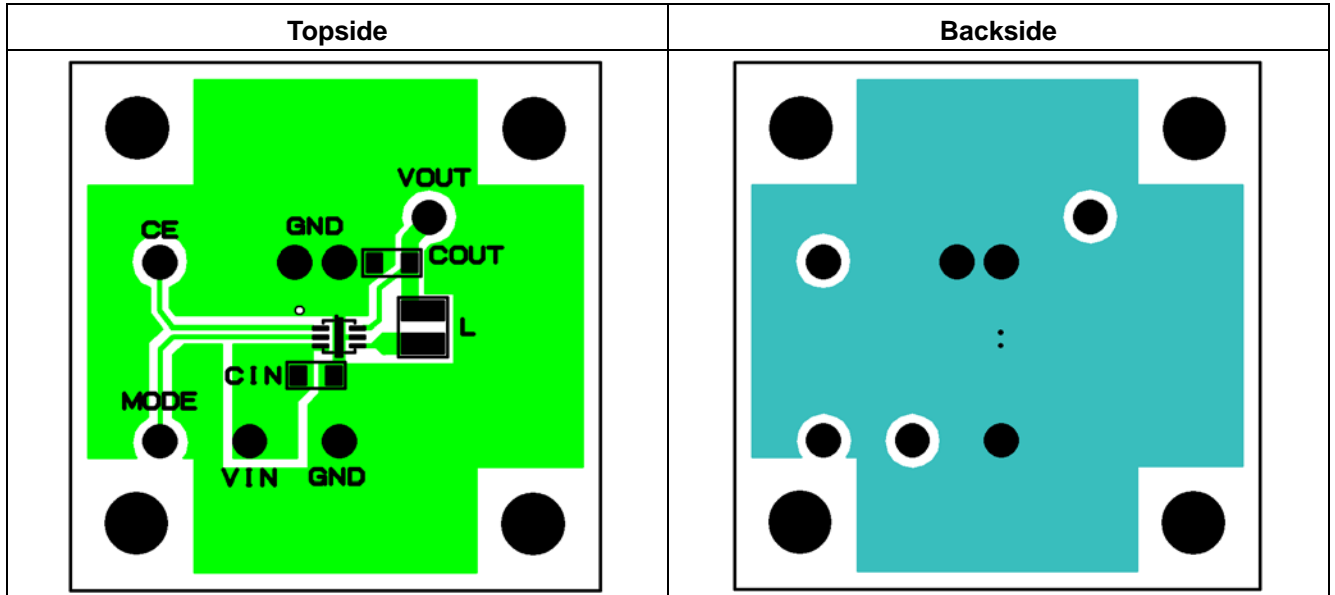
RP504Nxx1B/C (PKG: SOT-23-5) typical board layout



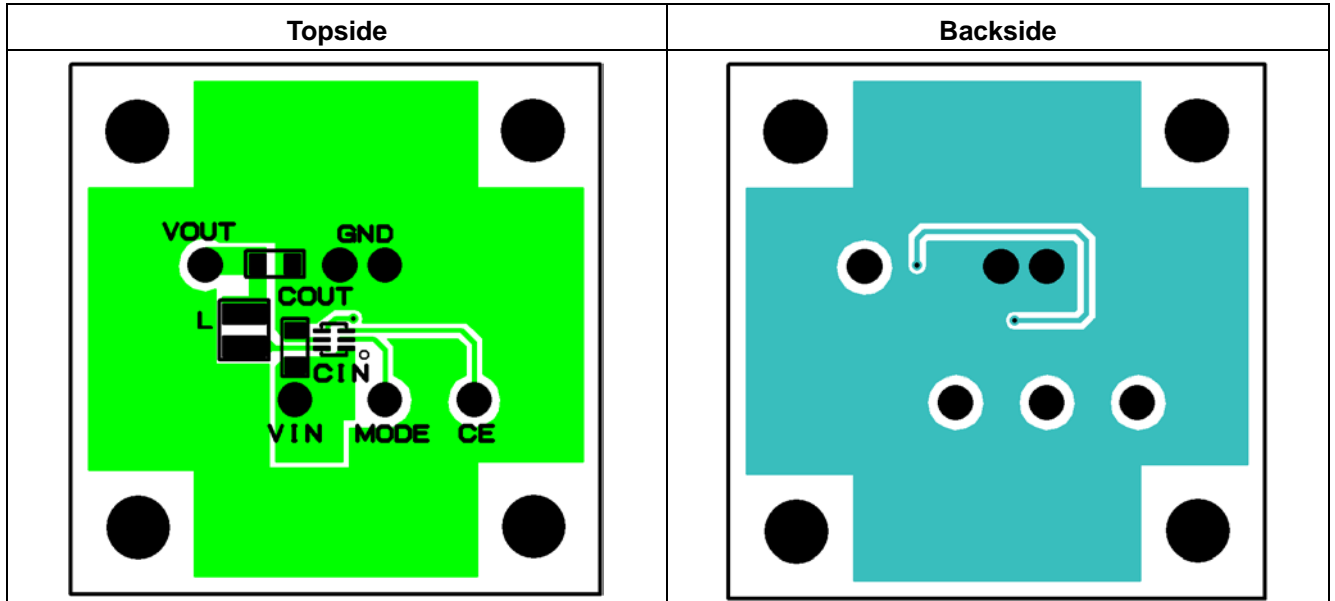
RP504x

NO.EA-259-170620

RP504Lxx1A/D (PKG: DFN1616-6B) typical board layout



RP505Kxx1A/D (PKG: DFN(PLP)1216-6F) typical board layout



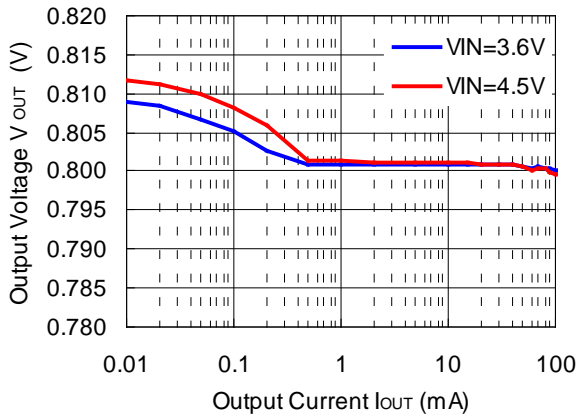
TYPICAL CHARACTERISTICS

Note: Typical Characteristics are intended to be used as reference data; they are not guaranteed.

1) Output Voltage vs. Output Current

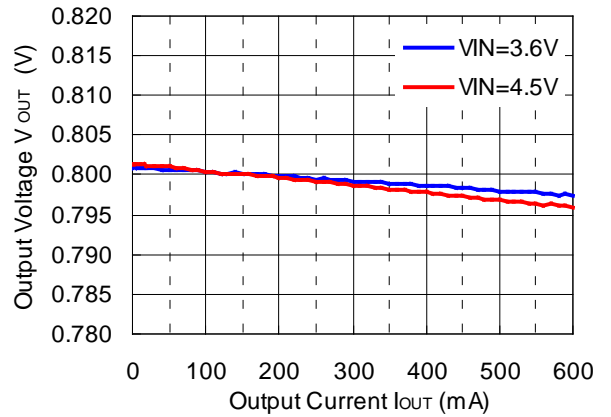
RP504x $V_{OUT} = 0.8\text{ V}$

MODE = "L" PWM/VFM Auto Switching Control



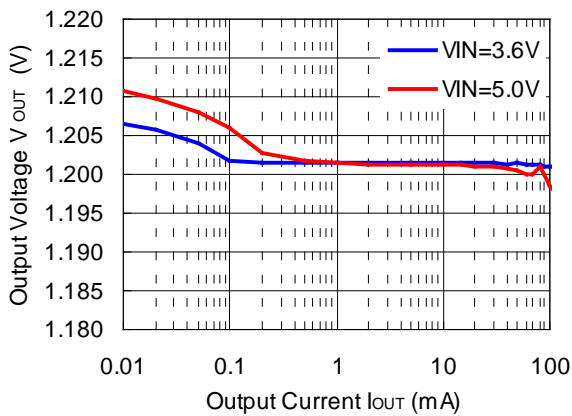
RP504x $V_{OUT} = 0.8\text{ V}$

MODE = "H" Forced PWM Control



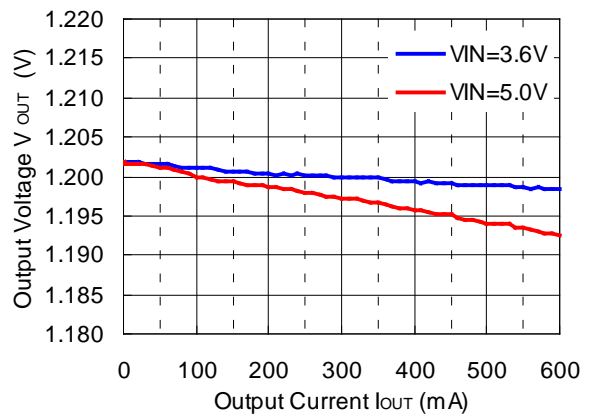
RP504x $V_{OUT} = 1.2\text{ V}$

MODE = "L" PWM/VFM Auto Switching Control



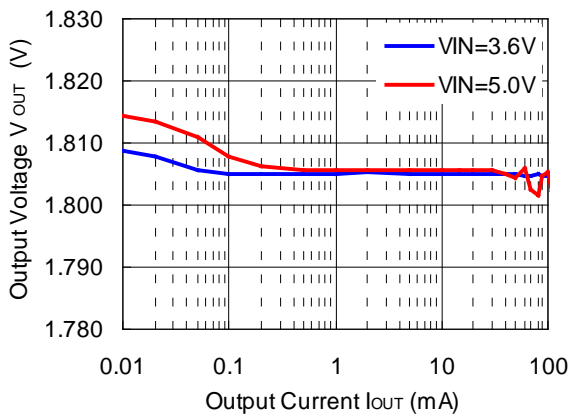
RP504x $V_{OUT} = 1.2\text{ V}$

MODE = "H" Forced PWM Control



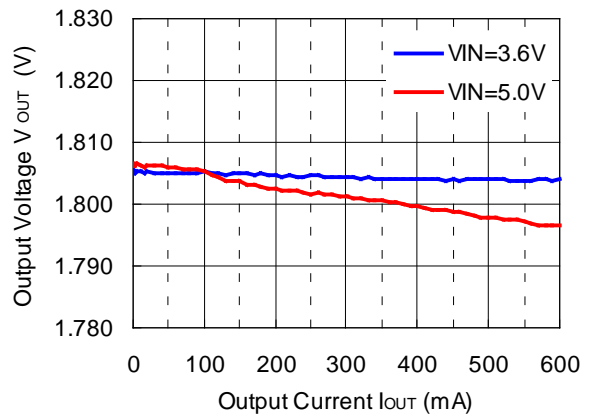
RP504x $V_{OUT} = 1.8\text{ V}$

MODE = "L" PWM/VFM Auto Switching Control



RP504x $V_{OUT} = 1.8\text{ V}$

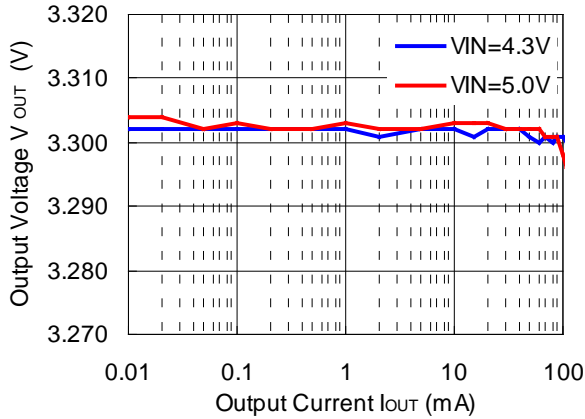
MODE = "H" Forced PWM Control



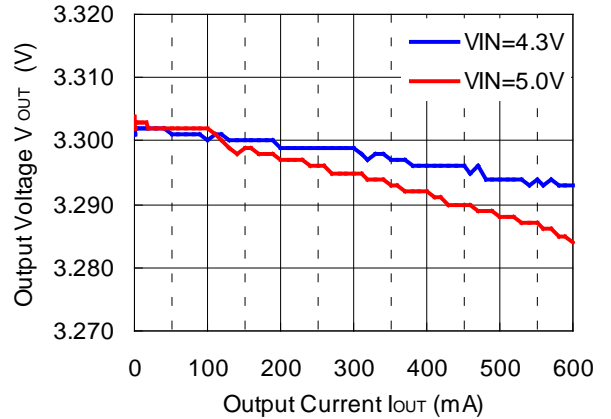
RP504x

NO.EA-259-170620

RP504x $V_{OUT} = 3.3\text{ V}$
MODE = "L" PWM/VFM Auto Switching Control

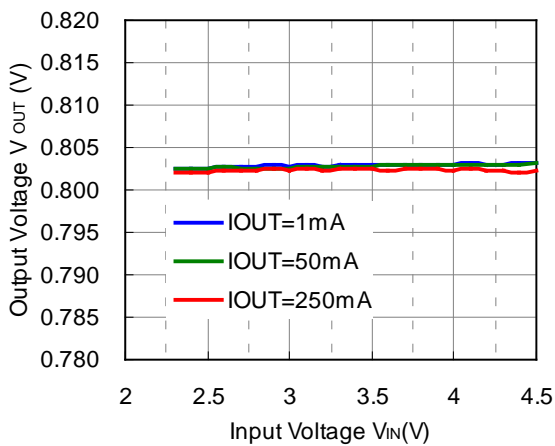


RP504x $V_{OUT} = 3.3\text{ V}$
MODE = "H" Forced PWM Control

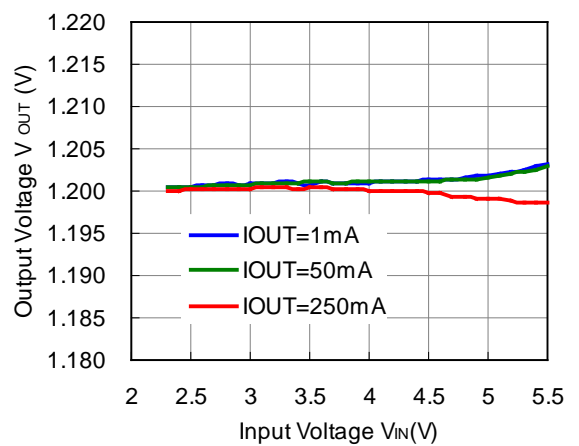


2) Output Voltage vs. Input Voltage

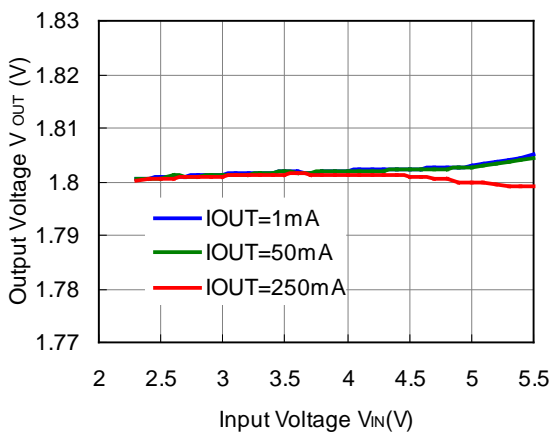
RP504x $V_{OUT} = 0.8\text{ V}$
MODE = "H" Forced PWM Control



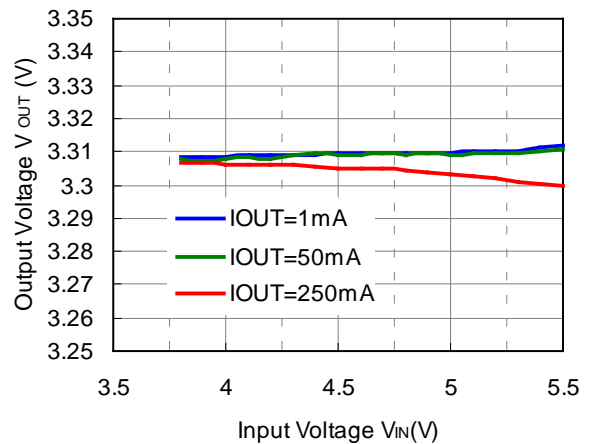
RP504x $V_{OUT} = 1.2\text{ V}$
MODE = "H" Forced PWM Control



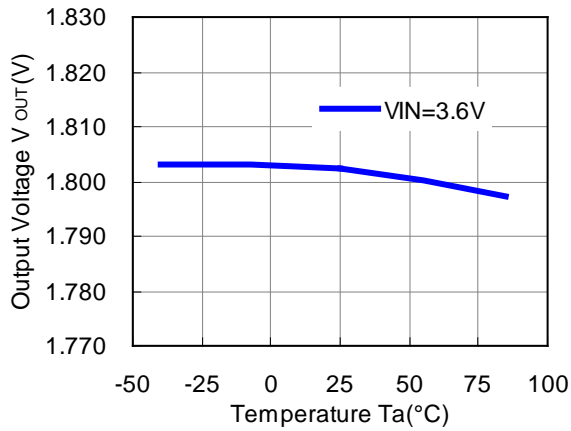
RP504x $V_{OUT} = 1.8\text{ V}$
MODE = "H" Forced PWM Control



RP504x $V_{OUT} = 3.3\text{ V}$
MODE = "H" Forced PWM Control

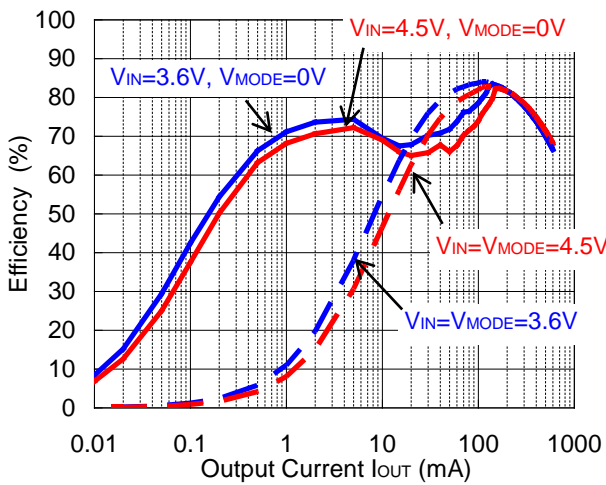


3) Output Voltage vs. Temperature

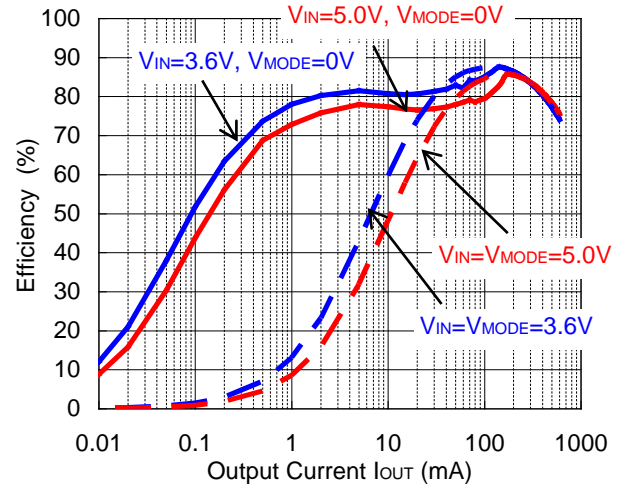


4) Efficiency vs. Output Current

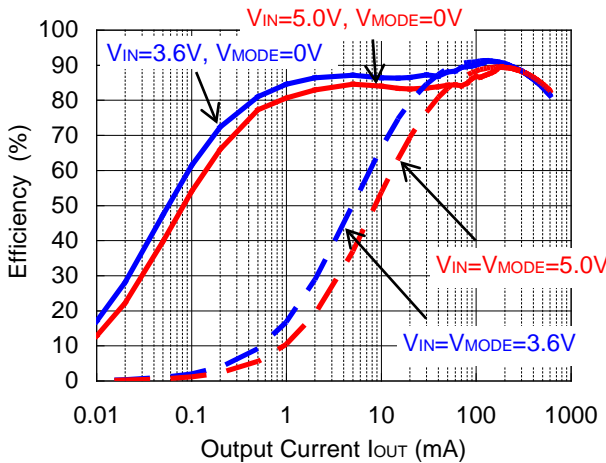
RP504x V_{OUT} = 0.8 V



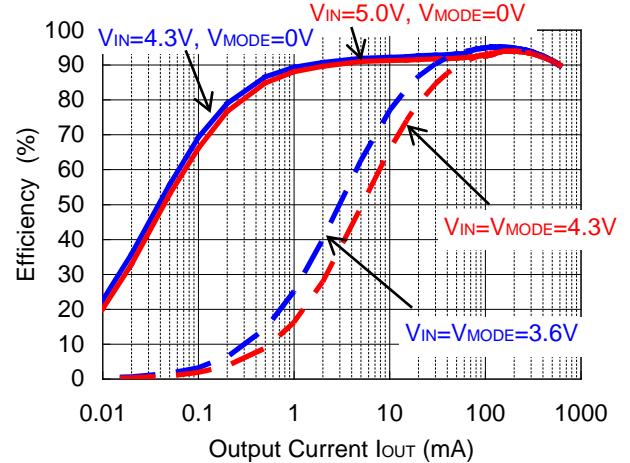
RP504x V_{OUT} = 1.2 V



RP504x V_{OUT} = 1.8 V



RP504x V_{OUT} = 3.3 V



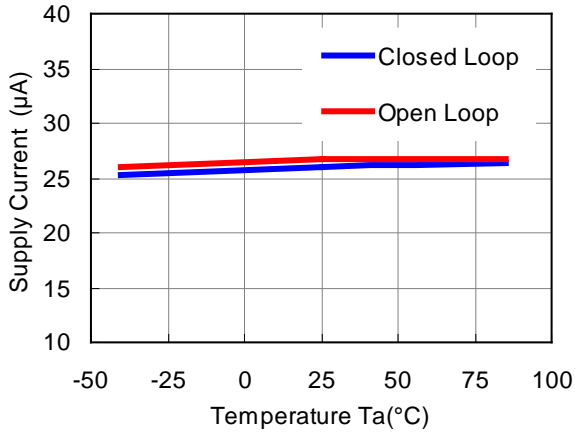
RP504x

NO.EA-259-170620

5) Supply Current vs. Temperature

RP504x $V_{OUT} = 1.8\text{ V}$ ($V_{IN} = 5.5\text{ V}$)

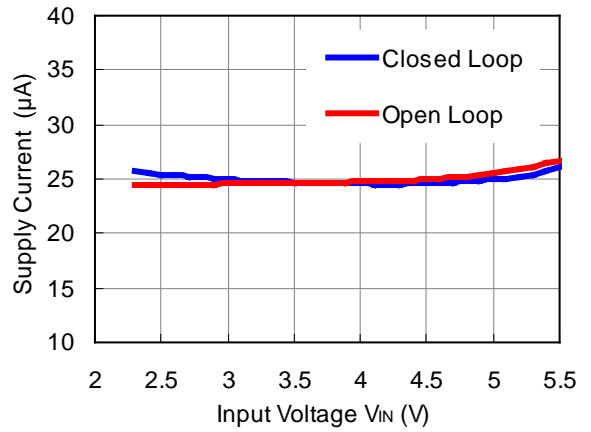
MODE = "L" PWM/VFM Auto Switching Control



6) Supply Current vs. Input Voltage

RP504x $V_{OUT} = 1.8\text{ V}$

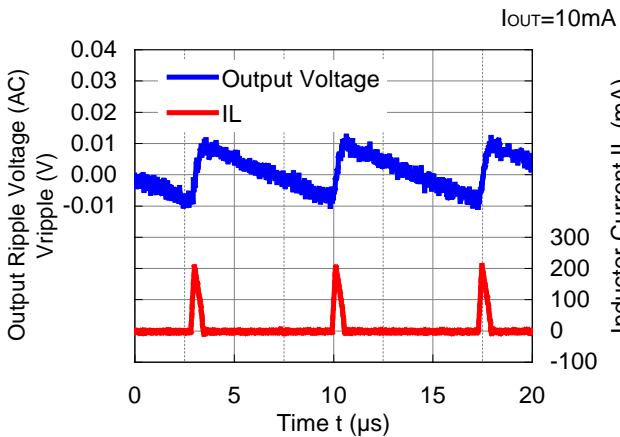
MODE = "L" PWM/VFM Auto Switching Control



7) Output Voltage Waveform

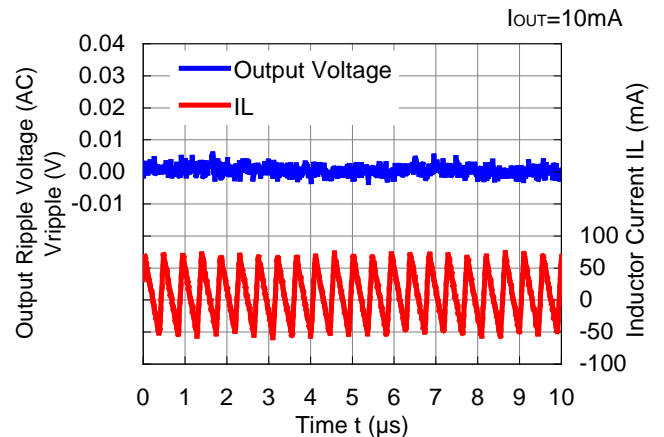
RP504x $V_{OUT} = 0.8\text{ V}$ ($V_{IN} = 3.6\text{ V}$)

MODE = "L" PWM/VFM Auto Switching Control



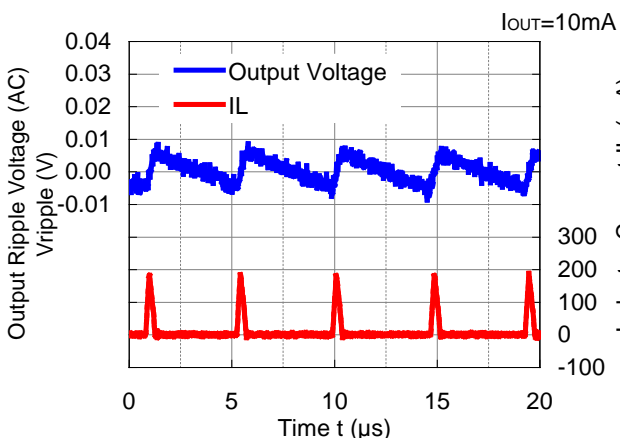
RP504x $V_{OUT} = 0.8\text{ V}$ ($V_{IN} = 3.6\text{ V}$)

MODE = "H" Forced PWM Control



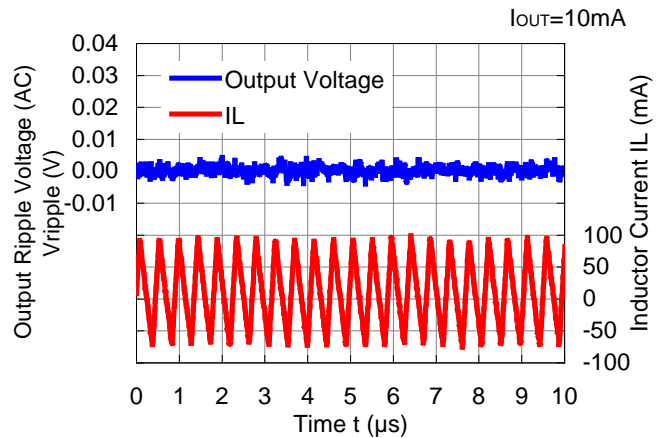
RP504x $V_{OUT} = 1.2\text{ V}$ ($V_{IN} = 3.6\text{ V}$)

MODE = "L" PWM/VFM Auto Switching Control

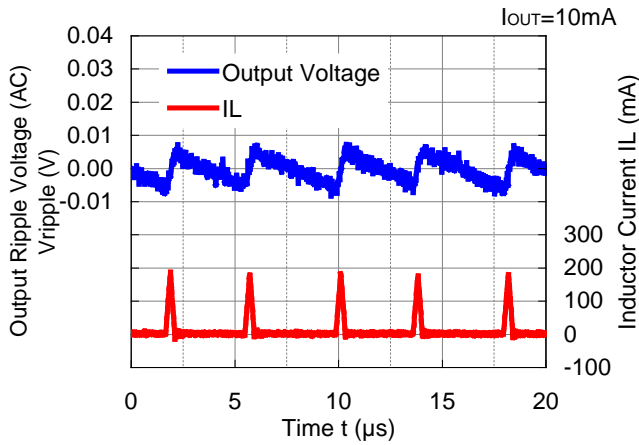


RP504x $V_{OUT} = 1.2\text{ V}$ ($V_{IN} = 3.6\text{ V}$)

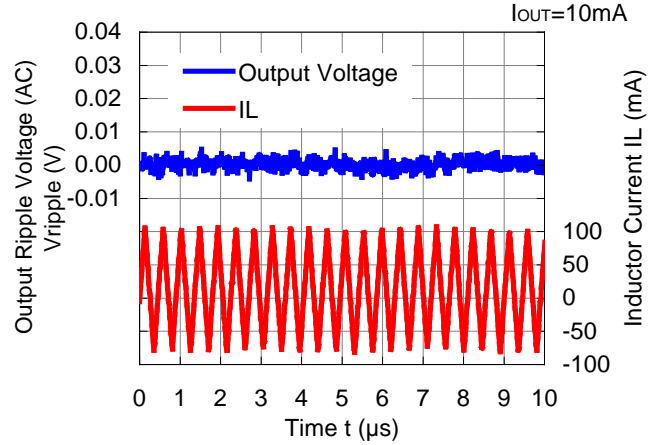
MODE = "H" Forced PWM Control



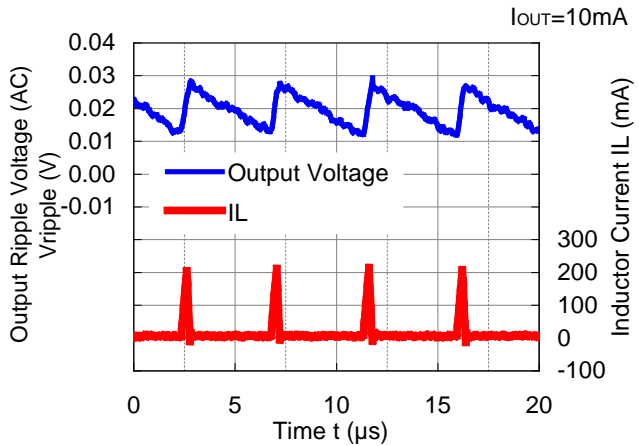
RP504x $V_{OUT} = 1.8\text{ V}$ ($V_{IN} = 3.6\text{ V}$)
MODE = "L" PWM/VFM Auto Switching Control



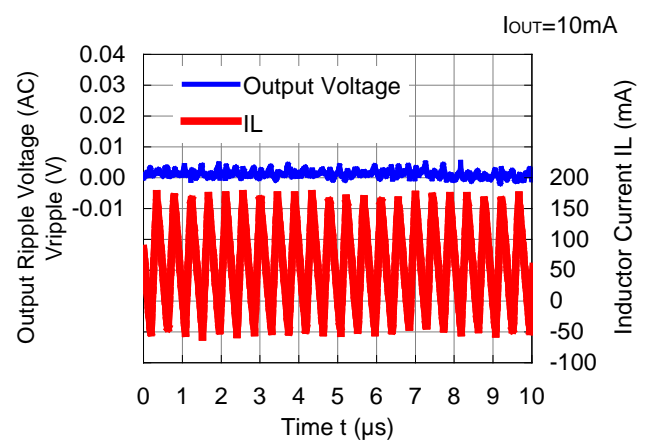
RP504x $V_{OUT} = 1.8\text{ V}$ ($V_{IN} = 3.6\text{ V}$)
MODE = "H" Forced PWM Control



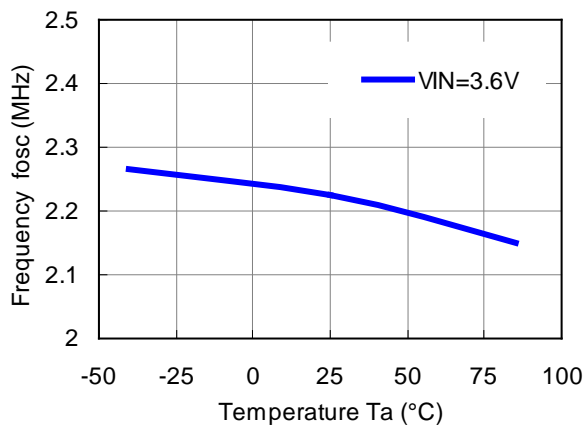
RP504x $V_{OUT} = 3.3\text{ V}$ ($V_{IN} = 5.0\text{ V}$)
MODE = "L" PWM/VFM Auto Switching Control



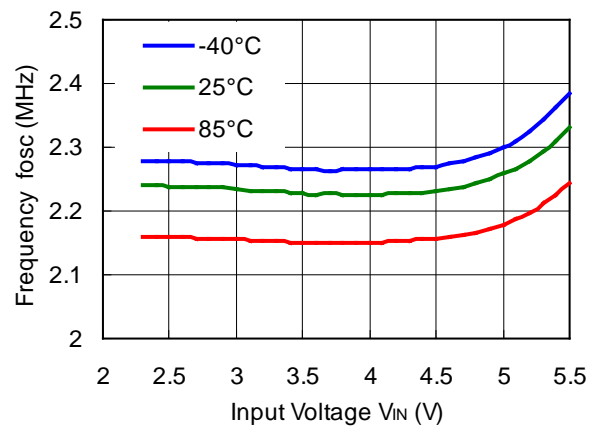
RP504x $V_{OUT} = 3.3\text{ V}$ ($V_{IN} = 5.0\text{ V}$)
MODE = "H" Forced PWM Control



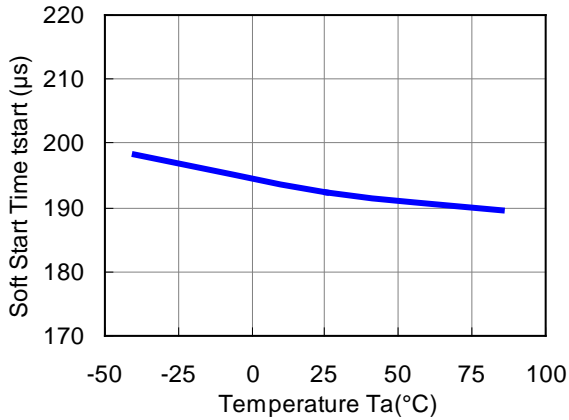
8) Frequency vs. Temperature



9) Frequency vs. Input Voltage

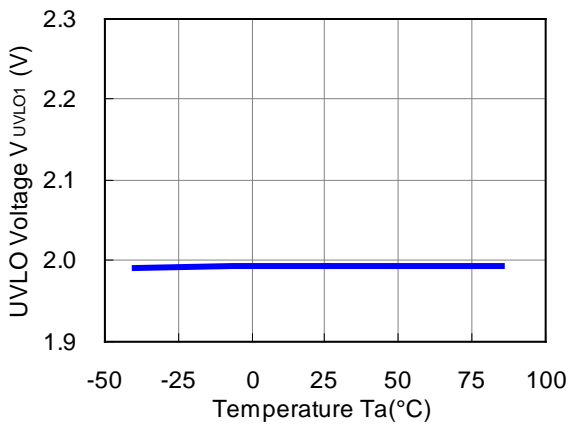


10) Soft Start Time vs. Temperature

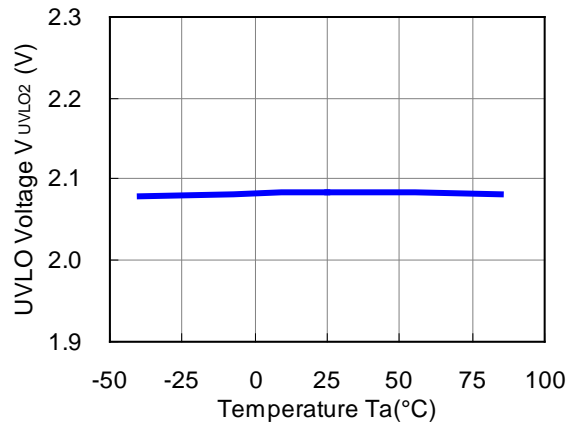


11) UVLO Detector Threshold / Released Voltage vs. Temperature

UVLO Detector Threshold Voltage

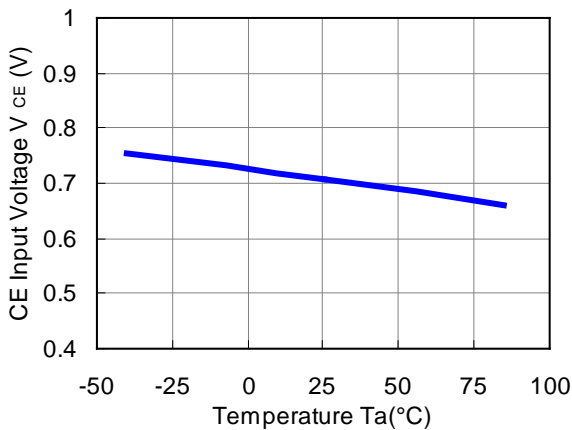


UVLO Released Voltage

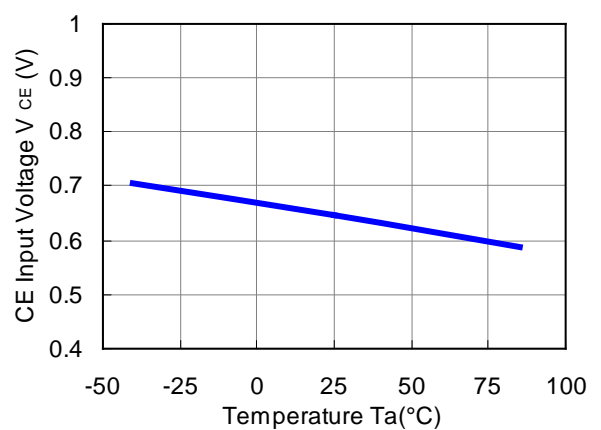


12) CE Input Voltage vs. Temperature

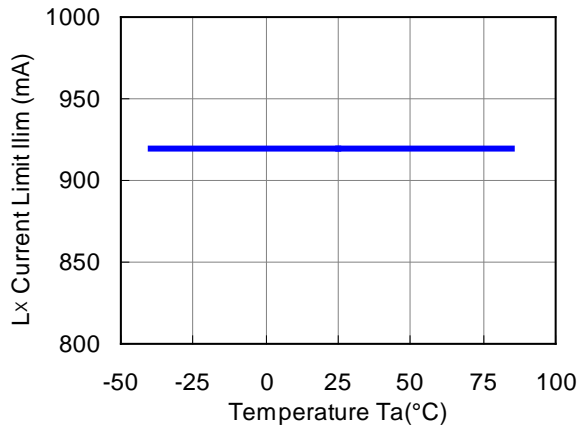
CE "H" Input Voltage (V_IN = 5.5 V)



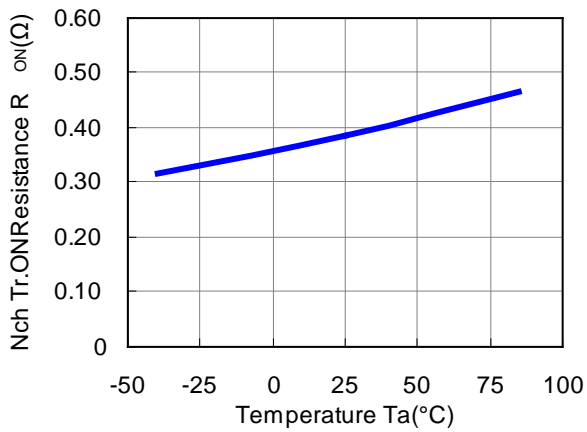
CE "H" Input Voltage (V_IN = 2.3 V)



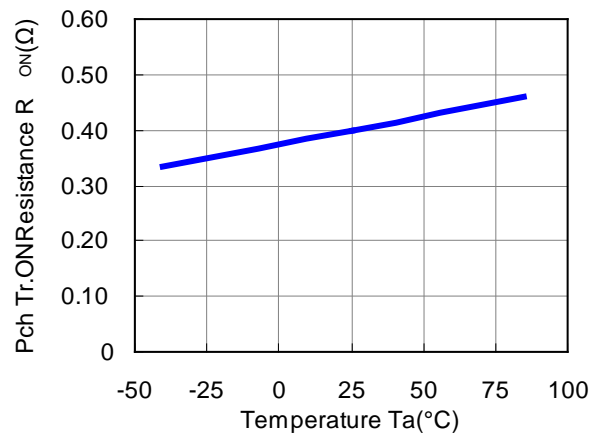
13) L_x Current Limit vs. Temperature



14) Nch Tr. ON Resistance vs. Temperature



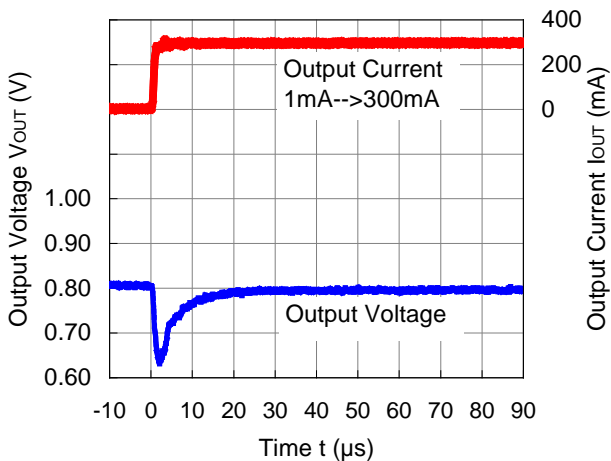
15) Pch Tr. ON Resistance vs. Temperature



16) Load Transient Response

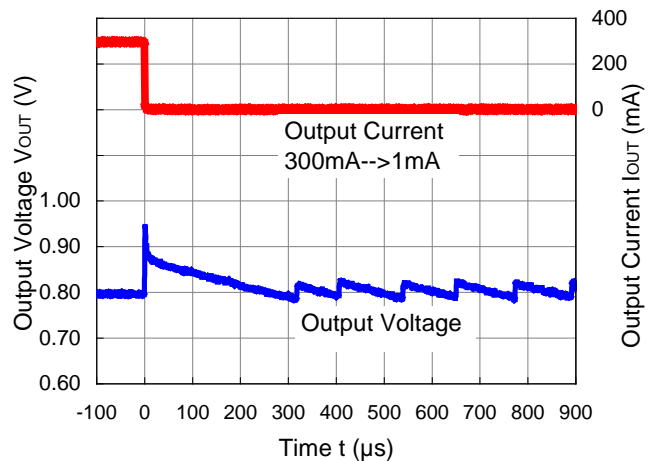
RP504x081x (V_{IN} = 3.6 V)

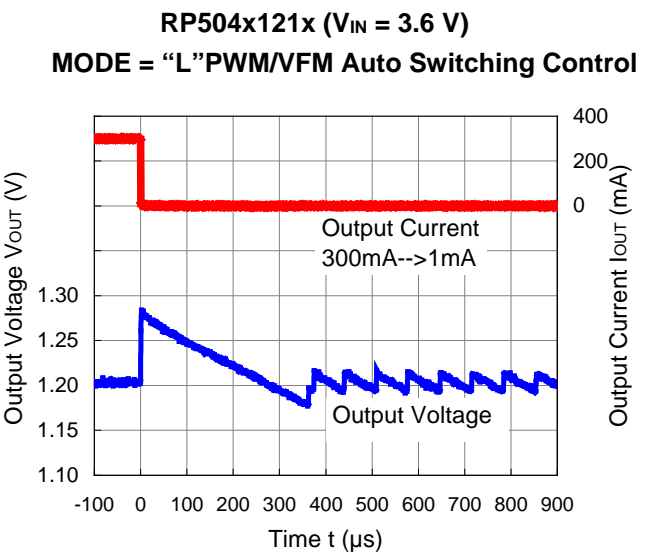
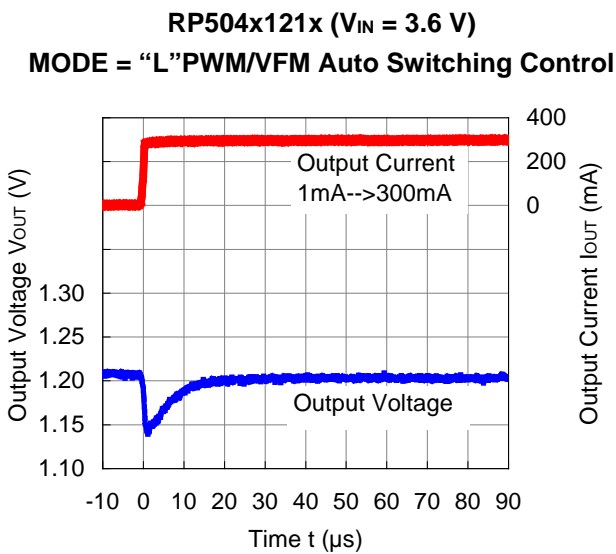
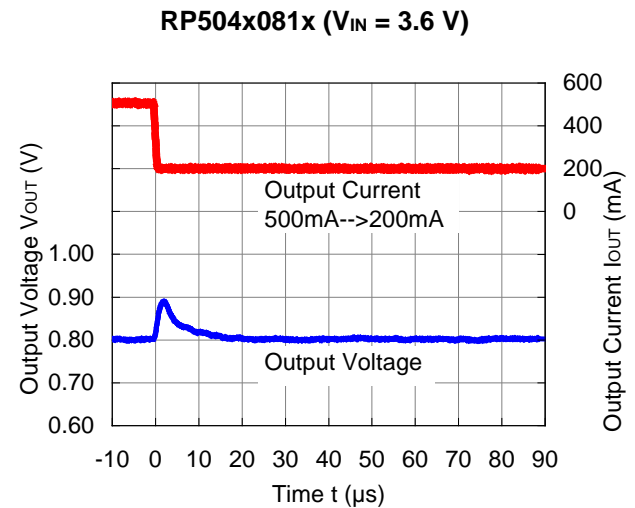
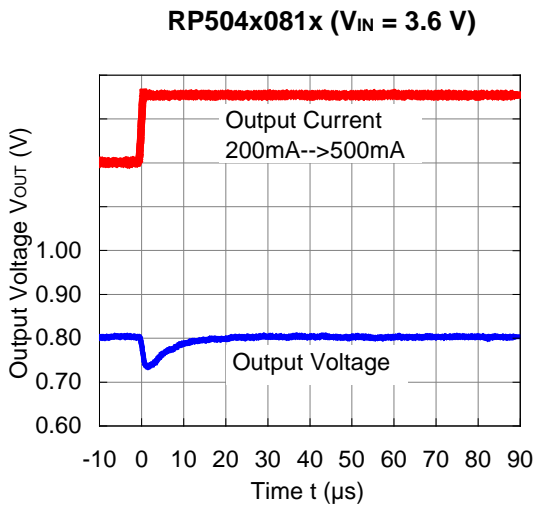
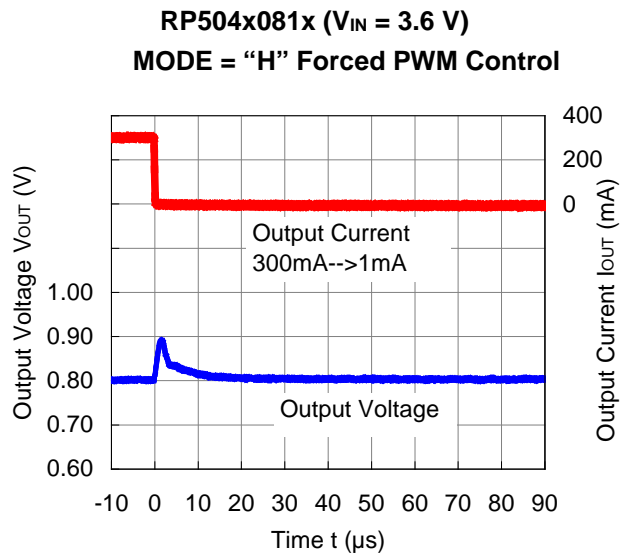
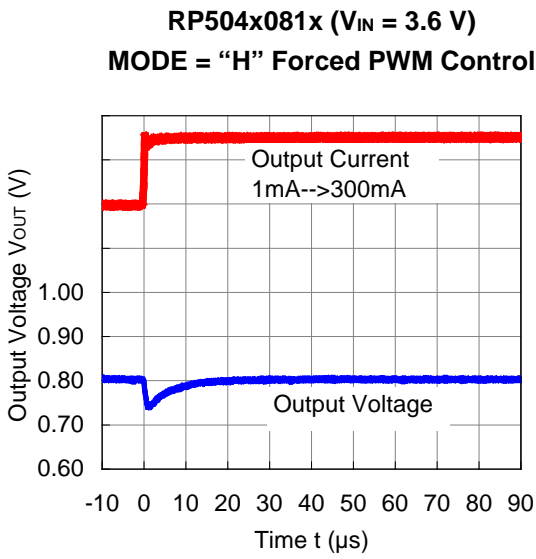
MODE = "L" PWM/VFM Auto Switching Control

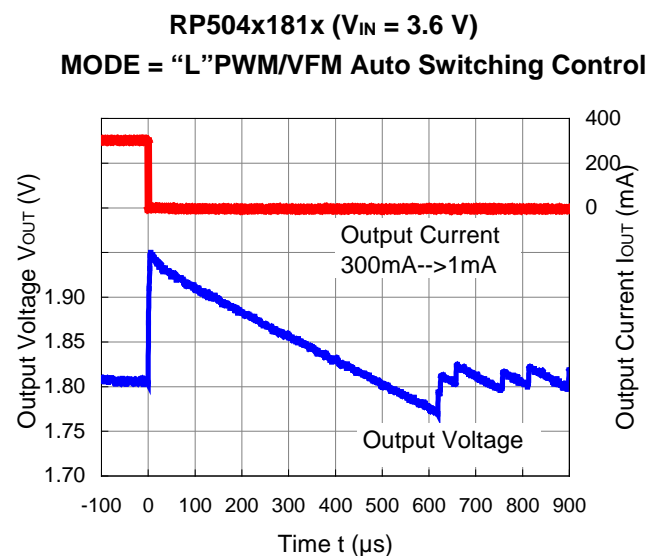
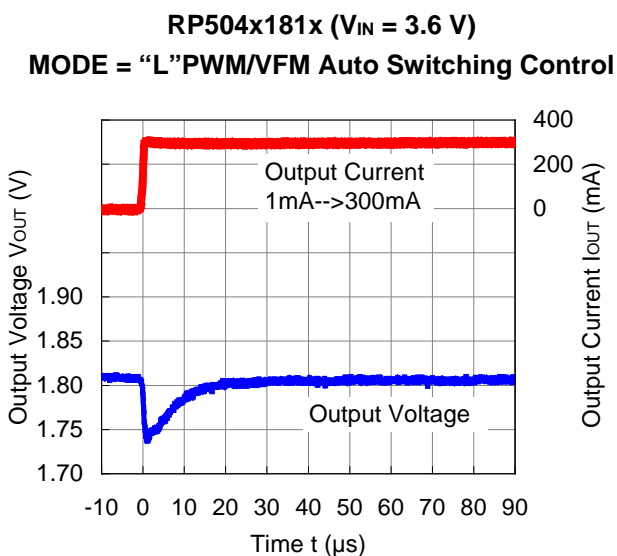
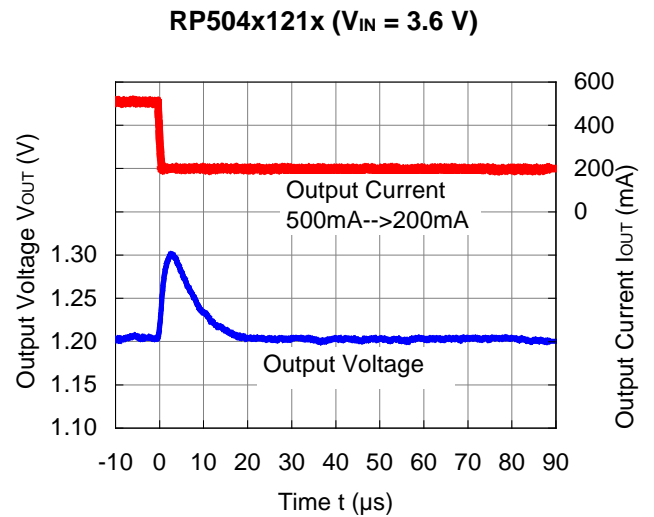
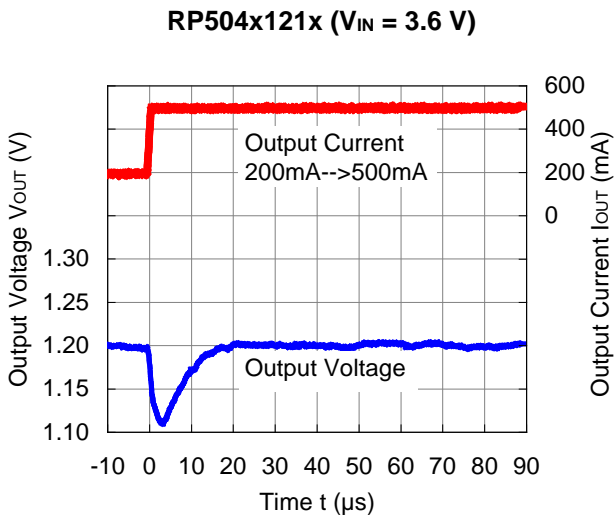
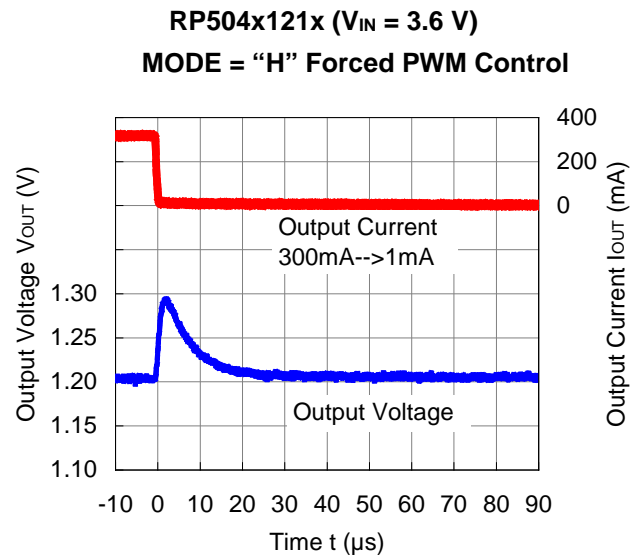
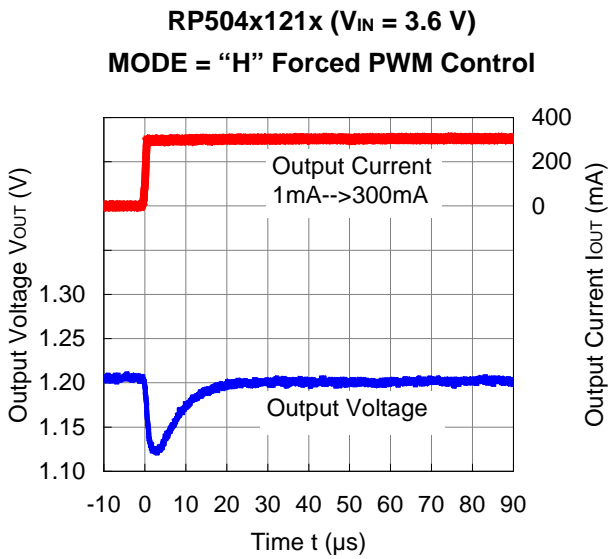


RP504x081x (V_{IN} = 3.6 V)

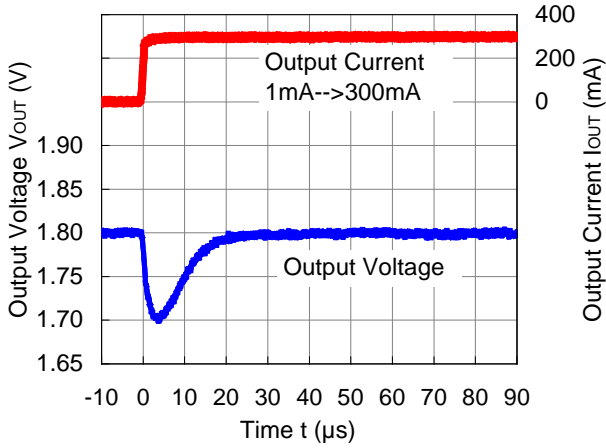
MODE = "L" PWM/VFM Auto Switching Control



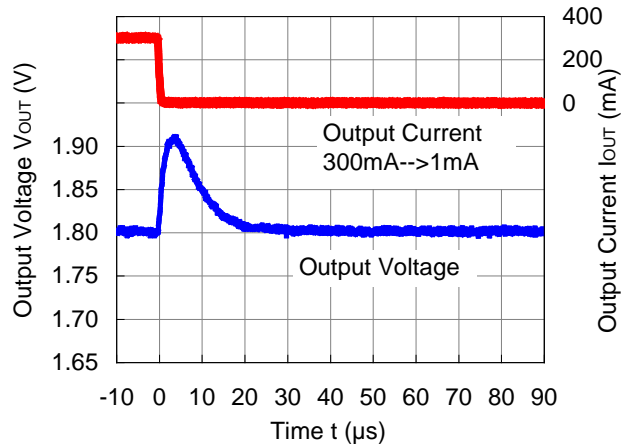




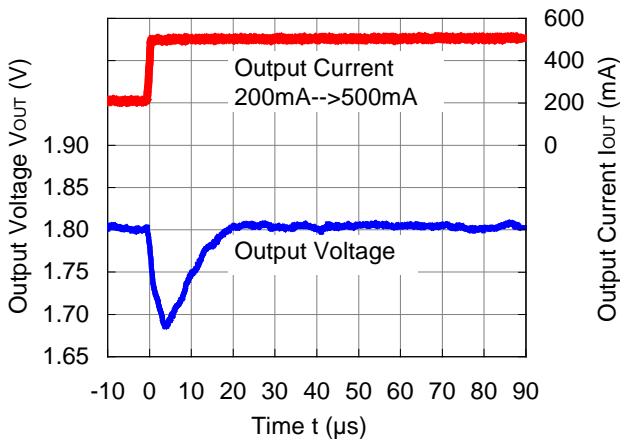
RP504x181x ($V_{IN} = 3.6\text{ V}$)
MODE = "H" Forced PWM Control



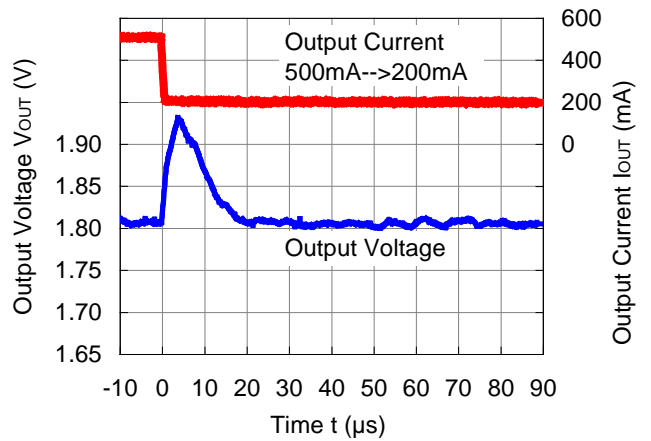
RP504x181x ($V_{IN} = 3.6\text{ V}$)
MODE = "H" Forced PWM Control



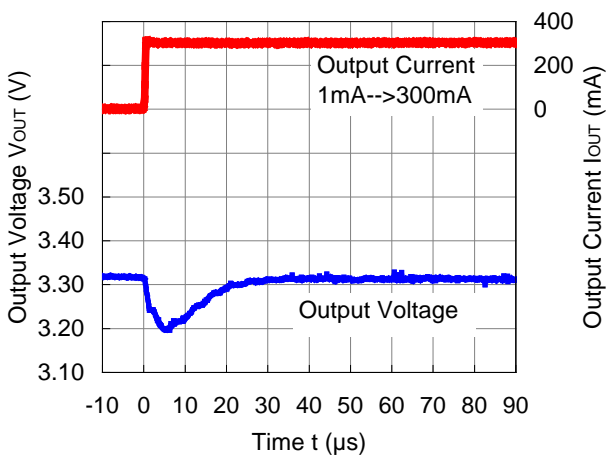
RP504x181x ($V_{IN} = 3.6\text{ V}$)



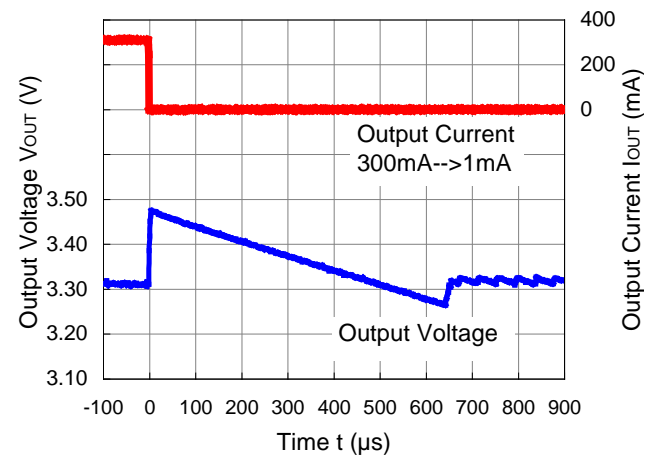
RP504x181x ($V_{IN} = 3.6\text{ V}$)

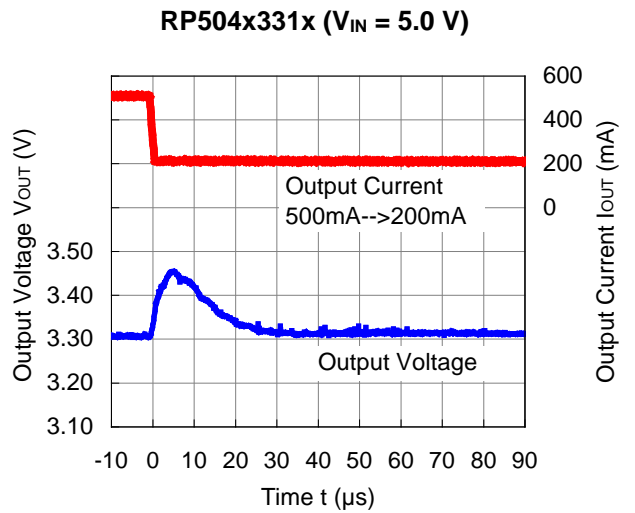
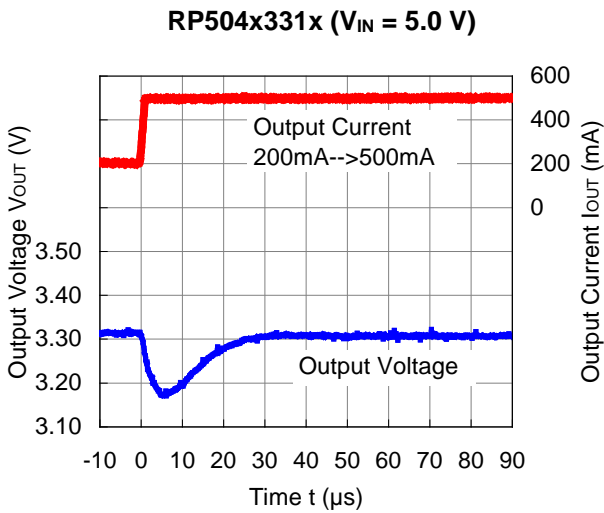
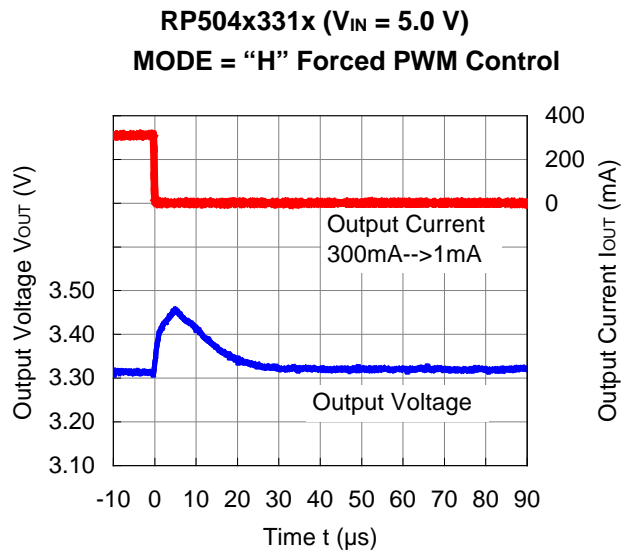
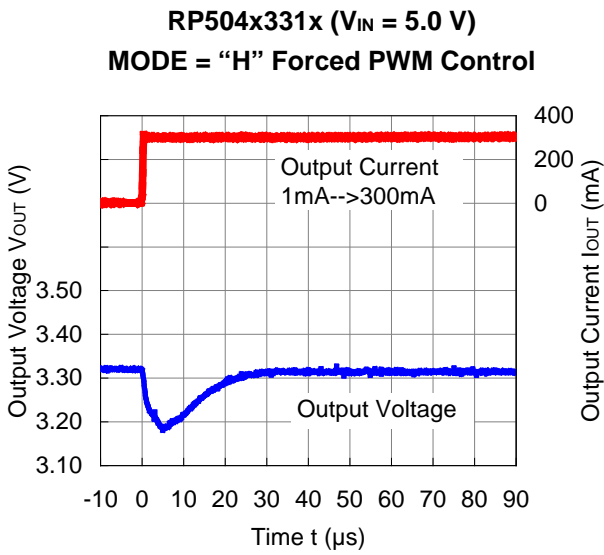


RP504x331x ($V_{IN} = 5.0\text{ V}$)
MODE = "L" PWM/VFM Auto Switching Control

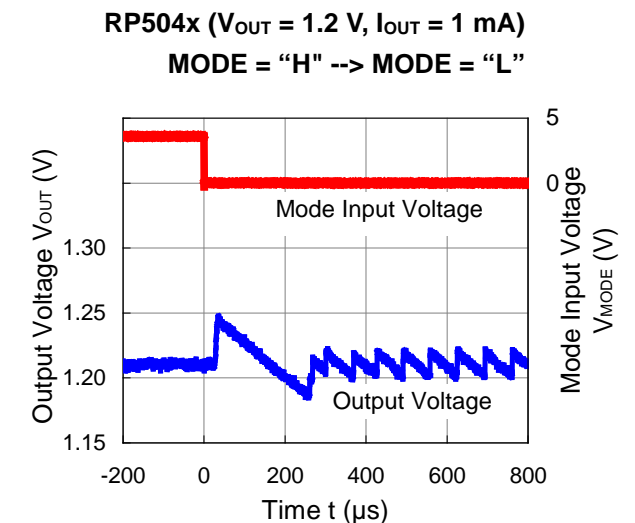
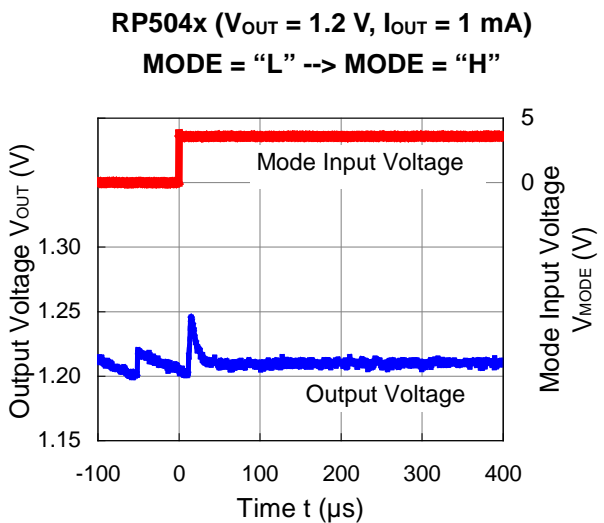


RP504x331x ($V_{IN} = 5.0\text{ V}$)
MODE = "L" PWM/VFM Auto Switching Control

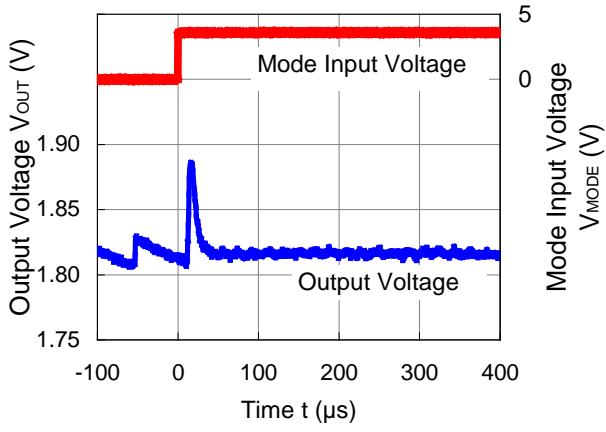




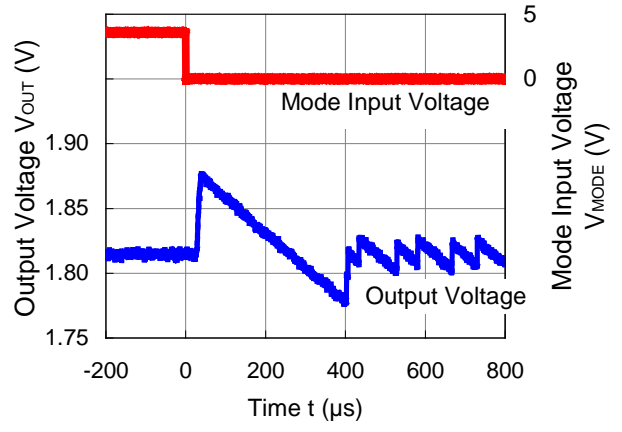
17) Mode Switching Waveform



RP504x ($V_{OUT} = 1.8\text{ V}$, $I_{OUT} = 1\text{ mA}$)
MODE = "L" --> MODE = "H"



RP504x ($V_{OUT} = 1.8\text{ V}$, $I_{OUT} = 1\text{ mA}$)
MODE = "H" --> MODE = "L"



The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following conditions are used in this measurement.

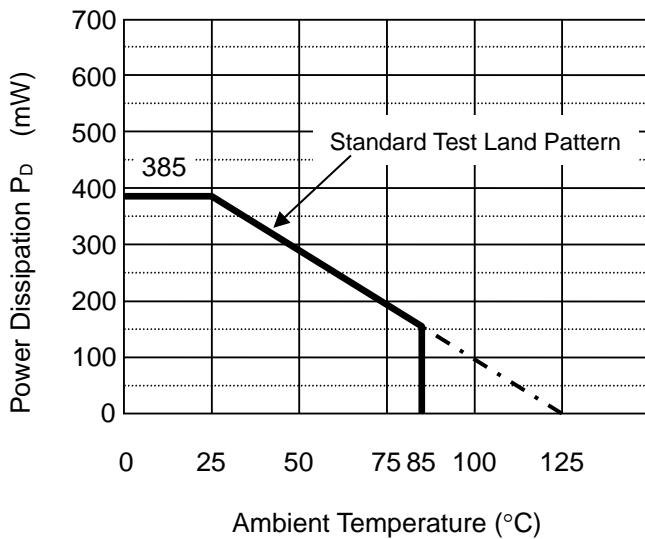
Measurement Conditions

	Standard Test Land Pattern
Environment	Mounting on Board (Wind Velocity = 0 m/s)
Board Material	Glass Cloth Epoxy Plastic (Double-Sided Board)
Board Dimensions	40 mm × 40 mm × 1.6 mm
Copper Ratio	Top Side: Approx. 50% Bottom Side: Approx. 50%
Through-holes	φ 0.3 mm × 26 pcs

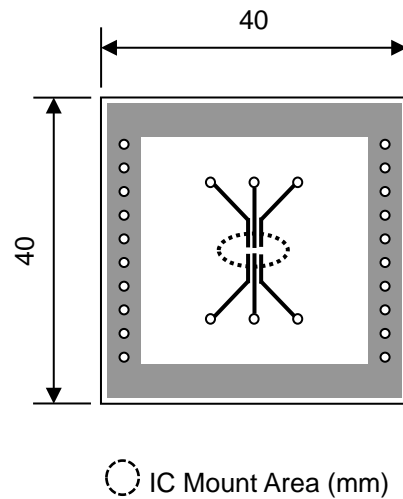
Measurement Result

(Ta = 25°C, Tjmax = 125°C)

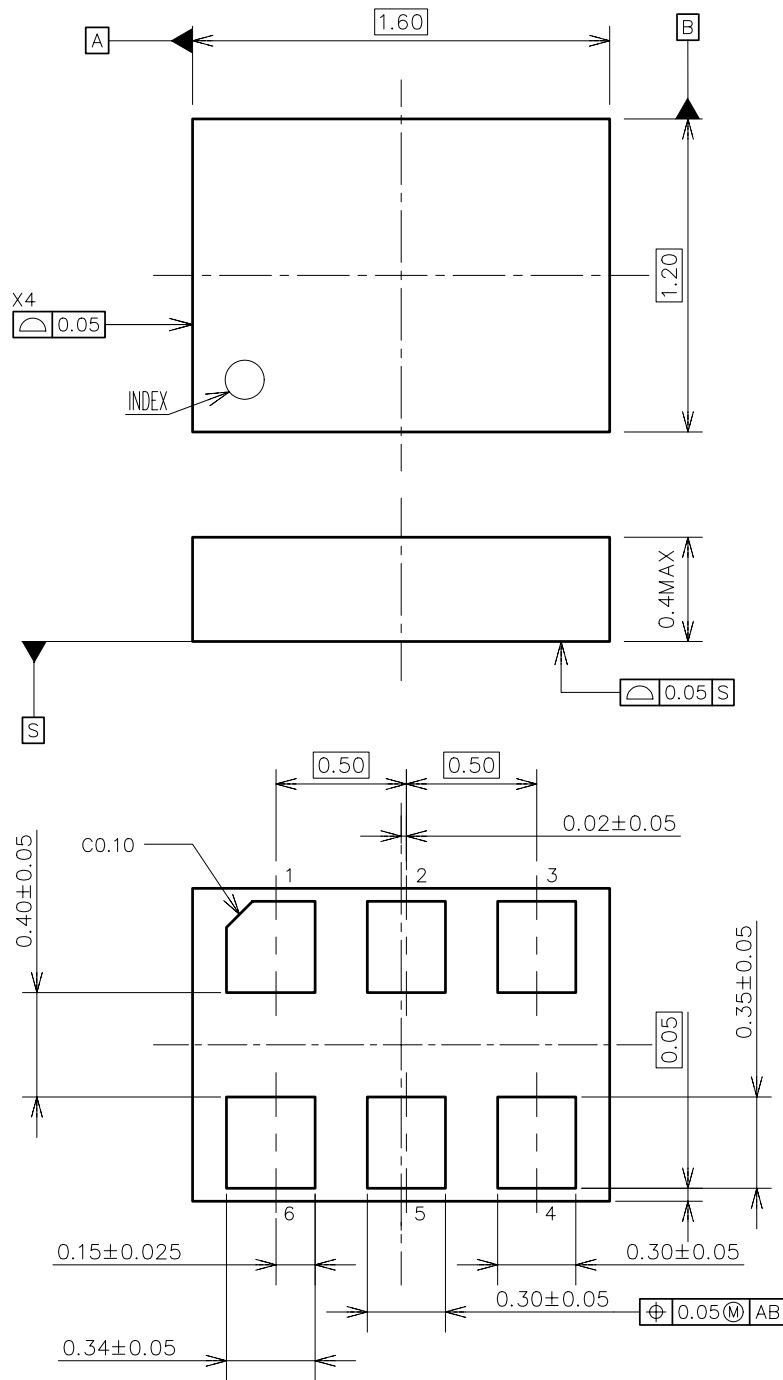
	Standard Test Land Pattern
Power Dissipation	385 mW
Thermal Resistance	$\theta_{ja} = (125 - 25^\circ\text{C}) / 0.385 \text{ W} = 260^\circ\text{C/W}$
	$\theta_{jc} = 30^\circ\text{C/W}$



Power Dissipation vs. Ambient Temperature



Measurement Board Pattern



DFN(PLP)1216-6F Package Dimensions (Unit: mm)

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following conditions are used in this measurement.

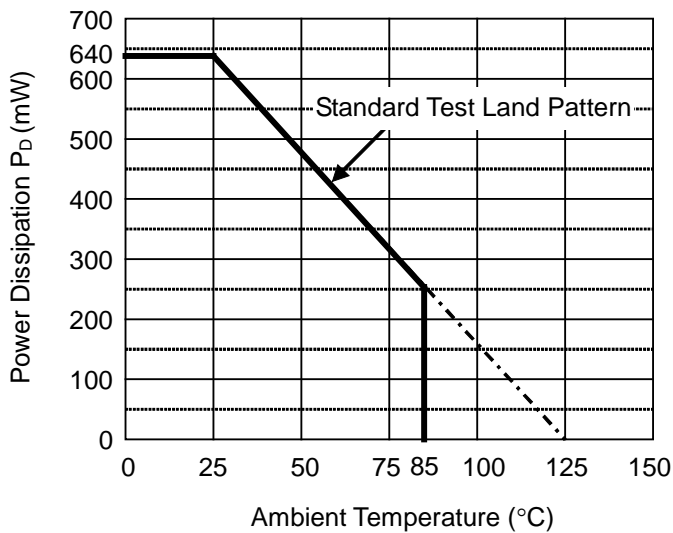
Measurement Conditions

	Standard Test Land Pattern
Environment	Mounting on Board (Wind Velocity = 0 m/s)
Board Material	Glass Cloth Epoxy Plastic (Double-Sided Board)
Board Dimensions	40 mm × 40 mm × 1.6 mm
Copper Ratio	Top Side: Approx. 50% Bottom Side: Approx. 50%
Through-holes	φ 0.5 mm × 32 pcs

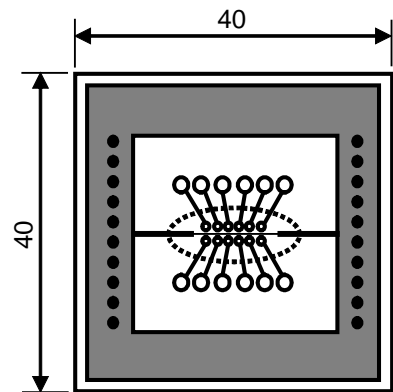
Measurement Result

(Ta = 25°C, Tjmax = 125°C)

	Standard Test Land Pattern
Power Dissipation	640 mW
Thermal Resistance	$\theta_{ja} = (125 - 25^\circ\text{C}) / 0.64 \text{ W} = 156^\circ\text{C/W}$ $\theta_{jc} = 23^\circ\text{C/W}$



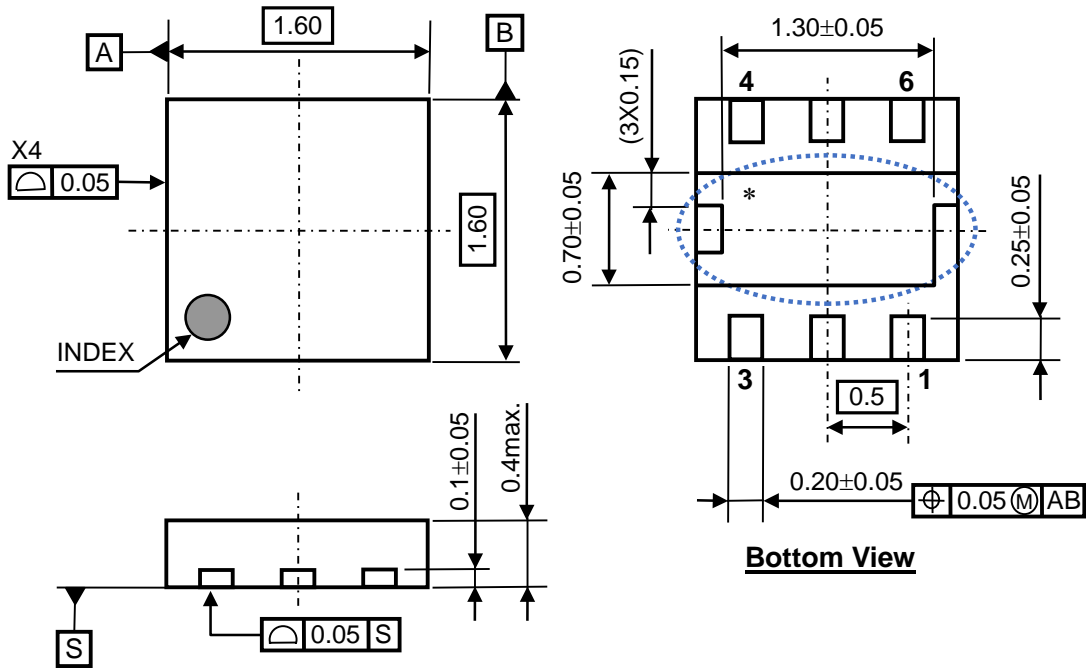
Power Dissipation vs. Ambient Temperature



Measurement Board Pattern

○ IC Mount Area (mm)

Measurement Board Pattern



DFN1616-6B Package Dimensions (Unit: mm)

* The tab on the bottom of the package shown by blue circle is a substrate potential (GND). It is recommended that this tab be connected to the ground plane pin on the board but it is possible to leave the tab floating.

The power dissipation of the package is dependent on PCB material, layout, and environmental conditions. The following conditions are used in this measurement.

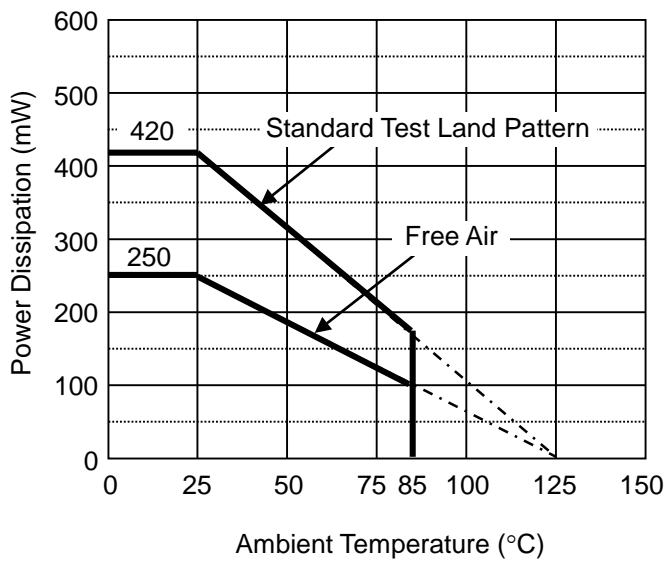
Measurement Conditions

	Standard Test Land Pattern
Environment	Mounting on Board (Wind Velocity = 0 m/s)
Board Material	Glass Cloth Epoxy Plastic (Double-Sided Board)
Board Dimensions	40 mm x 40 mm x 1.6 mm
Copper Ratio	Top Side: Approx. 50% Bottom Side: Approx. 50%
Through-holes	φ 0.5 mm x 44 pcs

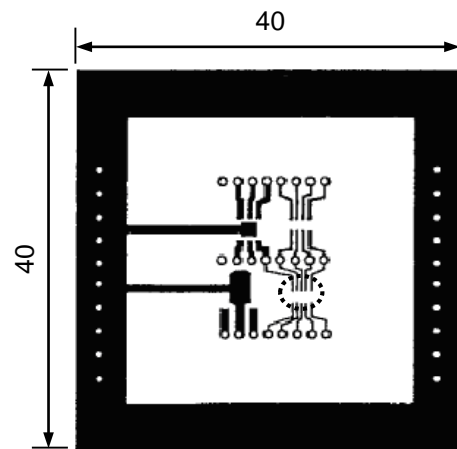
Measurement Result

(Ta = 25°C, Tjmax = 125°C)

	Standard Test Land Pattern	Free Air
Power Dissipation	420 mW	250 mW
Thermal Resistance	$\theta_{ja} = (125 - 25^\circ\text{C}) / 0.42 \text{ W} = 238^\circ\text{C/W}$	400°C/W

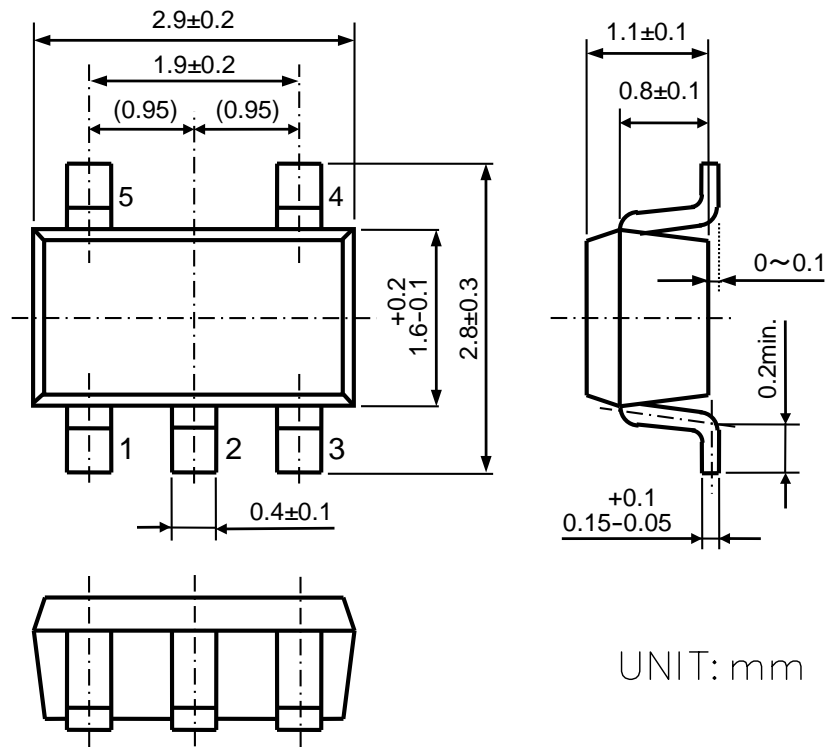


Power Dissipation vs. Ambient Temperature



○ IC Mount Area (mm)

Measurement Board Pattern



UNIT: mm

SOT-23-5 Package Dimensions



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