

Discription

The HSTN061050B101 protects sensitive semiconductor components from damage or upset due to electrostatic discharge (ESD) and other voltage induced transient events. Excellent clamping capability, low leakage, low capacitance, and fast response time provide best in class protection on designs that are exposed to ESD.

It gives designer the flexibility to protect one bi-directional line in applications where arrays are not practical.

Features

- ★ Small Body Outline Dimensions: 0.61 mm x 0.31 mm
- ★ Low Body Height: 0.28 mm
- ★ Low Leakage
- ★ Response Time is Typically < 1 ns
- ★ ESD Rating of Class 3 per Human Body Model
- ★ IEC61000-4-2 Level 4 ESD Protection
- ★ These are Pb-Free Devices
- ★ We declare that the material of product compliance with RoHS requirements and Halogen Free.

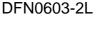
Orderingin formation



Absolute Ratings(Tamb = 25°C)

| Symbol | Parameter | Value | Units |
|------------------|--|-------------|-------|
| P _{PP} | Peak Pulse Power (t _p = 8/20µs) | 90 | W |
| TL | Maximum lead temperature for soldering during 10s | 260 | °C |
| T _{stg} | Storage Temperature Range | -55 to +150 | °C |
| T _{op} | Operating Temperature Range | -40 to +125 | °C |
| Tj | Maximum junction temperature 150 | | °C |
| | IEC61000-4-2 (ESD) air discharge contact discharge | | ΚV |







Circuit Diagram



Electrical Characteristics

| | V _{RWM} | I _R | VB | R | Ι _Τ | I _{PP} | Vc | P _{PK} | С |
|----------------|------------------|----------------|------------------|-----|----------------|-----------------|-----------------------|-----------------|------|
| | (V) | (µA) | (V) | | (mA) | (A) | (V) | (W) | (pF) |
| Device | | @ | @ I _T | | | | @ Max I _{PP} | (8*20 µs) | |
| | | V_{RWM} | (Note 1) | | | | | | |
| | Max | Max | Min | Max | | Max | Max | Max | Тур |
| HSTN061050B101 | 5.0 | 1.0 | 5.7 | 8.0 | 1.0 | 9.0 | 10 | 90 | 15 |

Other voltage available upon request.

- 2. V_{BR} is measured with a pulse test current IT at an ambient temperature of 25 $^\circ\!\!\mathbb{C}$
- 3. Surge current waveform per Figure 3.

Typical Characteristics



Fig1. ESD Clamping Voltage Screenshot Positive 8 kV Contact per IEC61000-4-2



Fig2. ESD Clamping Voltage Screenshot Negative 8 kV Contact per IEC61000-4-2

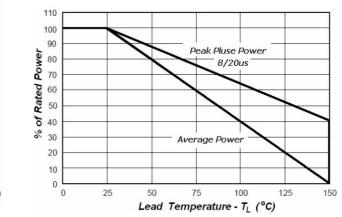


Fig4.Power Derating Curve

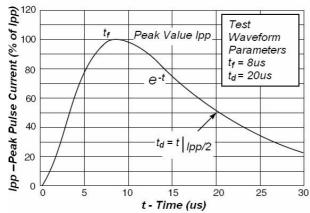
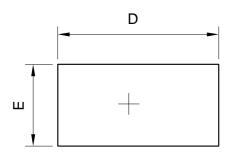
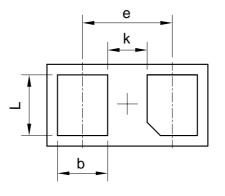


Fig3. Pulse Waveform



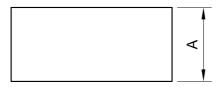
Outline And Dimensions





BOTTOM VVIEW

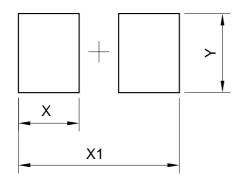
| DFN0603-2L | | | | |
|----------------------|------|------|------|--|
| Dim | Min | Тур. | Max | |
| D | 0.58 | 0.61 | 0.64 | |
| E | 0.28 | 0.31 | 0.34 | |
| е | - | 0.34 | - | |
| L | 0.20 | 0.23 | 0.26 | |
| b | 0.16 | 0.19 | 0.22 | |
| A 0.25 0.28 0.31 | | | | |
| k | 0.12 | 0.15 | 0.18 | |
| All Dimensions in mm | | | | |



SSIDE VIEW

TOP VIEW

Soledering Footprint



| DFN0603-2L | | |
|------------|------|--|
| DIM | (mm) | |
| Х | 0.23 | |
| X1 | 0.61 | |
| Y | 0.30 | |



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