# multicomp

RoHS

Compliant



#### **Description:**

MLCC consists of a conducting material and electrodes. To manufacture a chip-type SMT and achieve miniaturization, high density and high efficiency, ceramic condensers are used. WTC RF series MLCC is used at high frequencies generally have a small temperature coefficient of capacitance, typical within the ±30ppm/°C required for NP0 (C0G) classification and have excellent conductivity internal electrode. Thus, WTC RF series MLCC will be with the feature of low ESR and high Q characteristics.

#### Features:

- High Q and low ESR performance at high frequency.
- Ultra low capacitance to 0.1pF.
- Can offer high precision tolerance to ±0.05pF.
- Quality improvement of telephone calls for low power loss and better performance.

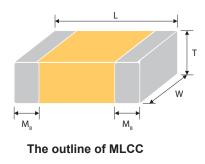
#### Applications: • Telecommunicat

- Telecommunication products & equipments: Mobile phone, WLAN, Base station.
- RF module: Power amplifier, VCO.
- Tuners.

# How To Order:

	15	N	100	J	500	С	Т
MRF	<u>Size</u>	<u>Dielectric</u>	<u>Capacitance</u>	<u>Tolerance</u>	Rated Voltage	<u>Termination</u>	<u>Packaging</u> <u>style</u>
Multicomp RF = Ultra High Q & Low ESR	$\begin{array}{c} 03 = 0201 \\ (0603) \\ 15 = 0402 \\ (1005) \\ 18 = 0603 \\ (1608) \\ 21 = 0805 \\ (2012) \end{array}$	N = NP0	Two significant digits followed by no. of zeros. And R is in place of decimal point. eg.: 0R5 = 0.5pF 1R0 = 1pF 100 = 10 × 10 <sup>o</sup> = 10pF	A = $\pm 0.05 pF$ B = $\pm 0.1 pF$ C = $\pm 0.25 pF$ D = $\pm 0.5 pF$ F = $\pm 1\%$ G = $\pm 2\%$ J = $\pm 5\%$	Two significant digits followed by no. of zeros. And R is in place of decimal point. 6R3 = 6.3V DC 100 = 10V DC 250 = 25V DC 500 = 50V DC 101 = 100V DC 251 = 250V DC 501 = 500V DC	C = Cu/Ni/Sn	T = 7" reeled G = 13" reeled

# **External Dimensions:**



Size Inch (mm)	L (mm)	W (mm)			Remark	Мв (mm)
0201 (0603)	0.6 ±0.03	0.3 ±0.03	0.3 ±0.03	L	#	0.15 ±0.05
0402 (1005)	1 ±0.05	0.5 ±0.05	0.5 ±0.05	N	#	0.25 +0.05/-0.1
0603 (1608)	1.6 ±0.1	0.8 ±0.1	0.8 ±0.07	S	-	0.4 ±0.15
0805 (2012)	2 ±0.2	1.25 ±0.2	0.8 ±0.1	Т	-	0.5 ±0.2

# Reflow soldering only is recommended.





### **General Electrical Data:**

Dielectric	NP0
Size	0201, 0402, 0603, 0805
Capacitance*	0201: 0.1pF to 33pF; 0402: 0.1pF to 100pF; 0603: 0.3pF to 47pF; 0805: 0.3pF to 100pF
Capacitance tolerance	Cap ≤5pF: A (±0.05pF ), B (±0.1pF), C (±0.25pF) 5pF <cap (±0.1pf),="" (±0.25pf),="" (±0.5pf)<br="" <10pf:="" b="" c="" d="">Cap ≥10pF: F (±1%), G (±2%), J (±5%)</cap>
Rated voltage (WVDC)	6.3V, 10V, 25V, 50V, 100V, 250V, 500V
Q*	Cap ≥30pF, Q ≥1000; Cap <30pF,Q ≥400 +20C
Insulation resistance at Ur	≥10GΩ or R × C ≥100Ω-F whichever is smaller.
Operating temperature	-55°C to +125°C
Capacitance change	±30ppm/°C; 0201 Cap ≥22pF, ±60ppm/°C
Termination	Ni/Sn (lead-free termination)

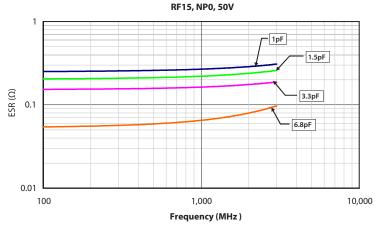
\* Measured at the conditions of 25°C ambient temperature and 30~70% related humidity. Apply 1.0±0.2Vrms, 1.0MHz±10% for Cap≤1000pF and 1.0±0.2Vrms, 1.0kHz±10% for Cap>1000pF.

# Packaging Dimension and Quantity:

Size	Thickness (mm)/S	umbol	Paper tape		
Size	Thickness (mm)/S	ymbol	7" reel	13" reel	
0201 (0603)	0.3 ±0.03	L	15k	70k	
0402 (1005)	0.5 ±0.05	N	10k	50k	
0603 (1608)	0.8 ±0.07	S	4k	15k	
0805 (2012)	0.85±0.1	Т	4k	15k	

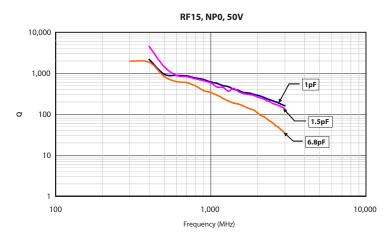
Unit : pieces

# **Electrical Characteristics:**

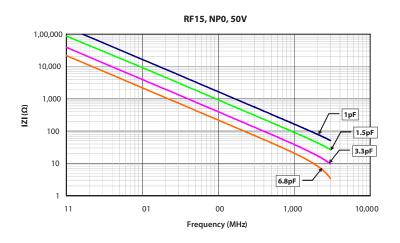


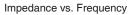
ESR vs. Frequency



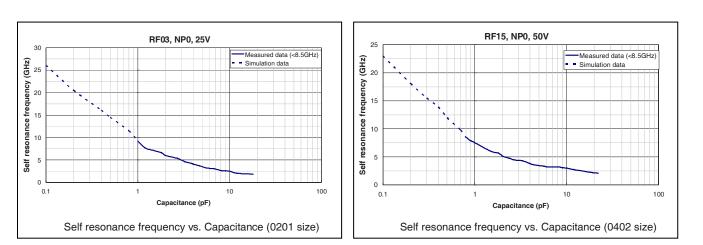


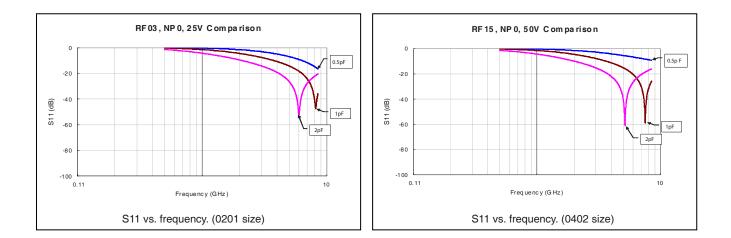


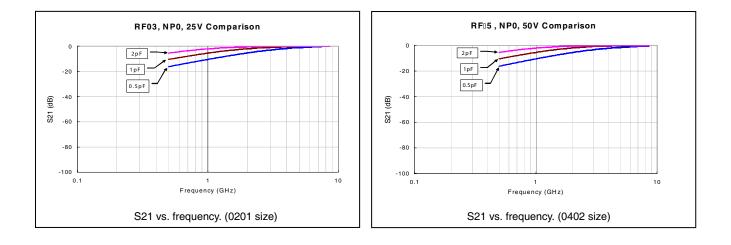












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## **Reliability Test Conditions and Requirements:**

No	ltem	Test Condition	Requirements
1	Visual and Mechanical	-	No remarkable defect. Dimensions to conform to individual specification sheet.
2	Capacitance	1 ±0.2Vrms, 1MHz ±10%	Shall not exceed the limits given in the detailed spec.
3	Q/ D.F. (Dissipation Factor)	At 25°C ambient temperature.	Cap ≥30pF, Q ≥1000; Cap <30pF, Q ≥400 +20C
		To apply voltage: ≤100V, ≥250% of rated voltage. Duration: 1 to 5 sec. Charge and discharge current less than 50mA.	No evidence of damage or flash over during test.
4	Dielectric Strength	To apply voltage: 200V~300V ≥2 times V DC 500V~999V ≥1.5 times V DC 1000V~3000V ≥1.2 times V DC Cut-off, set at 10mA TEST = 15 sec. RAMP = 0	
5	Insulation	≤100V : To apply rated voltage 5. Insu- lation for Max. 120 sec.	≥10GΩ or R × C ≥100Ω -F whichever is smaller
	Resistance	≥200V :To apply rated voltage (500V Max.) for 60 sec.	
6	Temperature Coefficient	With no electrical load. Operating temperature: -55°C ~ 125°C at 25°C	Capacitance change: within ±30ppm/°C; 0201Cap ≥22pF, within ±60ppm/°C
7	Adhesive Strength of Termination	Pressurizing force : 0201: 2N 0402 & 0603: 5N >0603: 10N Test time: 10±1 sec.	No remarkable damage or removal of the terminations.
8	Vibration Resistance	Vibration frequency: 10~55 Hz/min. Total amplitude: 1.5mm Test time: 6 hrs. (Two hrs each in three mutually perpendicular directions.) Measurement to be made after keeping at room temp. for 24 ±2 hrs.	No remarkable damage. Cap change and Q/D.F.: To meet initial spec.
9	Solderability	Solder temperature: 235 ±5°C Dipping time: 2 ±0.5 sec.	95% Min. coverage of all metalized area.



# Microwave Multilayer SMD Ceramic Capacitor 0201 to 0805 Sizes (6.3V to 500V), NPO Dielectric, (MCRF)Series



No	ltem		Test Condition		Requirements
10.	Bending Test	pressu izing ro secono 1 mm a mainta Measu	iddle part of substrate shal irized by means of the pres od at a rate of about 1 mm d until the deflection becom and then the pressure shal nined for $5\pm1$ sec. irrement to be made after ke n temp. for $24\pm2$ hrs.	ssur- per nes I be	No remarkable damage. Cap change: within ±5.0% or ±0.5pF whichever is larger. (This capacitance change means the change of capacitance under specified flexure of substrate from the capacitance measured before the test.)
11	Resistance to Soldering Heat	Solder temperature: 260±5°C Dipping time: 10±1 sec Preheating: 120 to 150°C for 1 min- ute before immerse the capacitor in a eutectic solder. Before initial measurement (Class II only): Perform 150+0/-10°C for 1 hr and then set for 24±2 hrs at room temp. Measurement to be made after keeping at room temp. for 24±2 hrs.		in a s II hr and າp.	No remarkable damage. Cap change: within ±2.5% or ±0.25pF whichever is larger. Q/D.F., I.R. and dielectric strength: To meet initial require- ments. 25% Max. leaching on each edge.
		the ter	ict the five cycles according nperatures and time.	g to Time	
	Temperature Cycle	Step	Temp. (°C)	(min.)	
		1	Min. operating temp. +0/-3	30 ±3	
		2	Room temp.	2~3	No remarkable damage.
12		3	Max. operating temp. +3/-0	30 ±3	Cap change: within $\pm 2.5\%$ or $\pm 0.25pF$ whichever is larger. Q/D.F., I.R. and dielectric strength: To meet initial require-
		4Room temp.2~3Before initial measurement (Class II only): Perform 150 +0/-10°C for 1 hr and then set for 24 ±2 hrs at room temp.Measurement to be made after keeping at room temp. for 24 ±2 hrs.		s II hr n	I ments.
13	Humidity (Damp Heat) Steady State	Test temp.: 40±2°C Humidity: 90% ~ 95% RH Test time: 500+24/-0hrs. Before initial measurement (Class II only): Perform 150+0/-10°C for 1 hr and then set for 24±2 hrs at room temp. Measurement to be made after keeping at room temp. for 24±2 hrs.		hr and p.	No remarkable damage. Cap change: within ±5.0% or ±0.5pF whichever is larger. Q/D.F. value: Cap ≥30pF, Q ≥350; 10pF ≤Cap <30pF, Q ≥275 +2.5C Cap <10pF; Q ≥200 +10C I.R.: ≥1GΩ.
14	Humidity (Damp Heat) Load	at room temp. for 24±2 hrs. Test temp.: 40±2°C Humidity: 90% ~ 95%RH Test time: 500 +24/-0 hrs. To apply voltage : rated voltage Before initial measurement (Class II only): To apply test voltage for 1hr at 40°C and then set for 24 ±2 hrs at room temp. Measurement to be made after keeping at room temp. for 24 ±2 hrs.			No remarkable damage. Cap change: within ±7.5% or ±0.75pF whichever is larger. Q/D.F. value: Cap ≥30pF, Q ≥200; Cap <30pF, Q ≥100 +10/3C I.R.: ≥500MΩ.



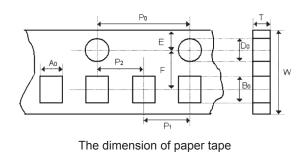
# Microwave Multilayer SMD Ceramic Capacitor 0201 to 0805 Sizes (6.3V to 500V), NPO Dielectric, (MCRF)Series



No	ltem	Test Condition	Requir	ements	
15	High Temperature Load (Endurance)	<ul> <li>Test temp.: 125±3°C</li> <li>* To apply voltage:</li> <li>(1) 10V ≦Ur&lt;500V: 200% of rated voltage.</li> <li>(2) ≦ 6.3V or 500V: 150% of rated voltage.</li> <li>(3) Ur ≧630V: 120% of rated voltage.</li> <li>Test time: 1000+24/-0 hrs.</li> <li>Before initial measurement (Class II only): To apply test voltage for 1hr at test temp. and then set for 24 ±2 hrs at room temp.</li> <li>Measurement to be made after keeping at room temp. for 24 ±2 hrs</li> </ul>	No remarkable damage. Cap change: within ±3.0% or ±0.3pF whichever is larger. Q/D.F. value: Cap ≥30pF, Q ≥350 10pF ≤Cap <30pF, Q ≥275 +2.5C Cap <10pF, Q ≥200 +10C * I.R.: ≥1GΩ.		
16	ESR	The ESR should be measured at room temperature and tested at frequency 1 ±0.1GHz. The ESR should be measured at room temperature and tested at frequency 500 ±50 MHz.	0201 0.1pF ≤Cap ≤1pF: < 350mΩ 1pF <cap 300mω<br="" <="" ≤5pf:="">5pF <cap 250mω<br="" <="" ≤22pf:="">0603 0.3pF ≤Cap ≤1pF: &lt; 1,500mΩ 1pF <cap 250mω<br="" <="" ≤10pf:="">10pF <cap 200mω<br="" <="" ≤10pf:="">0201, 22pF ≤Cap ≤33pF: &lt; 30</cap></cap></cap></cap>	0402 0.1pF ≤Cap ≤1pF: < 350mΩ 1pF <cap 300mω<br="" <="" ≤5pf:="">5pF <cap 250mω<br="" <="" ≤100pf:="">0805 0.3pF≤Cap ≤1pF: &lt; 1,500mΩ 1pF <cap 250mω<br="" <="" ≤10pf:="">Cap &gt;10pF: &lt; 200mΩ</cap></cap></cap>	

## **Appendixes**

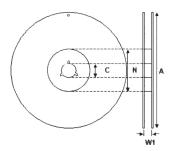
#### Tape & Reel Dimensions



Size	0201	0402	0603	0805
Thickness	L	Ν	S	Т
A <sub>0</sub>	0.37 ±0.03	0.62 ±0.05	1 +0.05/-0.1	1.5 ±0.1
B <sub>0</sub>	0.67 ±0.03	1.12 ±0.05	1.8 ±0.1	2.3 ±0.1
Т	0.42 ±0.03	0.6 ±0.05	0.95 ±0.05	0.95 ±0.05
K <sub>0</sub>	-	-	-	-
W	8 ±0.1	8 ±0.1	8 ±0.1	8 ±0.1
P <sub>0</sub>	4 ±0.1	4 ±0.1	4 ±0.1	4 ±0.1
10 × P <sub>0</sub>	40 ±0.1	40 ±0.1	40 ±0.2	40±0.2
P <sub>1</sub>	2 ±0.05	2 ±0.05	4 ±0.1	4 ±0.1
P <sub>2</sub>	2 ±0.05	2 ±0.05	2 ±0.05	2 ±0.05
D <sub>0</sub>	1.55 ±0.05	1.55 ±0.05	1.55 ±0.05	1.55 ±0.05
D <sub>1</sub>	-	-	-	-
E	1.75 ±0.05	1.75 ±0.05	1.75 ±0.05	1.75 ±0.05
F	3.5 ±0.05	3.5 ±0.05	3.5 ±0.05	3.5 ±0.05





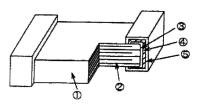


Size	0201, 0402, 0603, 0805			
Reel size	7"	13"		
С	13 +0.5/-0.2	13 +0.5/-0.2		
W <sub>1</sub>	8.4 +1.5/-0	8.4 +1.5/-0		
A	178 ±1	330 ±1		
N	60 +1/-0	100 ±1		

The dimension of reel

#### **Constructions:**

No.	Na	me	NP0
1	Ceramic	material	BaTiO3 based
2	Inner el	ectrode	Cu
3		Inner layer	Cu
4	Termination	Middle layer	Ni
5		Outer layer	Sn (Matt)



The construction of MLCC

\* Partial NP0 items are with Ag/Ni/Sn(NME) terminations, please ref to product range for detail.

#### Storage and handling conditions

- (1) To store products at 5 to 40 C ambient temperature and 20 to 70%. related humidity conditions.
- (2) The product is recommended to be used within one year after shipment. Check solderability in case of shelf life extension is needed.

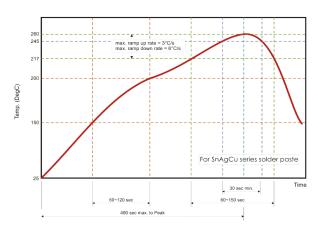
#### Cautions:

- a. The corrosive gas reacts on the terminal electrodes of capacitors, and results in the poor solderability. Do not store the capacitors in the ambience of corrosive gas (e.g., hydrogen sulfide, sulfur dioxide, chlorine, ammonia gas etc.)
- b. In corrosive atmosphere, solderability might be degraded, and silver migration might occur to cause low reliability.
- c. Due to the dewing by rapid humidity change, or the photochemical change of the terminal electrode by direct sunlight, the solderability and electrical performance may deteriorate. Do not store capacitors under direct sunlight or dewing condition. To store products on the shelf and avoid exposure to moisture.

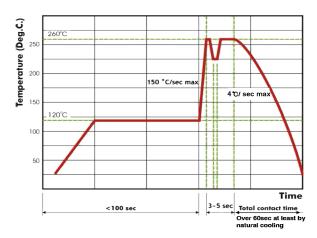


#### **Recommended Soldering Conditions:**

The lead-free termination MLCCs are not only to be used on SMT against lead-free solder paste, but also suitable against lead-containing solder paste. If the optimized solder joint is requested, increasing soldering time, temperature and concentration of  $N_2$  within oven are recommended.



Recommended reflow soldering profile for SMT process with SnAgCu series solder paste.



Recommended wave soldering profile for SMT process with SnAgCu series solder.

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