



COPLANER MATED SIGNAL PATHS
 DC PIN A10-----RAM PIN A10
 DC PIN A1-----RAM PIN A1

SEE SHEET 1 SEC NO: UCP2016-3973 DRAWN BY: DRWINDAVARA CHECKED BY: CHYKOTILO APPROVED BY: APPRCHYKOTILO DATE: 2015/07/29 DATE: 2016/02/22 DATE: 2016/04/20	QUALITY SYMBOLS	GENERAL TOLERANCES (UNLESS SPECIFIED)	DIMENSION STYLE	SCALE	DESIGN UNITS	THIRD ANGLE PROJECTION	
	▽=0	4 PLACES ±--- ±---	MM ONLY	4:1	METRIC	☑	
	▽=0	3 PLACES ±--- ±.005					
	▽=0	2 PLACES ±0.13 ±.010					
		1 PLACE ±0.25 ±---					
		0 PLACE ±--- ±---					
		ANGULAR ±1/2°					
		DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS					
			DRAWN BY: JOUILLES	DATE: 2008/11/24	TITLE: IMPACT 4 PAIR RAM UNGUIDED SIGNAL MODULE SALES DRAWING		
			CHECKED BY: JLAURX	DATE: 2008/11/25			
			APPROVED BY: JLAURX	DATE: 2008/11/26			
			MATERIAL NO. SEE TABLE	DOCUMENT NO. SD-76500-001		SHEET NO. 2 OF 2	
			THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				