

### Description

The NTTFS4821NTAG uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

#### **General Features**

V<sub>DS</sub> = 30V I<sub>D</sub> =60 A

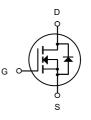
 $R_{DS(ON)} < 8 \, m\Omega$  @ V<sub>GS</sub>=10V

#### Application

Battery protection Load switch Uninterruptible power supply

#### Package Marking and Ordering Information





N-Channel MOSFET

Product ID	Pack	Brand	Qty(PCS)
NTTFS4821NTAG	DFN3X3-8L	HXY MOSFET	5000

#### Absolute Maximum Ratings (Tc=25°C unless otherwise specified)

Symbol	Parameter	Rating	Units	
VDS	Drain-Source Voltage	30	V	
Vgs	Gate-Source Voltage	±20	V	
I <sub>D</sub> @Tc=25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	60	А	
I <sub>D</sub> @Tc=100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	20	А	
I <sub>D</sub> @T <sub>A</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	15	А	
I <sub>D</sub> @T <sub>A</sub> =70°C	Continuous Drain Current, V <sub>GS</sub> @ 10V <sup>1</sup>	12	А	
Ідм	Pulsed Drain Current <sup>2</sup>	140	А	
EAS	Single Pulse Avalanche Energy <sup>3</sup>	115.2	mJ	
las	Avalanche Current	48	А	
P <sub>D</sub> @T <sub>C</sub> =25°C	Total Power Dissipation <sup>4</sup>	59	W	
P <sub>D</sub> @T <sub>A</sub> =25°C	Total Power Dissipation <sup>4</sup>	2	W	
Тѕтс	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range -55 to 150		°C	
Reja	Thermal Resistance Junction-ambient <sup>1</sup> 62		°C/W	
Rejc	Thermal Resistance Junction-Case <sup>1</sup>	2.1	°C/W	



# NTTFS4821NTAG

N-Channel Enhancement Mode MOSFET

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BVDSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V , I <sub>D</sub> =250uA	30			V
∆BVdss/∆Tj	BVDSS Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =1mA		0.027		V/°C
	Statia Ducin Source On	V <sub>GS</sub> =10V , I <sub>D</sub> =20A		6	8	
Rds(on)	Static Drain-Source On- Resistance <sup>2</sup>	V <sub>GS</sub> =4.5V , I <sub>D</sub> =10A		7.5	10	mΩ
VGS(th)	Gate Threshold Voltage		1.2		2.5	V
riangle VGS(th)	V <sub>GS(th)</sub> Temperature Coefficient	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA		-5.8		mV/°C
		V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =25°C			1	
ldss	Drain-Source Leakage Current	V <sub>DS</sub> =24V , V <sub>GS</sub> =0V , T <sub>J</sub> =55°C			5	uA
lgss	Gate-Source Leakage Current	$V_{GS}=\pm 20V$ , $V_{DS}=0V$			±100	nA
gfs	Forward Transconductance	V <sub>DS</sub> =5V , I <sub>D</sub> =30A		43		S
Rg	Gate Resistance	V <sub>DS</sub> =0V,V <sub>GS</sub> =0V, f=1MHz		1.7		Ω
Qg	Total Gate Charge (4.5V)			20		
Qgs	Gate-Source Charge	V <sub>DS</sub> =15V , V <sub>GS</sub> =4.5V , I <sub>D</sub> =15A		7.6		nC
Qgd	Gate-Drain Charge			7.2		
Td(on)	Turn-On Delay Time			7.8		
Tr	Rise Time	V <sub>DD</sub> =15V , V <sub>GS</sub> =10V ,		15		
Td(off)	Turn-Off Delay Time	R <sub>G</sub> =3.3Ω		37.3		ns
T <sub>f</sub>	Fall Time	_I <sub>D</sub> =15A		10.6		
Ciss	Input Capacitance			2295		pF
Coss	Output Capacitance	V <sub>DS</sub> =15V , V <sub>GS</sub> =0V ,		267		
Crss	Reverse Transfer Capacitance	_f=1MHz		210		
ls	Continuous Source Current <sup>1,6</sup>	V <sub>G</sub> =V <sub>D</sub> =0V , Force			40	Α
lsм	Pulsed Source Current <sup>2,6</sup>	Current			140	Α
Vsd	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V , I <sub>S</sub> =1A ,			1	V

## Electrical Characteristics (TJ=25°C, unless otherwise noted)

**Diode Characteristics** 

Note :

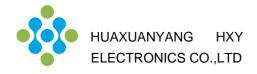
1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width  $\leq 300 \text{us}$  , duty cycle  $\leq 2\%$ 

3 .The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}\text{=}25\text{V}, V_{\text{GS}}\text{=}10\text{V}, \text{L=}0.1\text{mH}, \text{I}_{\text{AS}}\text{=}34\text{A}$ 

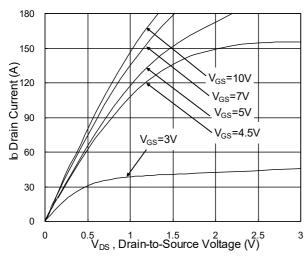
4.The power dissipation is limited by 150  $^\circ\text{C}$  junction temperature

5 .The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.



## NTTFS4821NTAG N-Channel Enhancement Mode MOSFET

### **Typical Characteristics**



**Fig.1 Typical Output Characteristics** 

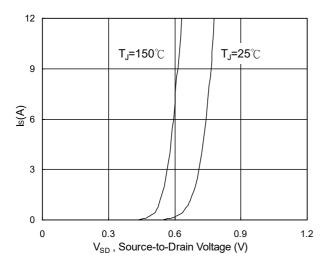


Fig.3 Forward Characteristics of Reverse

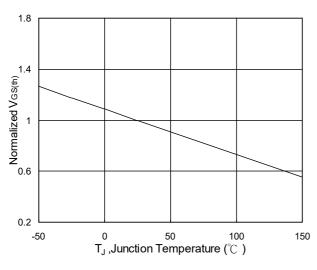


Fig.5 Normalized  $V_{\text{GS}(\text{th})}$  vs.  $T_{\text{J}}$ 

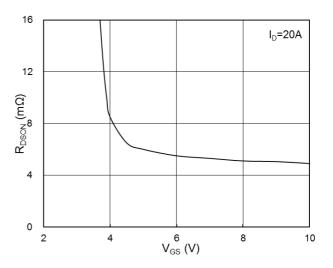


Fig.2 On-Resistance vs. G-S Voltage

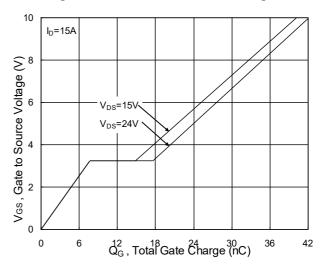


Fig.4 Gate-Charge Characteristics

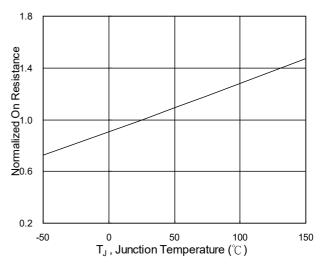
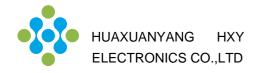
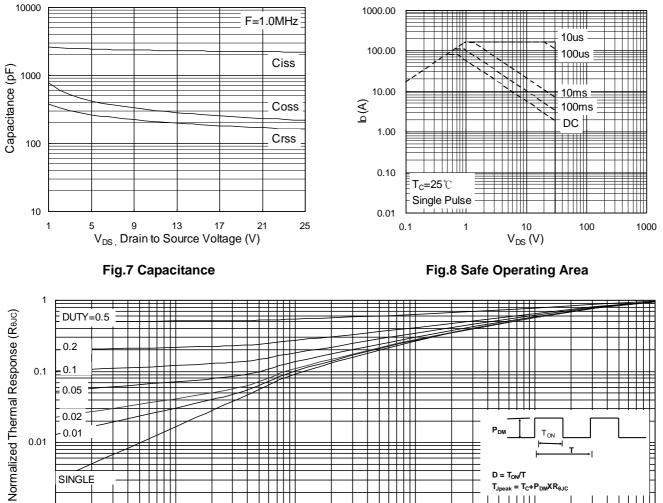


Fig.6 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>



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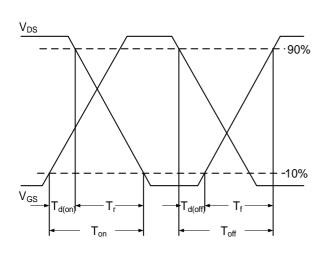


Fig.10 Switching Time Waveform

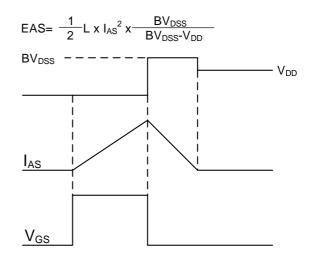
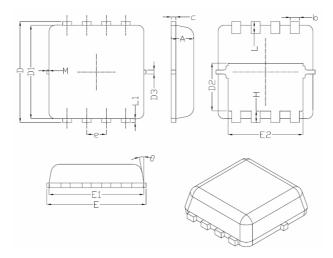


Fig.11 Unclamped Inductive Switching Waveform



## DFN3X3-8L Package Information



Symphol	Dimensions In Millimeters			
Symbol	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
е	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
М	*	*	0.15	
θ		10 <sup>°</sup>	12 <sup>°</sup>	



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