



BMF80N250C1

N-Channel Power MOSFET

800V, 18A, 250mΩ

Description

BMF80N250C1 is power MOSFET using bestirpower's advanced super junction technology that can realize very low on-resistance and gate charge.

It will provide much high efficiency by using optimized charge coupling technology. These user friendly devices give an advantage of Low EMI to designers as well as low switching loss.

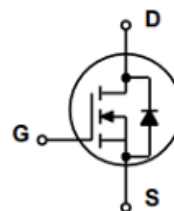
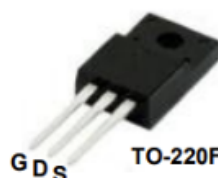
Applications

- PC power
- Telecom / Server power supply
- LED lighting
- EV charger
- Solar / UPS

Features

$V_{DS}@T_{J,max}$	I_D	$R_{DS(on),max}$	$Q_{g,typ}$
850V	18 A	250 mΩ	27nC

- Ultra-fast body diode
- Extremely low losses due to very low FOM
- Very high commutation ruggedness
- Halogen Free, and RoHS Compliant



Absolute Maximum Ratings ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Value	Unit
V_{DSS}	Drain to Source Voltage ¹⁾	800	V
V_{GSS}	Gate to Source Voltage	± 30	V
I_D	Drain Current ²⁾	Continuous ($T_C = 25^\circ\text{C}$)	18
		Continuous ($T_C = 125^\circ\text{C}$)	8
I_{DM}	Drain Current	Pulsed	54
E_{AS}	Single Pulsed Avalanche Energy ³⁾	650	mJ
I_{AR}	Repetitive Avalanche Energy	5.1	A
dv/dt	MOSFET dv/dt ruggedness	50	V/ns
	Diode Recovery dv/dt ruggedness ⁴⁾	50	
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	34	W
T_J, T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_L	Maximum Lead Temperature for Soldering, 1/8" from Case for 10 Seconds	260	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta C}$	Thermal Resistance, Junction to Case, Max.	3.67	$^\circ\text{C/W}$
$R_{\theta A}$	Thermal Resistance, Junction to Ambient, Max. *minimal footprint	62.5	

1) Limited by T_j max. Maximum duty cycle $D=0.75$.

2) Pulse width t_p limited by T_j, max .

3) $V_{DD}=50\text{V}$, $R_G=25\Omega$, Starting $T_j=25^\circ\text{C}$.

4) $V_{DClink}=400\text{V}$; $V_{DS,peak} < V_{(BR)DSS}$; identical low side and high side switch with identical R_G .

Package Marking and Ordering Information

Part Number	Top Marking	Package	Packing Method	Quantity
BMF80N250C1	BMF80N250C1	TO220F	Tube	50 units

Electrical Characteristics (T_C = 25°C unless otherwise noted)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
Off Characteristics						
BV _{DSS}	Drain to Source Breakdown Voltage	V _{GS} = 0V, I _D = 250μA	800	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 800V, V _{GS} = 0V, T _J = 25°C	-	-	10	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ±30V, V _{DS} = 0V	-	-	± 100	nA

On Characteristics

V _{GS(th)}	Gate Threshold Voltage	V _{GS} = V _{DS} , I _D = 250μA	2.0	3.0	4.0	V
R _{DS(on)}	Static Drain to Source On Resistance	V _{GS} = 10V, I _D = 8.5A T _J = 25° C	-	220	250	mΩ

Dynamic Characteristics

C _{iss}	Input Capacitance	V _{GS} = 0V, V _{DS} = 100V, f = 250KHz	-	1510	-	pF
C _{oss}	Output Capacitance		-	58	-	pF
C _{rss}	Reverse Transfer Capacitance		-	2	-	pF
Q _g	Total Gate Charge	V _{GS} = 0-10V, V _{DD} = 400V, I _D = 9A	-	27	-	nC
Q _{gs}	Gate to Source Charge		-	5.5	-	nC
Q _{gd}	Gate to Drain "Miller" Charge		-	8	-	nC
V _{plateau}	Gate plateau voltage		-	3.8	-	V
R _G	Gate Resistance	V _{DD} = 0V, V _{GS} = 0V, f = 1.0MHz	-	15	-	Ω

Switching Characteristics

t _{d(on)}	Turn-On Delay Time	V _{GS} = 10V, V _{DD} = 400V, I _D = 9A	-	13	-	ns
t _r	Turn-On Rise Time		-	2	-	ns
t _{d(off)}	Turn-Off Delay Time		-	80	-	ns
t _f	Turn-Off Fall Time		-	7	-	ns

Reverse Diode Characteristics

I _{SD}	Continuous Diode Forward Current		-	-	18	A
V _{SD}	Diode Forward Voltage	I _F = 8.5A, V _{GS} = 0V, T _J = 25°C	-	0.8	-	V
t _{rr}	Reverse Recovery Time	V _R = 400V, I _F = 9A, di _F /dt = 100A/μs	-	255	-	ns
Q _{rr}	Reverse Recovery Charge		-	3.1	-	μC
I _{rm}	Reverse Recovery Current		-	20	-	A

Figure 1. Power dissipation

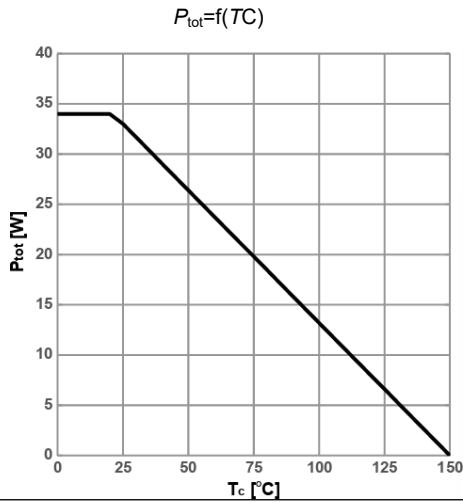


Figure 2. Max. transient thermal impedance

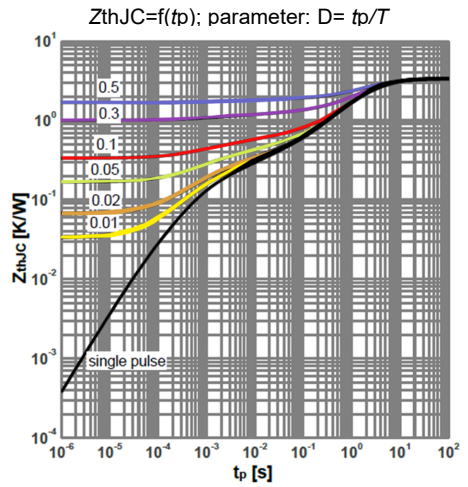


Figure 3: Safe operating area

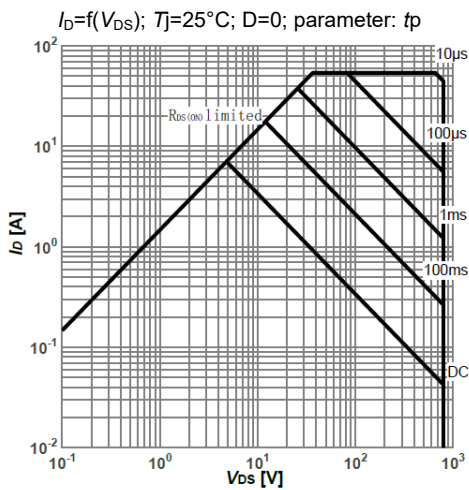


Figure 4: Typ. output characteristics

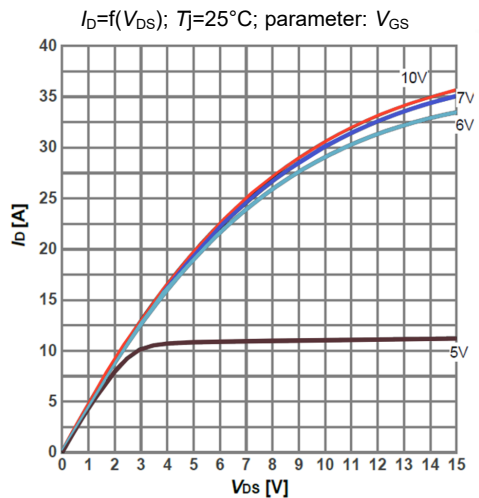


Figure 5:Typ. output characteristics

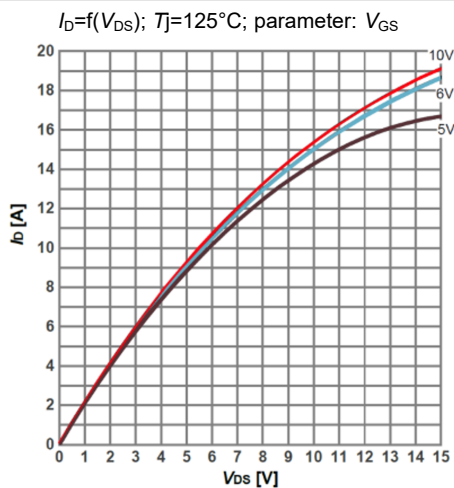


Figure 6: Typ. drain-source on-state resistance

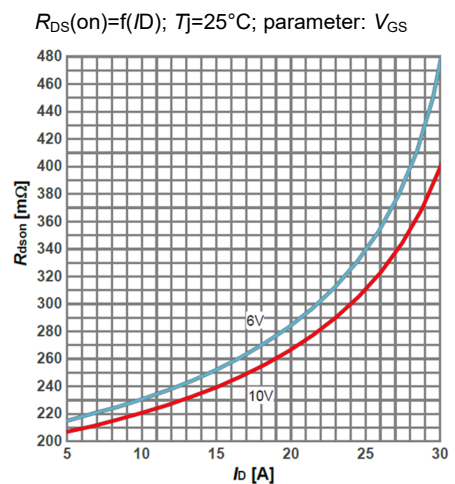


Figure 7: drain-source on-state resistance

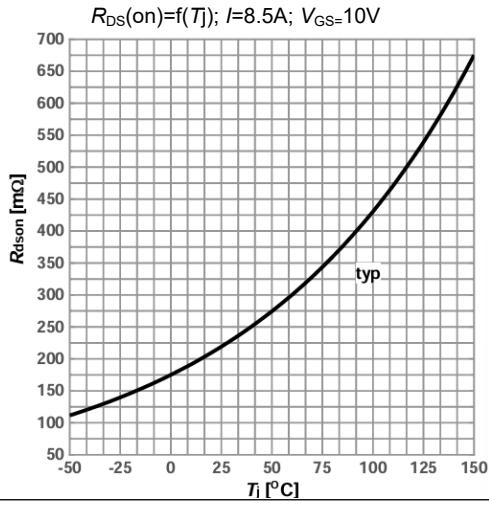


Figure 8: Typ. transfer characteristics

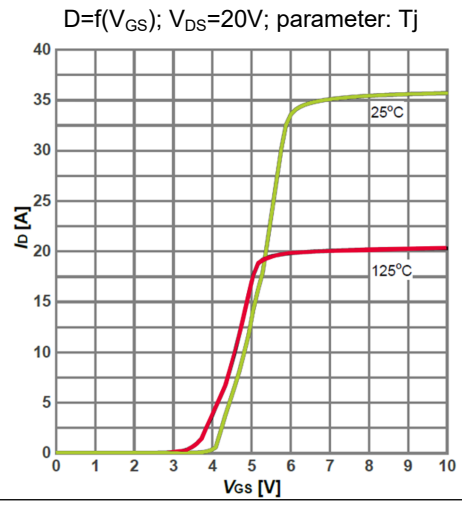


Figure 9:Typ. gate charge

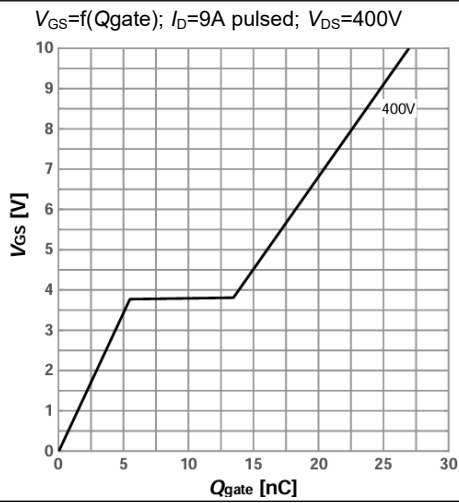


Figure10:Forward characteristics of reverse diode

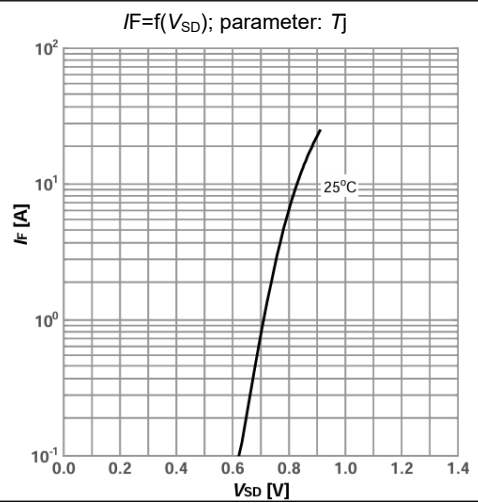


Figure11:Drain-source breakdown voltage

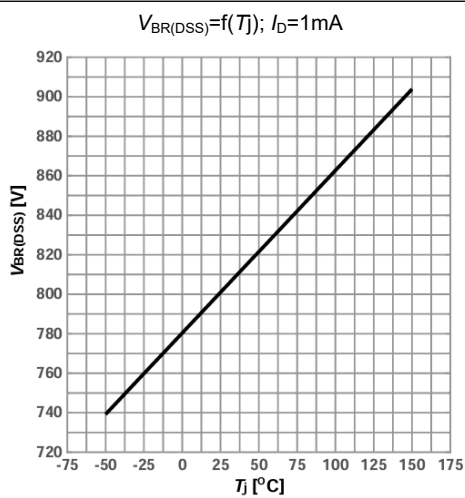


Figure12:Maximum Drain Current

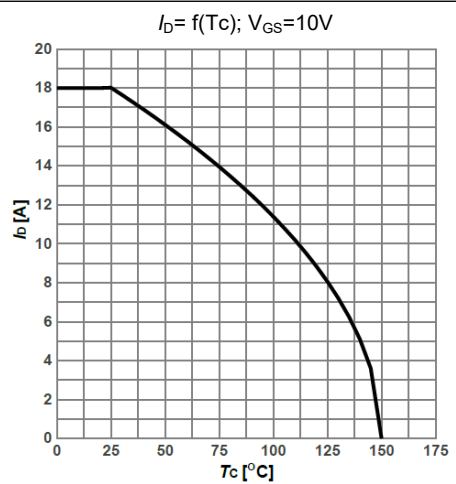


Figure13:Typ. capacitances

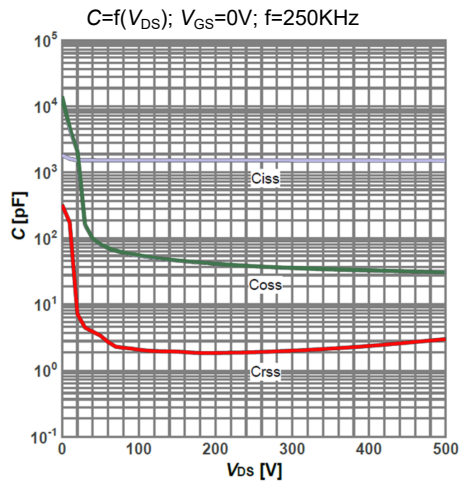
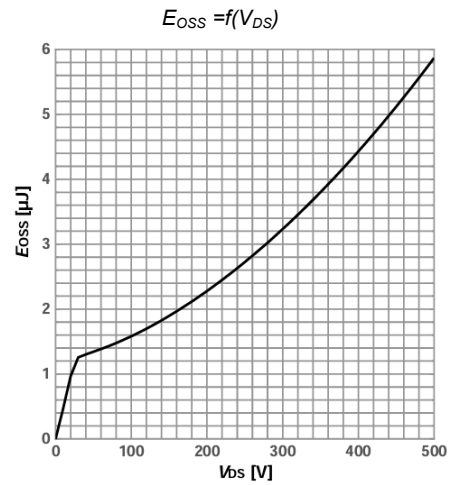


Figure14:Typ. Coss stored energy



Test Circuits

Figure15:Diode Characteristics

Test circuit for diode characteristics and Diode recovery waveform

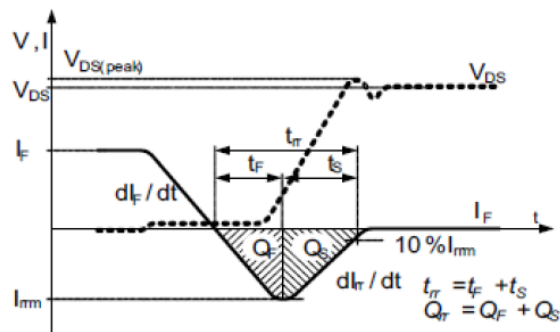
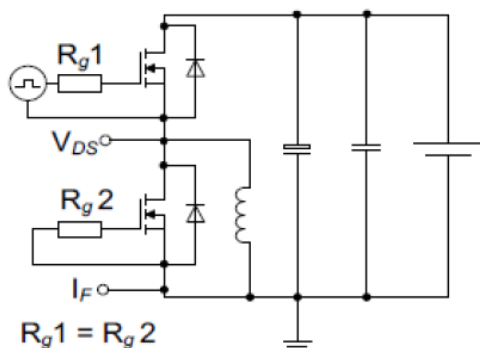


Figure16:Switching Times

Switching times test circuit for inductive load and Switching times waveform

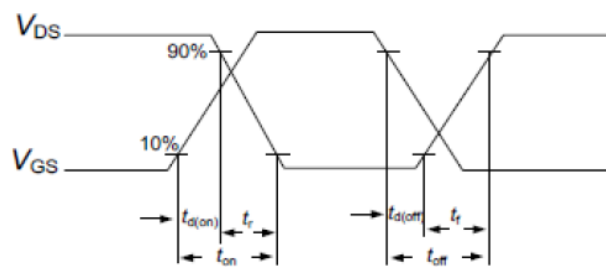
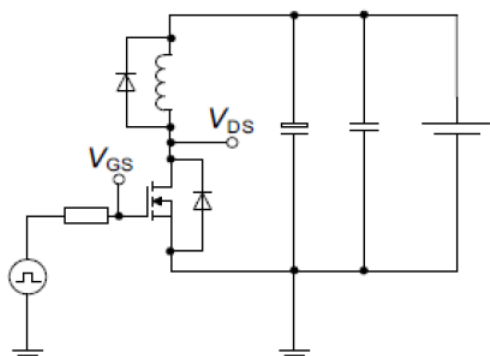
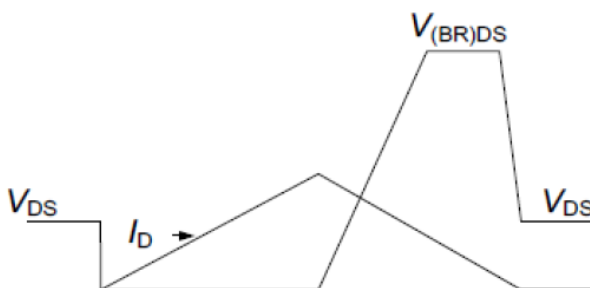
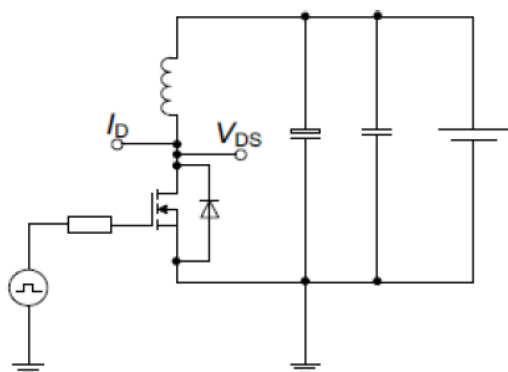


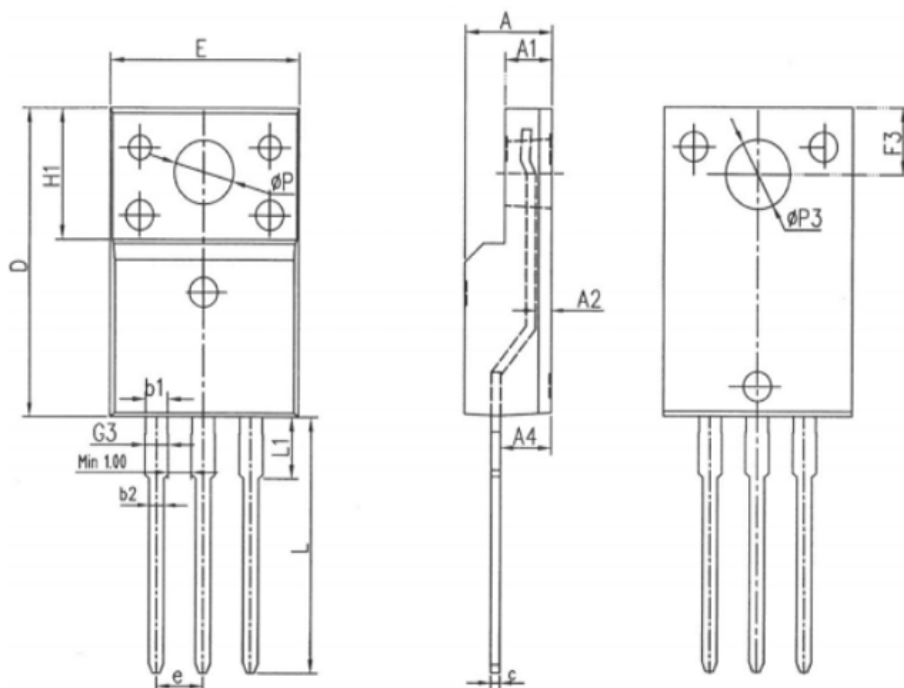
Figure17:Unclamped Inductive Load

Unclamped inductive load test circuit and Unclamped inductive waveform



Package Outlines

TO-220F



SYMBOL	MM		
	MIN	NOM	MAX
E	10.00	10.20	10.40
A	4.50	4.70	4.90
A1	2.34	2.54	2.74
A2	0.65	0.85	1.30
A4	2.55	2.75	2.95
c	0.40	0.50	0.65
D	15.57	15.87	16.17
H1	6.70REF		
e	2.54BSC		
ΦP	3.183REF		
L	12.68	12.98	13.28
L1	3.25	3.45	3.65
ΦP3	3.45REF		
F3	3.10	3.30	3.50
G3	1.10	1.30	1.50
b1	1.05	1.20	1.35
b2	0.70	0.80	0.92

* Dimensions in millimeters

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