

# **Product Specification**

# **XBLW** AO3404

N-Channel Enhancement Mode MOSFET











# **Description**

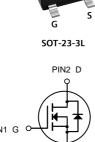
The AO3404 uses advanced trench technology to provide excellent RDS(ON), low gate charge and operation with gate voltages as low as 4.5V. This device is suitable for use as a Battery protection or in other Switching application.

### **General Features**

- ➤ VDS = 30V ID =5A
- $\triangleright$  RDS(ON) < 28m $\Omega$ @ VGS=10V

# **Application**

- Battery protection
- Load switch
- Uninterruptible power supply



#### N-Channel MOSFET

# **Package Marking and Ordering Information**

Product Model	Package Type	Marking	Packing	Packing Qty
XBLW AO3404	SOT-23-3L	X4HV	Tape	3000Pcs/Reel

# Absolute Maximum Ratings (TA=25°C unless otherwise noted)

symbol	parameter		unit
V <sub>DS</sub>	Drain-source voltage		V
V <sub>G</sub> s	Gate-source voltage	±20	V
lo	Drain current-continuousª@Tj=125℃		А
IDM	-pulse <i>d</i> <sup>b</sup>	20	А
ls	Drain-source Diode forward current		А
P <sub>D</sub>	Maximum power dissipation		W
Tj	Operating junction Temperature range		°C
Rth JA	Thermal Resistance junction-to ambient		°C/W



# **Electrical Characteristics (TA=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Drain-source breakdown voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	-	-	V
Zero gate voltage drain current	IDSS	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V	-	-	1	μΑ
Gate-body leakage	IGSS	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V	-	-	±100	nA
Gate threshold voltage	VGS(th)	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	0.8	1.4	2.2	V
		V <sub>GS</sub> =10V, I <sub>D</sub> =5A	-	24	28	
Drain-source on-state resistance	RDS(ON)	V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A		26	32	mΩ
Forward transconductance	gfs	V <sub>GS</sub> =5V, I <sub>D</sub> =5A	-	33	-	S
Input capacitance	Ciss	V <sub>DS</sub> =15V ,V <sub>GS</sub> =0V		255		
Output capacitance	coss	f=1.0MHz		45		pF
Reverse transfer capacitance	CRSS	_		35		
Turn-on delay time	tD(ON)		-	4.5	-	
Rise time	tr	V <sub>DS</sub> =15V V <sub>GS</sub> =10V	-	2.5	-	
Turn-off delay time	tD(OFF)	- R <sub>L</sub> =2.6 ohm R <sub>GEN</sub> =3ohm	-	14.5	-	ns
Fall time	tf		-	3.5	-	
Total gate charge	Qg		-	5.2	-	
Gate-source charge	Qgs	V <sub>DS</sub> =15V,I <sub>D</sub> =5.8A - V <sub>GS</sub> =10V		0.85	-	nC
Gate-drain charge	Qgd	VGS-10V	-	1.3	-	
Diode forward voltage	V <sub>SD</sub>	V <sub>GS</sub> =0V,Is=1A	-	0.76	1.16	V

#### Notes:

- 1、surface mounted on FR4 board,t≤10sec
- 2 pulse test: pulse width≤300µs,duty≤2%
- 3. guaranteed by design, not subject to production testing

1.0

V<sub>SD</sub> (Volts)

1.2

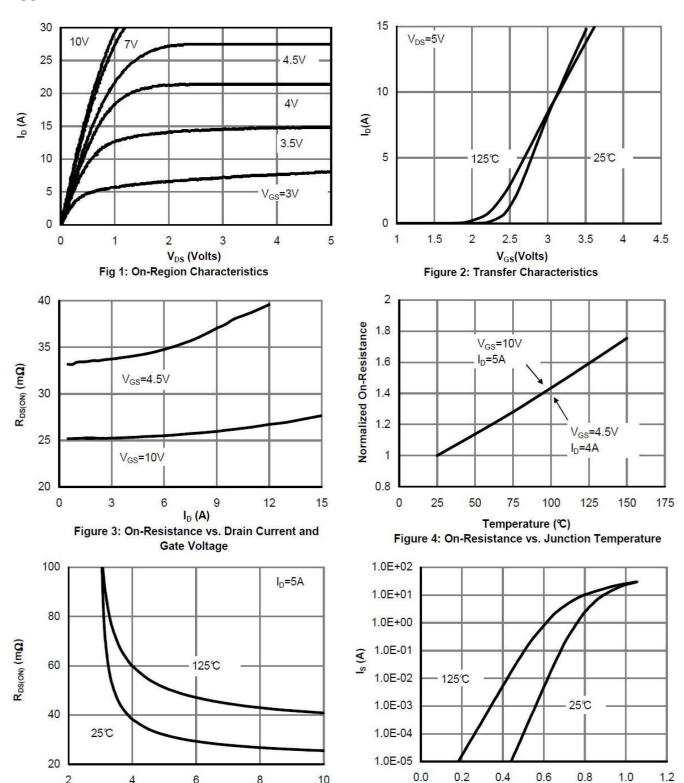


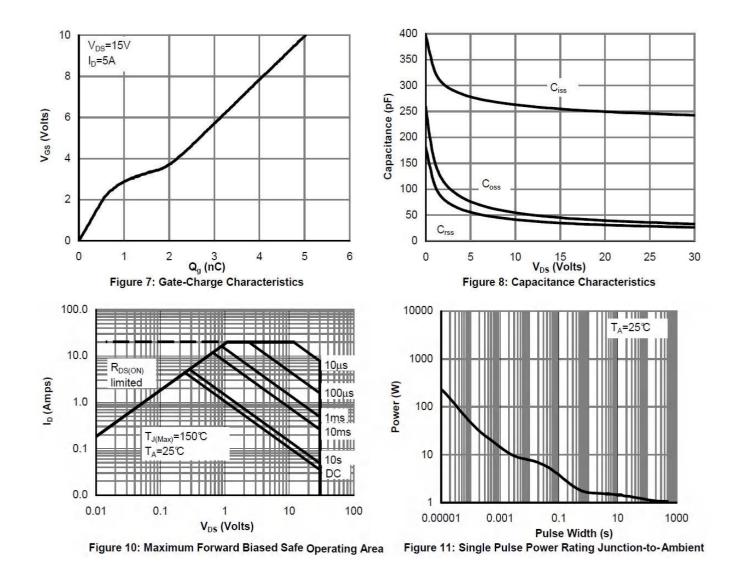
2

4

V<sub>GS</sub> (Volts)

# **Typical Performance Characteristics**





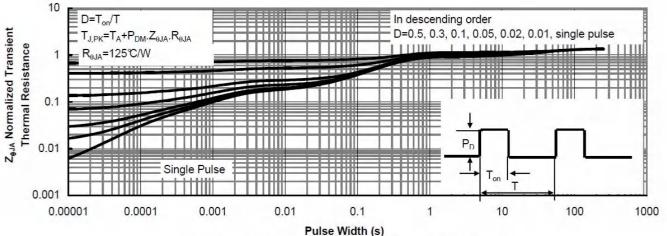
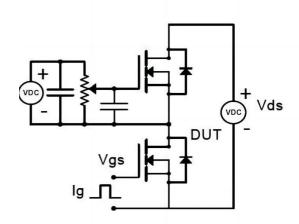
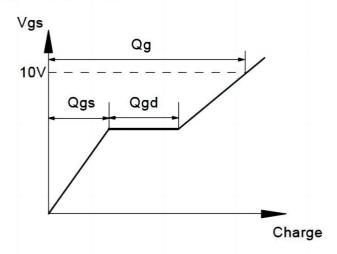


Figure 12: Normalized Maximum Transient Thermal Impedance



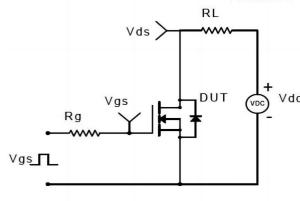
# Gate Charge Test Circuit & Waveform

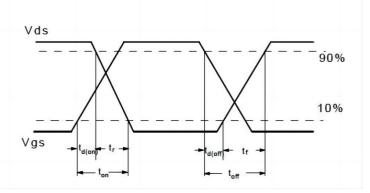




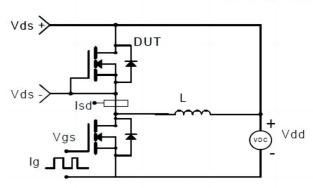
### Resistive Switching Test Circuit & Waveforms

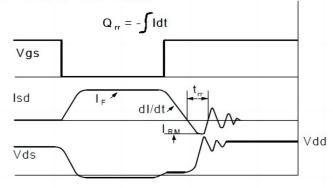
### Resistive Switching Test Circuit & Waveforms





## Diode Recovery Test Circuit & Waveforms

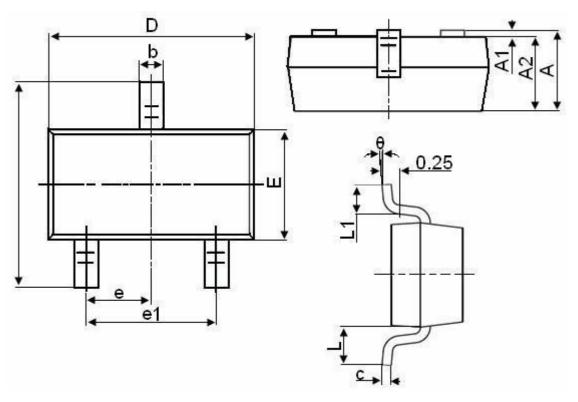






# **Package Information**

# SOT23-3L



Symbol	Dimensions in Millimeters		
	MIN.	MAX.	
Α	1.050	1.250	
A1	0.000	0.100	
A2	1.050	1.150	
b	0.300	0.500	
С	0.100	0.200	
D	2.800	3.000	
E	1.500	1.700	
E1	2.650	2.950	
е		0.950TYP	
e1	1.800	2.000	
L		0.550REF	
L1	0.300	0.600	
θ	0°	8°	



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