



Product Description

GRF2013 is a broadband gain block with low noise figure and high linearity designed for small cell, wireless infrastructure and other high performance applications. It exhibits outstanding broadband NF, linearity and return losses over 0.7 to 3.8 GHz with a single match.

Optimizing the bias inductor and coupling caps for lower frequency operation will yield strong performance down to 50 MHz. For applications above 4 GHz, the addition of simple external matching yields outstanding linearity and gain performance up to 8 GHz.

The device is operated from a supply voltage of 1.8 to 5.0V with a selectable I_{ddq} range of 10 to 120 mA for optimal efficiency and linearity.

Consult with the GRF applications engineering team for custom tuning/evaluation board data. De-embedded s-parameters are available on the website landing page for the device.

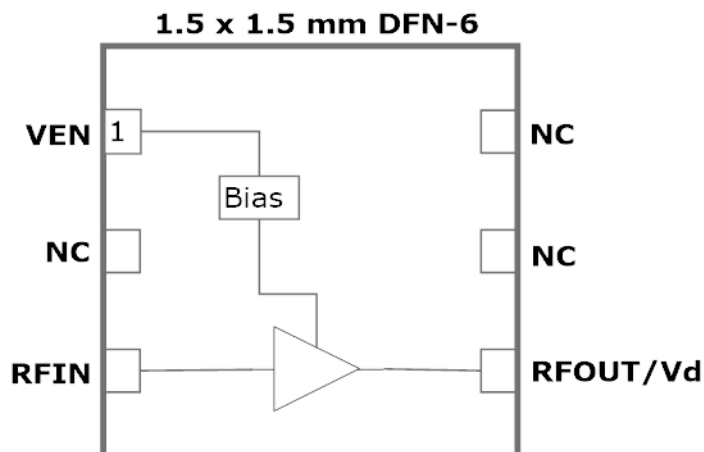
Features

Reference: 5V/90mA/1.9 GHz

- Gain: 18.5 dB
- OIP3: 38.5 dBm
- OP1dB: 22.5 dBm
- NF: 1.3 dB
- High Linearity
- Low Noise Figure
- Flat Gain
- Flexible Biasing
- Internally Matched to 50 Ω
- Process: GaAs pHEMT

Applications

- Linear Driver Amplifier
- Small Cells and Cellular Repeaters
- IF Amplifier
- Wireless Backhaul
- 802.11ac



Absolute Ratings:

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage	V _{DD}	0	6.0	V
RF Input Power: (Load VSWR < 2:1; V _D : 5.0 volts)	P _{IN MAX}		20	dBm
Operating Temperature (Package Heat Sink)	T _{AMB}	-40	105	°C
Maximum Channel Temperature (MTTF > 10 ⁶ Hours)	T _{MAX}		170	°C
Maximum Dissipated Power	P _{DISS MAX}		1.5	W
Electrostatic Discharge:				
Charged Device Model:	CDM	1500		V
Human Body Model:	HBM	250		V
Storage:				
Storage Temperature	T _{STG}	-65	150	°C
Moisture Sensitivity Level	MSL		1	--



Caution! ESD Sensitive Device

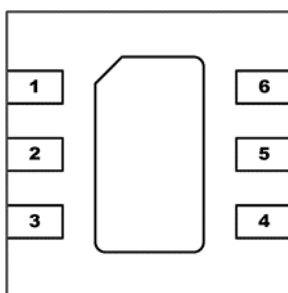


Exceeding Absolute Maximum Rating conditions may cause permanent damage to the device.

Note: For package dimensions and manufacturing information, see the Guerrilla-RF.com website for the following document located on the GRF2013 landing page: **Manufacturing Note—MN-001 Product Tape and Reel, Solderability and Package Outline Specification:**

[Link to manufacturing note](#)

Pin Out (Top View)



Pin Assignments:

Pin	Name	Description	Note
1	V_{ENABLE}	Enable Voltage Input	V _{ENABLE} and series resistor set I _{DDQ} . V _{ENABLE} < 0.2 volts disables device. On-die pull-down resistor will turn the part off if this node is allowed to float.
2	NC	No Connect or Ground	No internal connection to die
3	RF_In	LNA RF input	Internally matched 50Ω. An external DC blocking cap must be used.
4	RF_Out	LNA RF output	Internally matched 50Ω. V _{DD} must be applied through a choke to this pin
5	NC	No Connect or Ground	No internal connection to die
6	NC	No Connect or Ground	No internal connection to die
PKG BASE	GND	Ground	Provides DC and RF ground for LNA, as well as thermal heat sink. Recommend multiple 8 mil vias beneath the package for optimal RF and thermal performance. Refer to evaluation board top layer graphic on schematic page.



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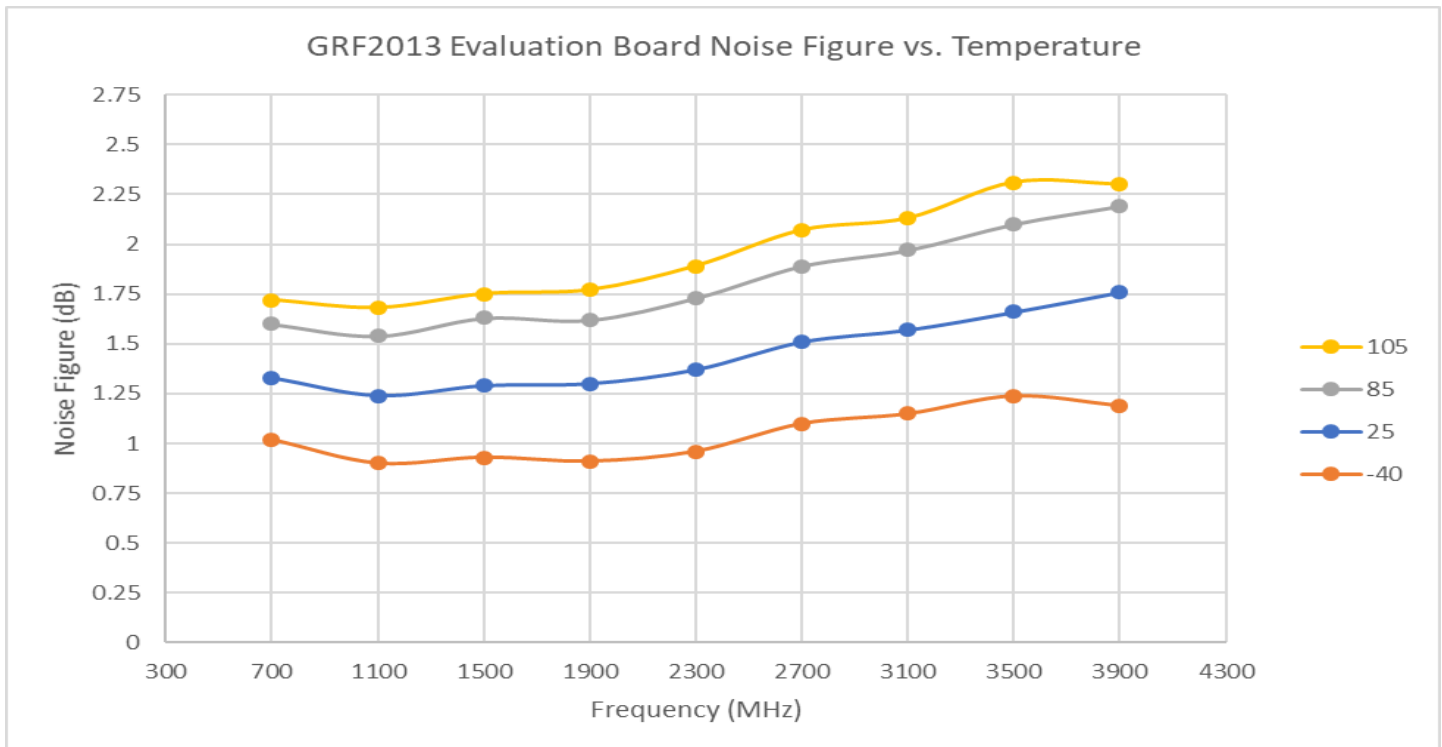
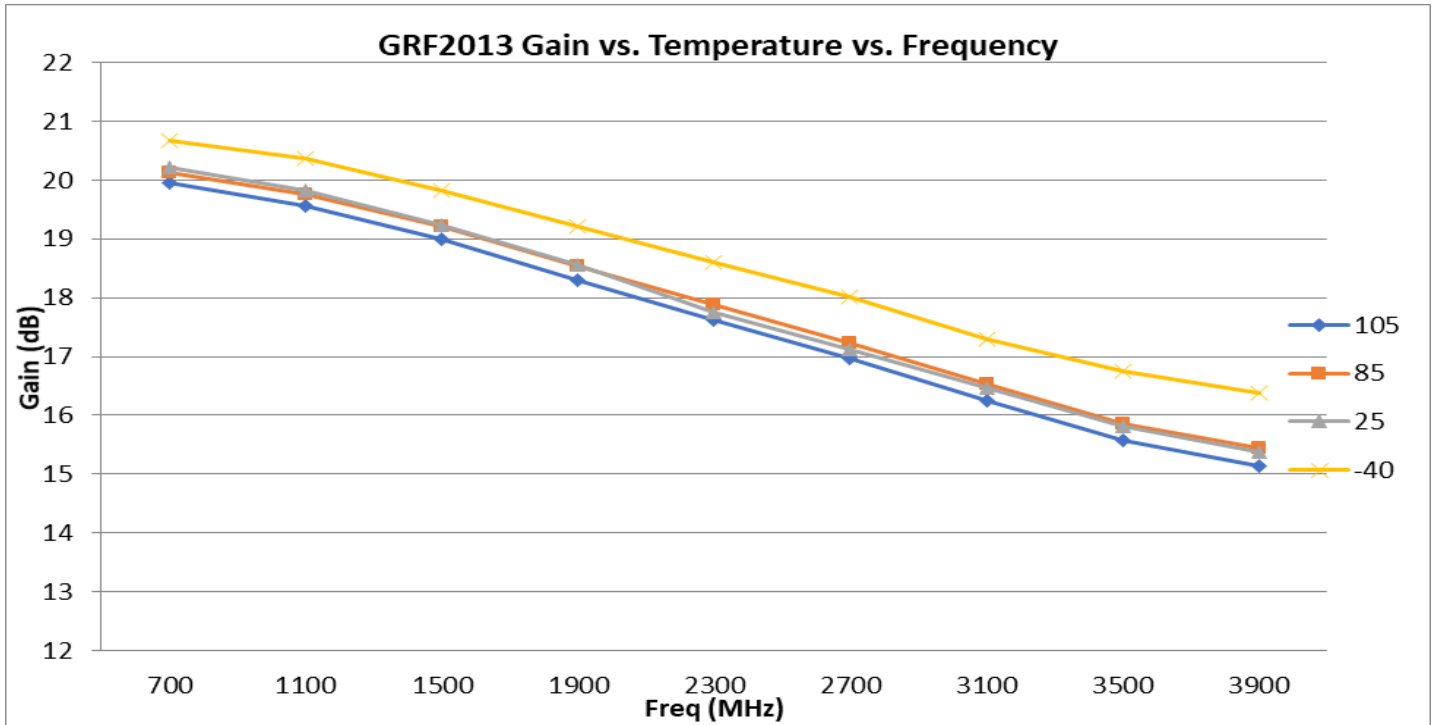
GRF2013

Broadband Linear Gain Block
0.05 to 8.0 GHz

Nominal Operating Parameters:

Parameter	Symbol	Specification			Unit	Condition
		Min.	Typ.	Max.		
Test Frequency	F_{TEST}		1.9		GHz	$V_{DD} = 5.0\text{ V}$, $T_A = 25^\circ\text{C}$
Gain	S21	17.5	18.5		dB	
Evaluation Board Noise Figure	NF		1.3		dB	
Output 3rd Order Intercept	OIP3		38.5		dBm	4.0 dBm P_{OUT} per tone (1899 and 1901 MHz)
Output 1dB Compression Power	OP1dB	21.0	22.5		dBm	
Switching Rise Time	T_{RISE}		500		ns	
Switching Fall Time	T_{FALL}		500		ns	
Supply Current	I_{DD}		90.0		mA	$V_{DD}=V_{ENABLE}: 5.0\text{V}$; $R_{bias}: 500\Omega$
Enable Current	I_{ENABLE}		5.0		mA	
Disabled Mode						
Leakage Current	$I_{LEAKAGE}$		100		uA	$V_{DD}: 5.0\text{V}$; $V_{ENABLE}: 0.0\text{V}$
Thermal Data						
Thermal Resistance: (Infra-Red Scan)	θ_{JC}		52		$^\circ\text{C}/\text{W}$	On standard Evaluation Board
Channel Temperature @ +85 C Reference (Package heat sink)	$T_{CHANNEL}$		109		$^\circ\text{C}$	$V_{DD}: 5.0\text{ V}$; $I_{DDQ}: 90\text{ mA}$; No RF; $P_{DISS}: 450\text{ mW}$

GRF2013 Evaluation Board Measured Data: (0.7 to 3.8 GHz Tune)



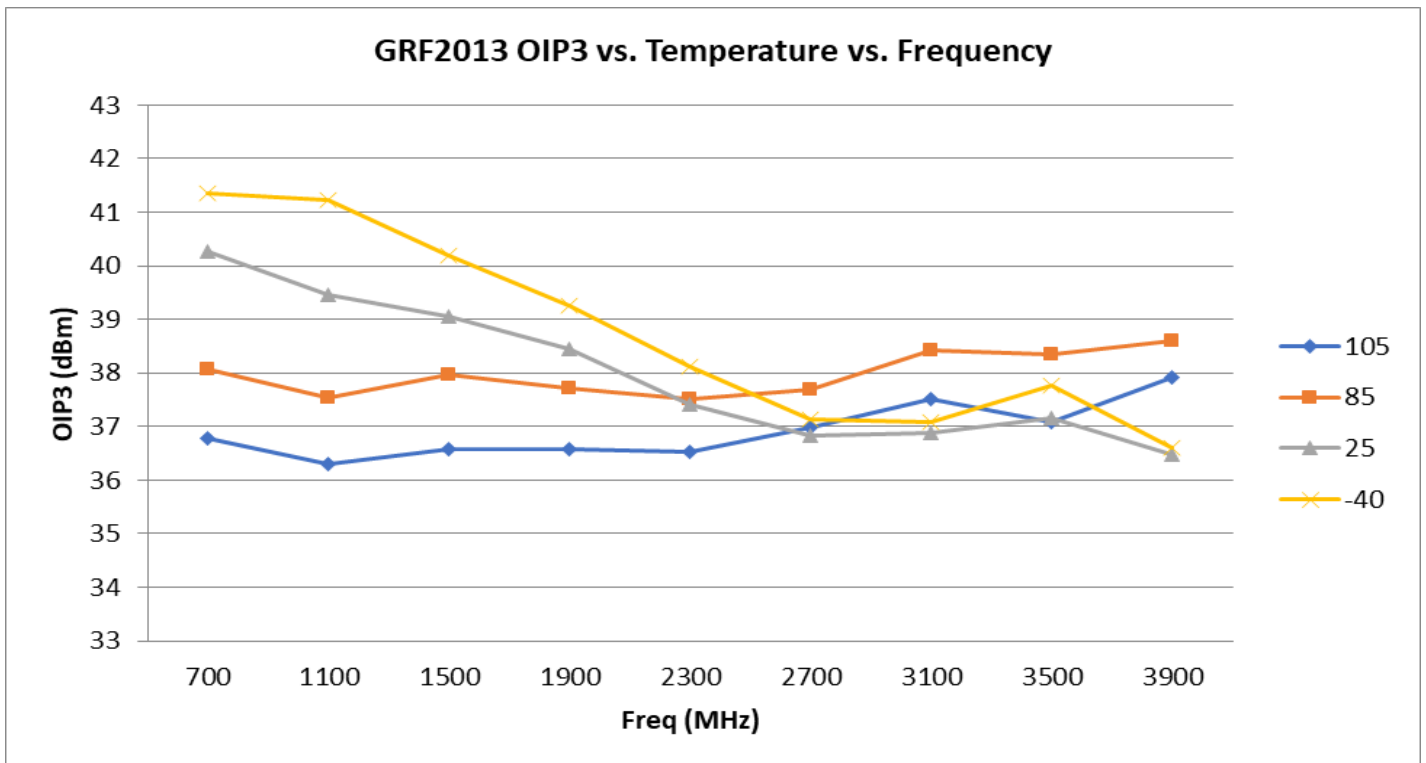
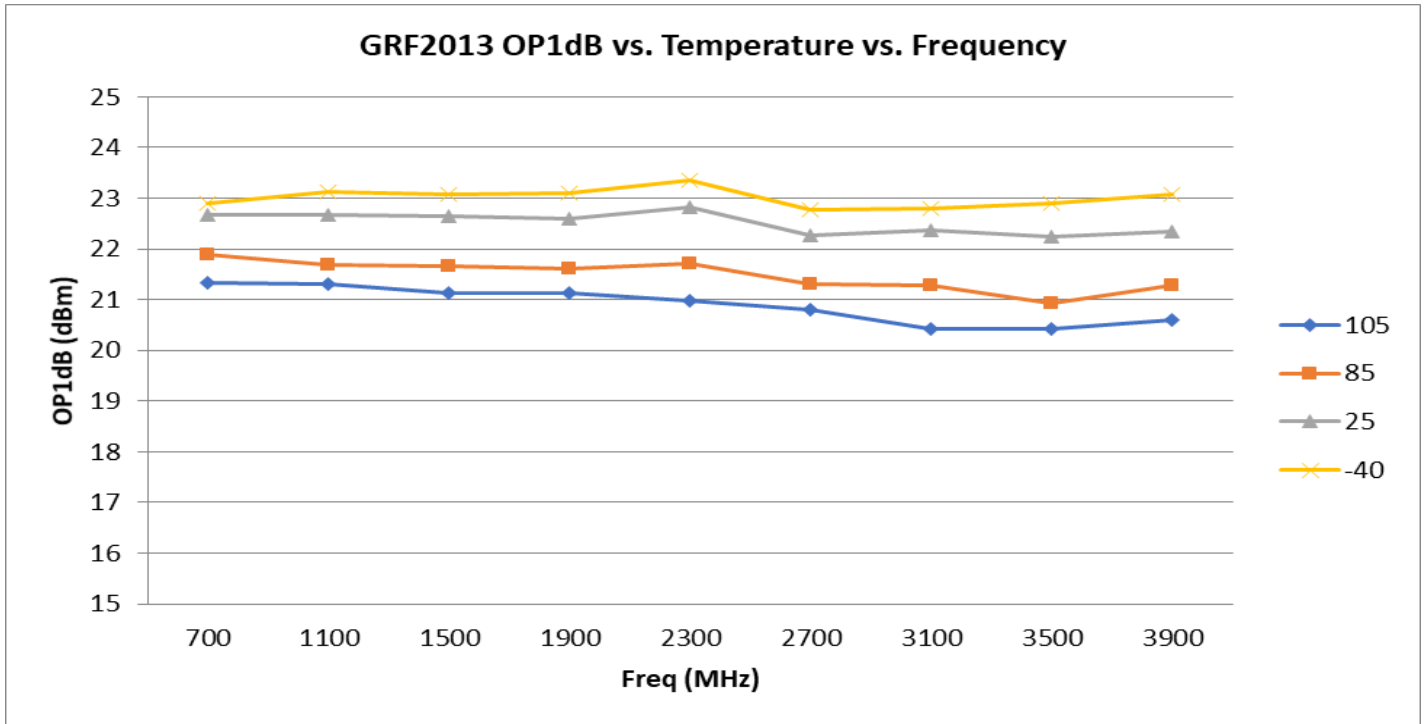


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GRF2013 Evaluation Board Data: (0.7 to 3.8 GHz Tune)



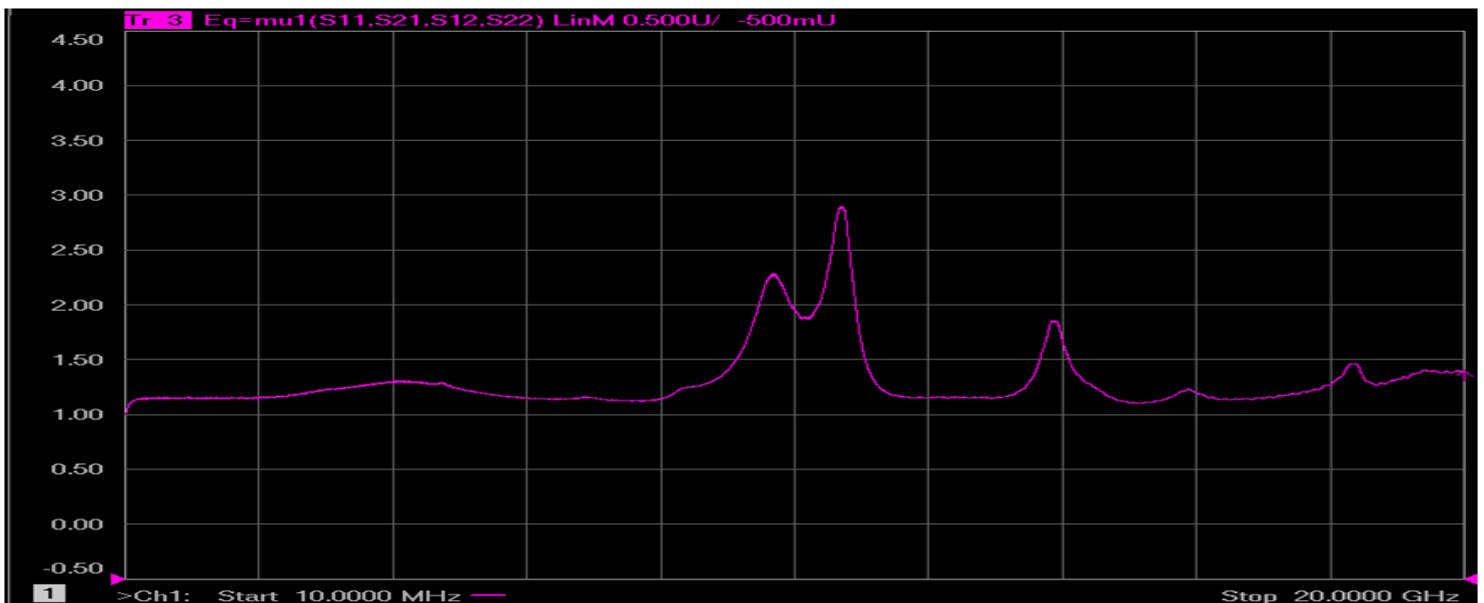
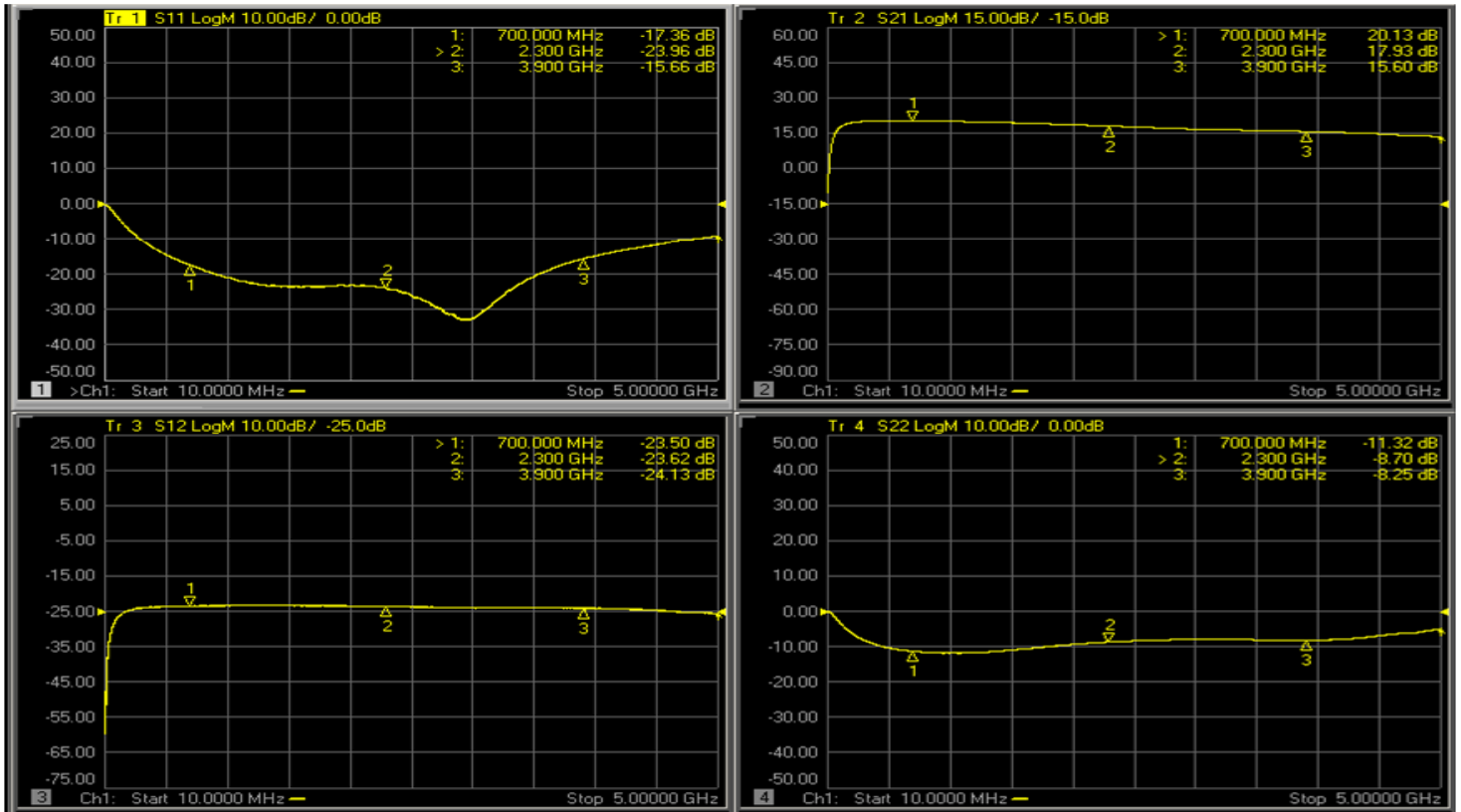


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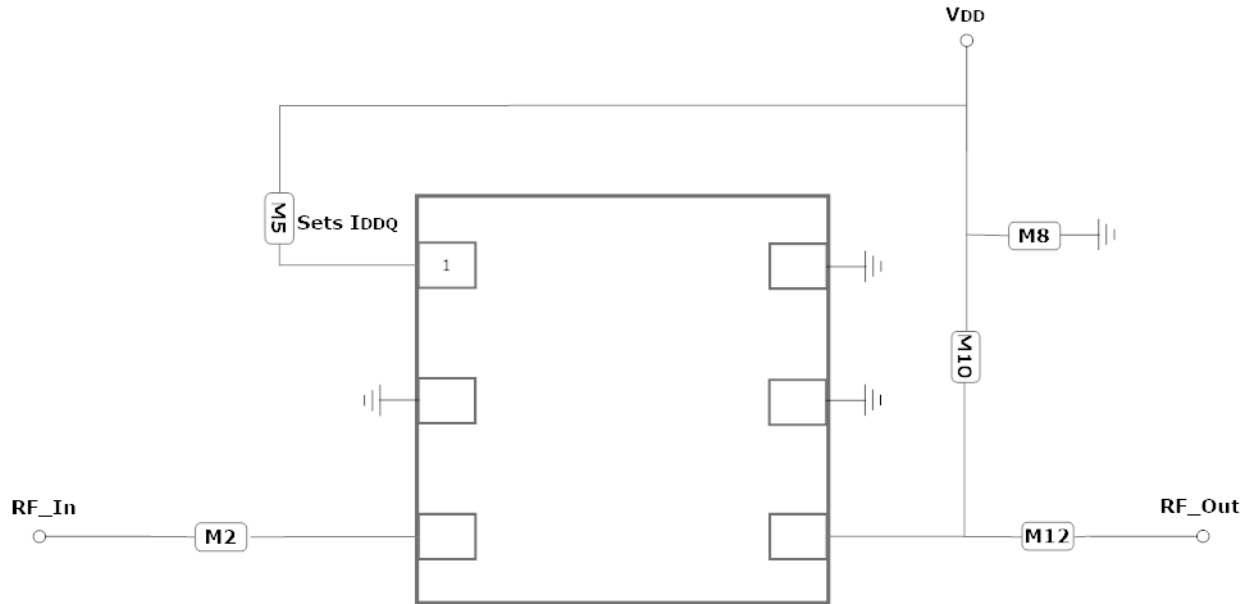
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0.05 to 8.0 GHz

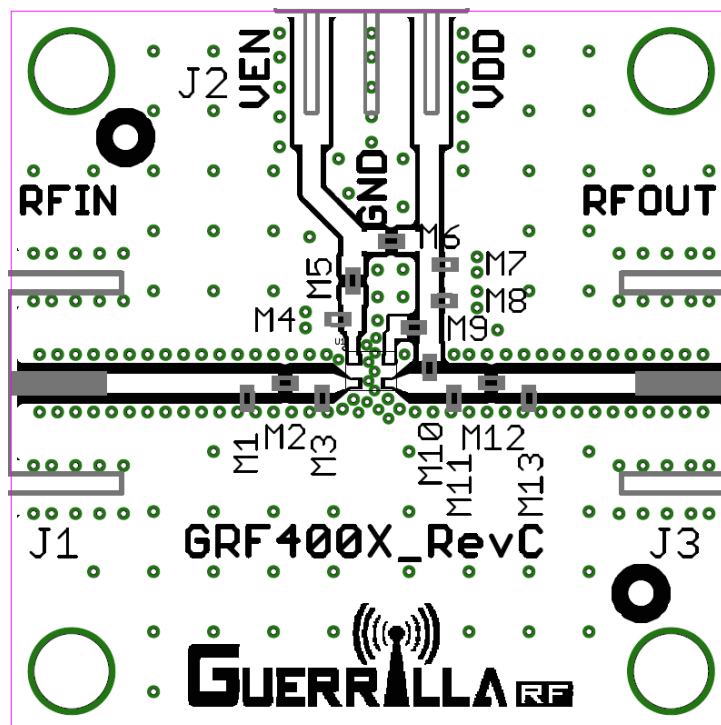
GRF2013 Evaluation Board S-Pars and Stability Mu Factor: (0.7 to 3.8 GHz Tune)



Note: Mu factor ≥ 1.0 implies unconditional stability.



GRF2013 Application Schematic



GRF2013 Evaluation Board Assembly Diagram



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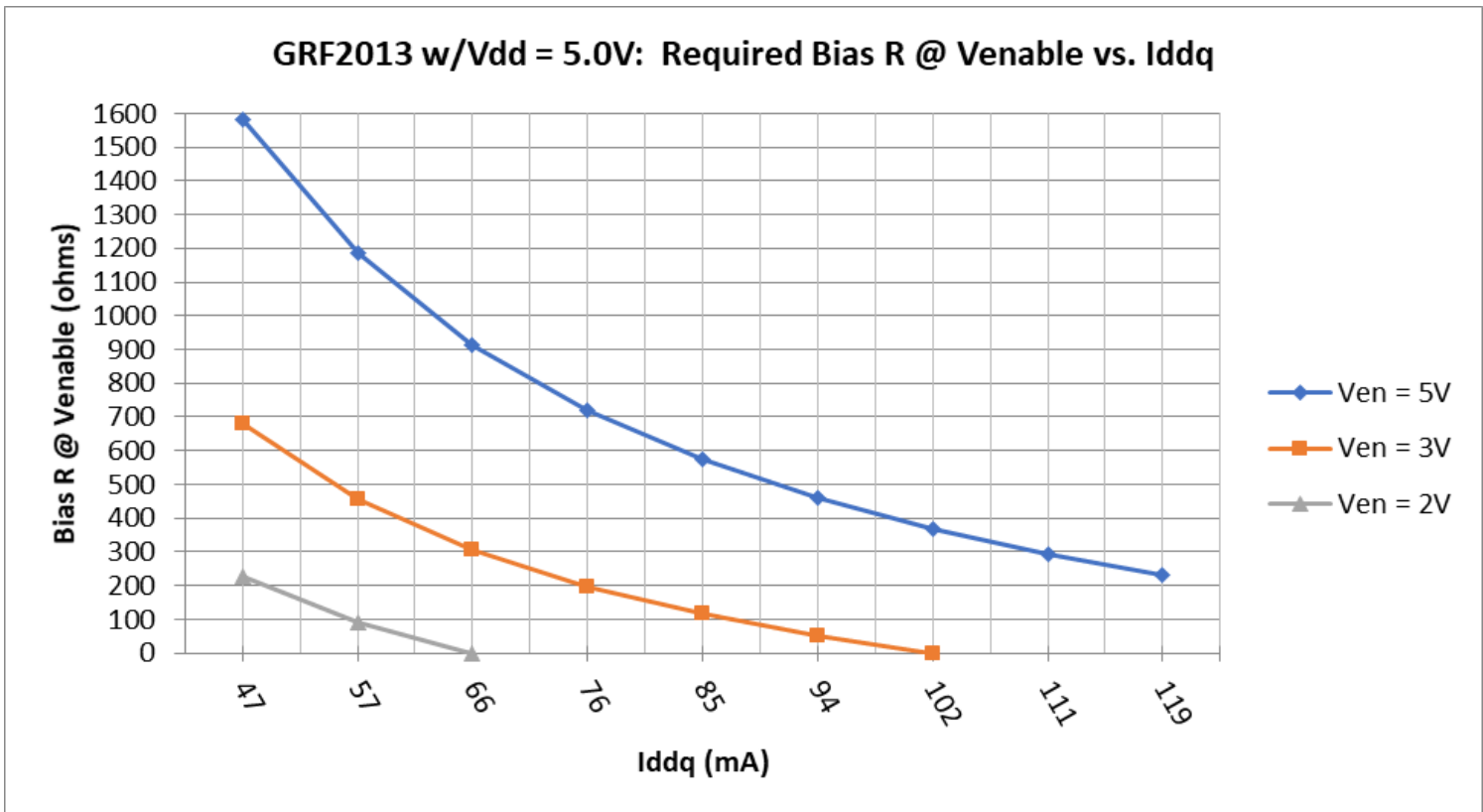
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Broadband Linear Gain Block
0.05 to 8.0 GHz

GRF2013 Standard Evaluation Board BOM: (0.7 to 3.8 GHz Tune)

Component	Type	Manufacturer	Family	Value	Package Size	Substitution
M2	Capacitor	Murata	GRM	100 pF	0402	ok
M5 (See curves)	Resistor	Various	5%	Sets Iddq	0402	ok
M8	Capacitor	Murata	GRM	0.1 uF	0402	ok
M10	Inductor	Various	MLC	33 nH	0402	ok
M12	Capacitor	Murata	GRM	100 pF	0402	ok
Evaluation Board	GRF400X_RevC					

Note: Standard evaluation board bias: Vdd: 5.0V; Venable: 5.0V; M5:





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Broadband Linear Gain Block 0.05 to 8.0 GHz

Data Sheet Release Status:	Notes
Advance	S-parameter and NF data based on EM simulations for the fully packaged device using foundry supplied transistor s-parameters. Linearity estimates based on device size, bias condition and experience with related devices.
Preliminary	All data based on evaluation board measurements in the Guerrilla RF Applications Lab.
Released	All data based on device qualification data. Typically, this data is nearly identical to the data found in the preliminary version. Max and min values for key RF parameters are included.

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