

DESCRIPTION

ST3485EBDR is an RS-485 transceiver with a 3.3V power supply, half duplex, low power consumption, and fully functional compliance with TIA/EIA-485 standards.

ST3485EBDR includes a driver and a receiver, both of which can be independently enabled and disabled. When both are disabled, both the driver and receiver output a high resistance state.

ST3485EBDR has a 1/8 load and allows 256 ST3485EBDR transceivers to be connected together on the same communication bus. Can achieve error free data transmission up to 12Mbps.The working voltage range of ST3485EBDR is 3.0~3.6 V, and it has functions such as fail safe, over temperature protection, current limiting protection, and overvoltage protection.

ABSOLUTE MAXIMUM RATINGS

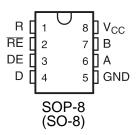
Supply Voltage (V_{CC}) 7V Control Input Voltage -0.3V to +7V Driver Input Voltage (DI) -0.3V to +7V

Driver Output Voltage (A, B) -7V to +13V Receiver Input Voltage (A, B) -7V to +13V Receiver Output Voltage (RO) -0.3V to +7V

Continuous Power Dissipation (T_A= +70°C) 8-Pin SO (derate 5.88mW/°C above +70°C) 400mW

Operating Temperature Ranges0°C to +70°C Storage Temperature Range -65°C to +150°C Lead Temperature (soldering, 10sec) +300°C

PIN CONFIGURATION



FEATURES

- 3.3V power supply, half-duplex
- 1/8 unit load, allowing up to 256 devices to be connected to the bus
- Driver short circuit output protection
- Over temperature protection function
- · Low power shutdown function
- · Receiver open circuit failure protection
- · Has strong noise resistance
- Integrated transient voltage resistance function
- The data transmission rate in an electrical noise environment can reach 12Mbps

APPLICATIONS

- Industrial Networks
- Utility Meters
- Motor Control



DC ELECTRICAL CHARACTERISTICS

(V_{CC} = 3.3V \pm 5%, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITION	MIN	TYP	MAX	UNITS	
Differential Driver Output (no load)	V _{OD1}			3.3		V	
Differential Driver Output	V _{OD2}	$R = 54\Omega (RS-422)$	1.5		VCC	V	
(with load)		$R = 100\Omega (RS-485)$		2		VCC	
Change in Magnitude of Driver Differential Output Voltage for Complementary Output States	ΔVod	R = 54Ω			0.2	V	
Driver Common-Mode Output Voltage	Voc	$R = 54\Omega$			3	V	
Change in Magnitude of Driver Common-Mode Output Voltage for Complementary Output States	ΔVod	R = 54				0.2	V
Input High Voltage	Vін	DE, DI, RE	2.0			V	
Input Low Voltage	VIL	DE, DI, RE				0.8	V
Input Current	I _{IN1}	DE, DI, RE				±2	μA
Input Current	I _{IN2}	DE = 0V;	VIN = 12V			125	μA
(A, B)		Vcc = 0V or 3.3V,	VIN = -7V	-100			μ/ι
Receiver Differential Threshold Voltage	Vтн	$-7V \le V_{CM} \le 12V$		-0.2		0.2	V
Receiver Input Hysteresis	ΔV th	Vcm = 0V	10	30		mV	
Receiver Output High Voltage	Vон	lo = -2.5mA, VID = 20	VCC-1.5			V	
Receiver Output Low Voltage	Vol	lo = 2.5mA, VID = -20			0.4	V	
Three-State (high impedance) Output Current at Receiver	lozr	0.4V ≤ Vo ≤ 2.4V			±1	μA	
Receiver Input Resistance	Rin	-7V ≤ Vcм ≤ 12V		96			kΩ

DC ELECTRICAL CHARACTERISTICS (continued) (Vcc = 3.3V ±5%, Ta = Tmin to Tmax, unless otherwiseoted.)

1 000 - 0:0 V ±070, TA = TWIN 10 TWI	v, arnooc on	ner wiedeted:)				
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
No-Load Supply Current	Icc	DE = V _{CC}		520	800	
		RE = 0V or Vcc		540	700	μΑ
		DE = 0V				
Driver Short-Circuit Current,	losp				±250	mA



SWITCHING CHARACTERISTICS

($Vcc = 5V \pm 5\%$, $T_A = T_{MIN}$ to T_{MAX} , unless otherwise noted.) (Notes 1, 2)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Driver Input to Output	t PLH	RDIFF = 27Ω		8	35	ns
	t PHL	C _{L1} = C _{L2} = 100pF		8	35	
Driver Output Skew to Output	t skew	RDIFF = 60Ω , CL1 = CL2 = 100 pF		10	35	ns
Driver Enable to Output High	tzн	C _L = 110pF, S2 closed		20	90	ns
Driver Enable to Output Low	t zL	C _L = 110pF, S1 closed		20	90	ns
Driver Disable Time from Low	tız	C _L = 110pF, S1 closed		20	80	ns
Driver Disable Time from High	tHZ	C _L = 110pF, S2 closed		20	80	ns
tPLH - tPHL Differential	t skd	RDIFF = 54Ω		7	10	ns
Receiver Skew		C _{L1} = C _{L2} = 100pF				
Receiver Enable to Output Low	t zL	C _{RL} = 15pF, S1 closed		20	45	ns
Receiver Enable to Output High	t zн	C _{RL} = 15pF, S2 closed		20	45	ns
Receiver Disable Time from Low	t LZ	C _{RL} = 15pF, S1 closed		200	1400	ns
Receiver Disable Time from High	t HZ	C _{RL} = 15pF, S2 closed		200	1400	ns

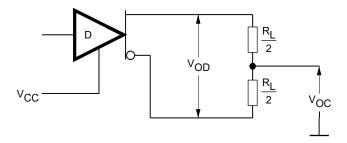
TABLEOF ST3485EBDR OPERATION

Transmission				Receipt				
Inputs		Outputs X			Outputs			
RE	DE	DI	Α	В	RE	DE	A-B	RO
X	1	1	Н	L	0	Χ	+0.2V	I
X	1	0	L	Η	0	Х	-0.2V	L
0	0	Х	Z	Z	0	Х	On/Short Circuit	Н
1	0	Х	Z(shutdown)		1	Х	Х	Z

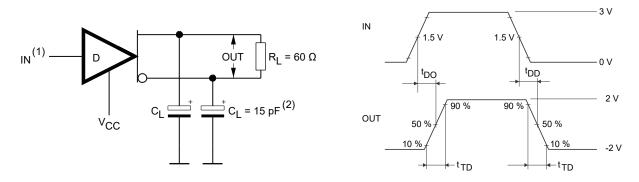
X-Any level

Z-High resistance

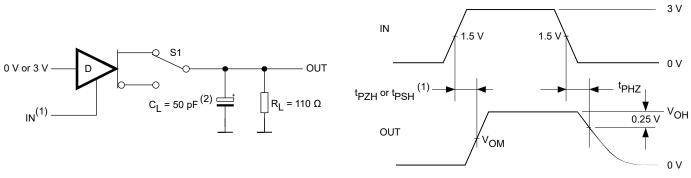
TEST CIRCUITS



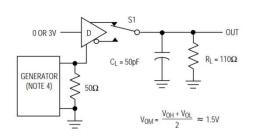
Driver and VOC test load

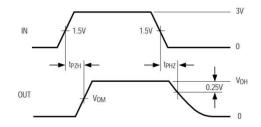


Driver differential delay and transition time

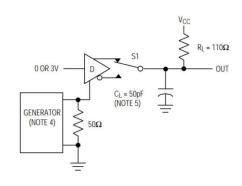


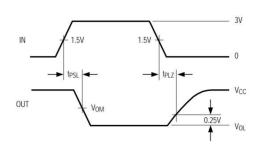
Drive propagation delay





Drive enable and disable time

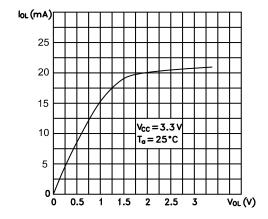




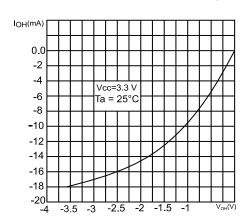
Drive enable and disable times test circuit (pull-up configuration)

TYPICAL CHARACTERISTICS

Receiver output current vs. output low voltag

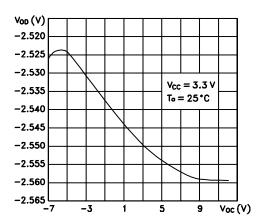


Receiver output current vs. output high voltaç

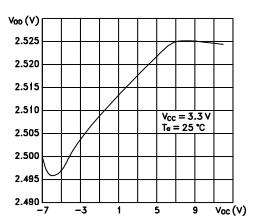




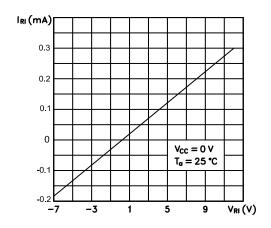
Low level driver output capability



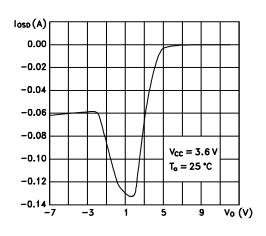
High level driver output capability



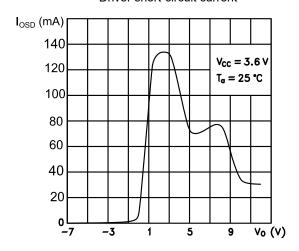
Receiver input characteristics



Driver short-circuit current

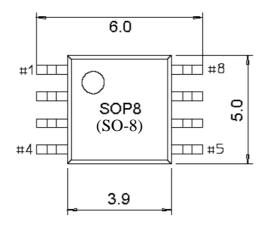


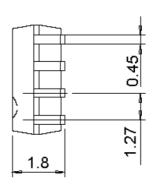
Driver short-circuit current

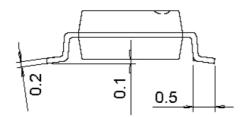


PACKAGE OUTLINE DIMENSIONS

SOP-8(SO-8)









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