



## Description

The UPA2815T1S uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a Battery protection or in other Switching application.

## General Features

$V_{DS} = -20V$   $I_D = -48A$

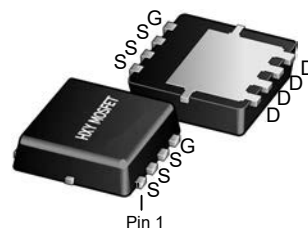
$R_{DS(ON)} < 10m\ \Omega$  @  $V_{GS} = -4.5V$

## Application

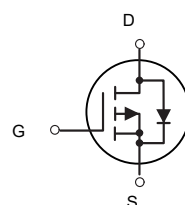
Battery protection

Load switch

Uninterruptible power supply



DFN3X3-8L  
(HWSON-8(3.3x3.3))



P-Channel MOSFET

## Package Marking and Ordering Information

Product ID	Pack	Brand	Qty(PCS)
UPA2815T1S	DFN3X3-8L (HWSON-8(3.3x3.3))	HXY MOSFET	5000

## Absolute Maximum Ratings (TA=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	-20	V
$V_{GS}$	Gate-Source Voltage	$\pm 8$	V
$I_D @ T_C = 25^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V^1$	-48	A
$I_D @ T_C = 70^\circ C$	Continuous Drain Current, $V_{GS} @ -4.5V^1$	-35	A
$I_{DM}$	Pulsed Drain Current <sup>2</sup>	-100	A
$P_D @ T_C = 25^\circ C$	Total Power Dissipation <sup>3</sup>	29	W
$P_D @ T_C = 70^\circ C$	Total Power Dissipation <sup>3</sup>	19	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup>	75	°C/W
$R_{\theta JA}$	Thermal Resistance Junction-Ambient <sup>1</sup> (t ≤ 10s)	40	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case <sup>1</sup>	4.2	°C/W



## Electrical Characteristics (TA=25 °C unless otherwise noted)

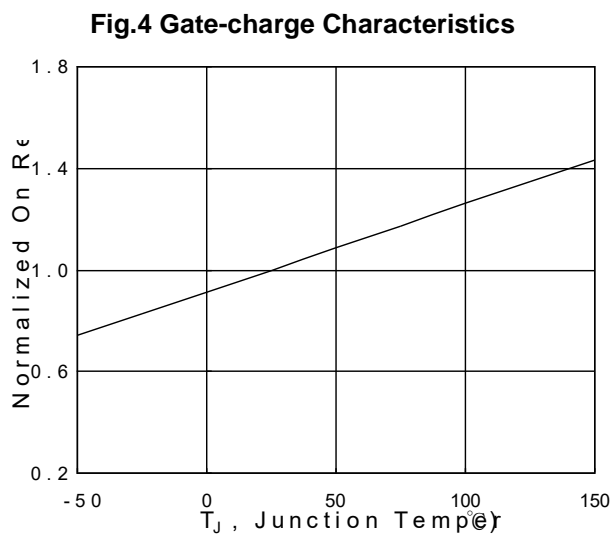
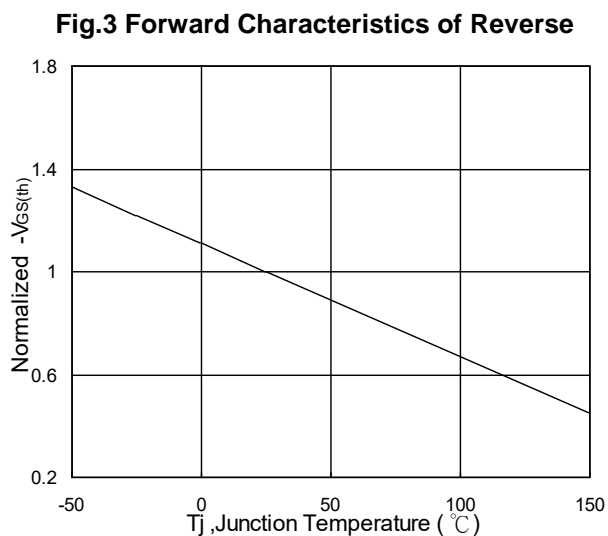
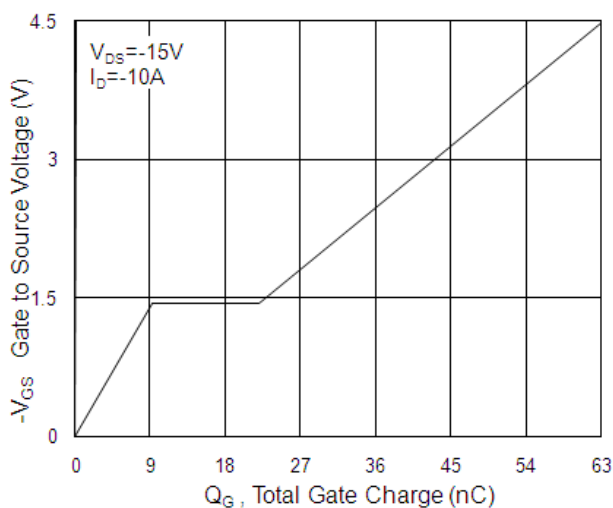
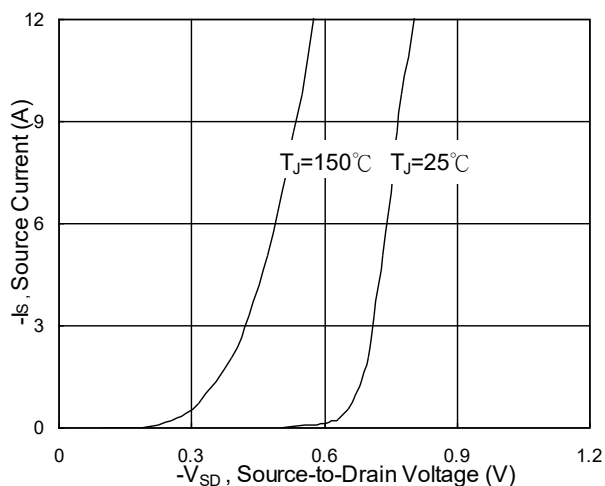
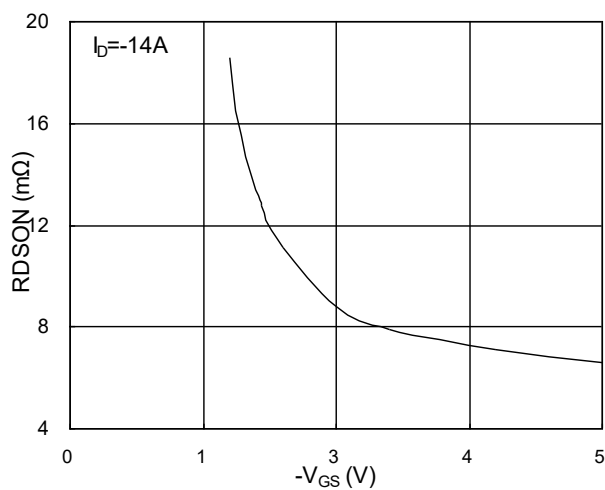
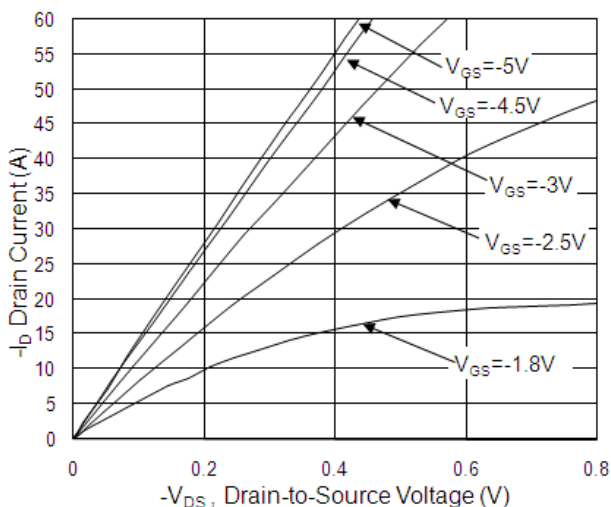
Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-20	-24	---	V
$\frac{\partial BV_{DSS}}{\partial T_J}$	BV <sub>DSS</sub> Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =-1mA	---	-0.012	---	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-10A	---	7.5	10	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-8A	---	8.7	11.5	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-6A	---	13	15	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-0.3	0.6	-1.0	V
$\frac{\partial V_{GS(th)}}{\partial T_J}$	V <sub>GS(th)</sub> Temperature Coefficient		---	2.94	---	mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±8V, V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-5V, I <sub>D</sub> =-10A	---	43	---	S
Q <sub>g</sub>	Total Gate Charge (-4.5V)	V <sub>DS</sub> =-15V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-10A	---	63	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	9.1	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	13	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =-10V, V <sub>GS</sub> =-4.5V, R <sub>G</sub> =3.3Ω, I <sub>D</sub> =-10A	---	15.8	---	ns
T <sub>r</sub>	Rise Time		---	76.8	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	193	---	
T <sub>f</sub>	Fall Time		---	186.4	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V, f=1MHz	---	5783	---	pF
C <sub>oss</sub>	Output Capacitance		---	509	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	431	---	
I <sub>S</sub>	Continuous Source Current <sup>1,4</sup>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-10.7	A
I <sub>SM</sub>	Pulsed Source Current <sup>2,4</sup>		---	---	-60	A
V <sub>SD</sub>	Diode Forward Voltage <sup>2</sup>	V <sub>GS</sub> =0V, I <sub>S</sub> =-1A, T <sub>J</sub> =25°C	---	---	-1.2	V
t <sub>rr</sub>	Reverse Recovery Time	I <sub>F</sub> =-10A, dI/dt=100A/μs, T <sub>J</sub> =25°C	---	27	---	nS
Q <sub>rr</sub>	Reverse Recovery Charge		---	17.8	---	nC

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.



## Typical Characteristics



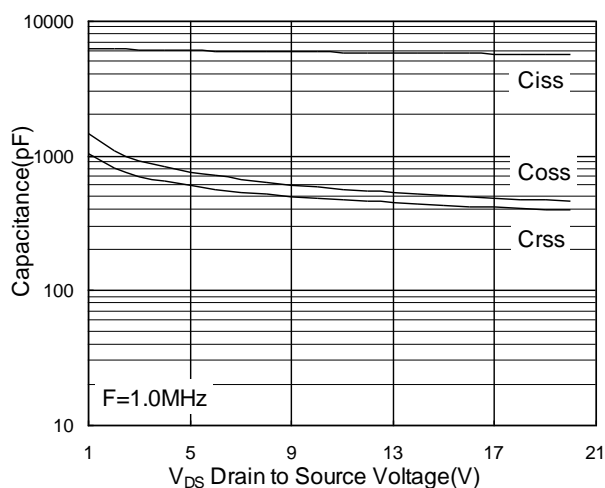


Fig.7 Capacitance

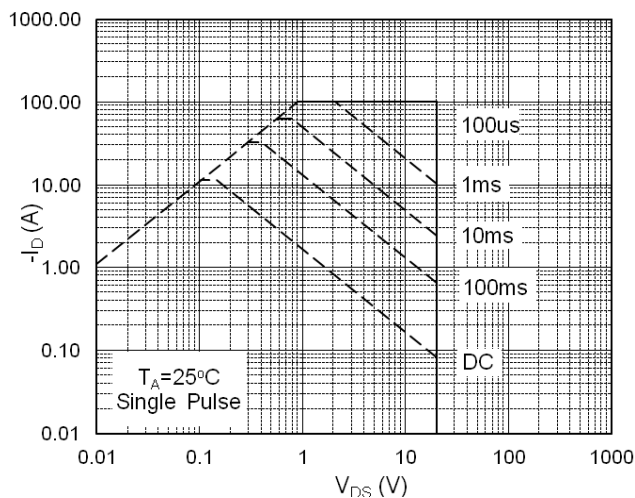


Fig.8 Safe Operating Area

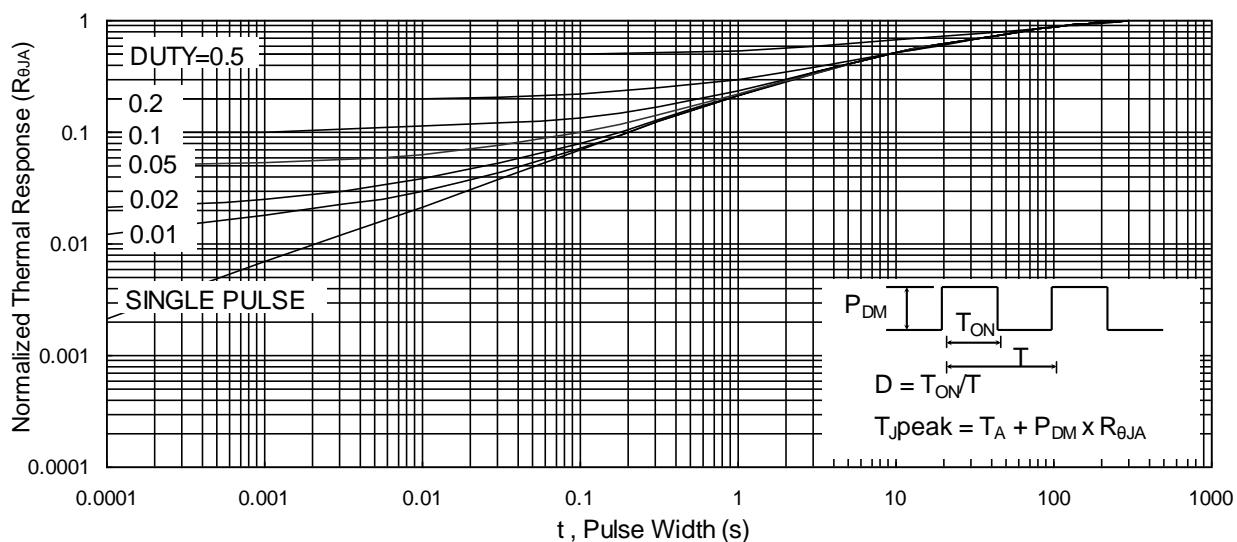


Fig.9 Normalized Maximum Transient Thermal Impedance

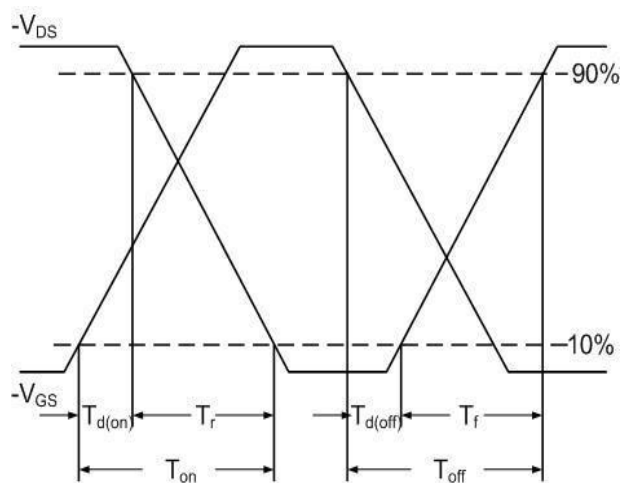


Fig.10 Switching Time Waveform

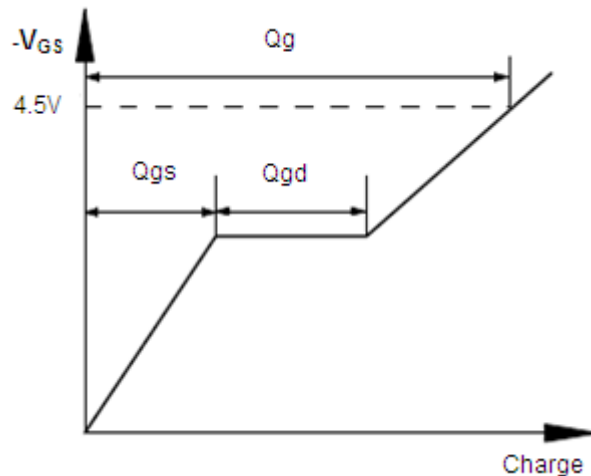
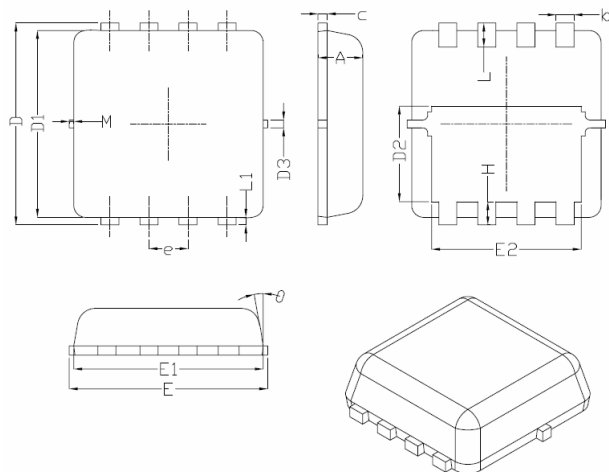


Fig.11 Gate Charge Waveform



## DFN3X3-8L(HWSON-8(3.3x3.3)) Package Information



Symbol	Dimensions In Millimeters		
	Min.	Nom.	Max.
A	0.70	0.75	0.80
b	0.25	0.30	0.35
c	0.10	0.15	0.25
D	3.25	3.35	3.45
D1	3.00	3.10	3.20
D2	1.48	1.58	1.68
D3	-	0.13	-
E	3.20	3.30	3.40
E1	3.00	3.15	3.20
E2	2.39	2.49	2.59
e	0.65BSC		
H	0.30	0.39	0.50
L	0.30	0.40	0.50
L1	-	0.13	-
M	*	*	0.15
θ		10°	12°



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