

Description

The DMP3013SFV uses advanced trench technology

to provide excellent R_{DS(ON)}, low gate charge and

operation with gate voltages as low as 4.5V. This

device is suitable for use as a

Battery protection or in other Switching application.

General Features

V_{DS} =-30V I_D =-55A

 $R_{DS(ON)}$ < 11m Ω @ V_{GS} =-10V

Application

Battery protection

Load switch

Uninterruptible power supply

Package Marking and Ordering Information

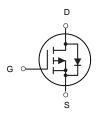
Product ID	Pack	Brand	Qty(PCS)
DMP3013SFV	DFN3X3-8L(PowerDI3333-8)	HXY MOSFET	5000

Absolute Maximum Ratings (T_c=25 ℃ unless otherwise noted)

Symbol	Parameter	Rating	Units
VDS	Drain-Source Voltage	-30	V
VGS	Gate-Source Voltage	±20	V
I _D @T _C =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	Current, V _{GS} @ 10V ¹ -55	
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V ¹	-23	А
IDM	Pulsed Drain Current ²	-140	А
EAS	Single Pulse Avalanche Energy ³	78.8	mJ
P _D @T _C =25°C	Total Power Dissipation⁴	21.5	W
TSTG	Storage Temperature Range	-55 to 150	°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R₀JC	Thermal Resistance Junction-Case ¹	5.8	°C/W



DFN3X3-8L (PowerDI333-8)



P-Channel MOSFET

Electrical Characteristics (TJ=25 $^{\circ}$ C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V,I _D = -250μA	-30	-	-	V
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = -30V, V_{GS} = 0V,$	-	-	-1	μA
I _{GSS}	Gate to Body Leakage Current	V _{DS} =0V, V _{GS} = ±20V	-	-	±100	nA
V _{GS(th)}	Gate Threshold Voltage	V_{DS} = V_{GS} , I_D = -250 μ A	-1.0	-1.5	-2.5	V
D	Static Drain-Source on-Resistance	V _{GS} =-10V, I _D =-12A	-	8.5	11	m0
R _{DS(on)}		V _{GS} =-4.5V, I _D =-8A	-	13	18	mΩ
C _{iss}	Input Capacitance	V _{DS} = -15V, V _{GS} = 0V,	-	2800	-	pF
Coss	Output Capacitance		-	346	-	pF
C _{rss}	Reverse Transfer Capacitance	f = 1.0MHz	-	319	-	pF
Qg	Total Gate Charge	V _{DS} = -15V, I _D = -20A,	-	30	-	nC
Q _{gs}	Gate-Source Charge		-	5.3	-	nC
Q _{gd}	Gate-Drain("Miller") Charge	V _{GS} = -10V	-	7.6	-	nC
t _{d(on)}	Turn-on Delay Time		-	14	-	ns
t _r	Turn-on Rise Time	$V_{DD} = -15V, I_D = -20A,$	-	20	-	ns
t _{d(off)}	Turn-off Delay Time	V_{GS} =-10V, R_{GEN} =2.5 Ω	-	95	-	ns
t _f	Turn-off Fall Time		-	65	-	ns
	Maximum Continuous Drain to Source	Diode Forward			55	^
I _S	Current		-	-	-55	Α
I _{SM}	Maximum Pulsed Drain to Source Diode Forward Current		•	-	-140	Α
V_{SD}	Drain to Source Diode Forward Voltage	V _{GS} = 0V, I _S = -35A	-	-0.8	-1.2	V
	voitage					

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition: T_J= 25 $^{\circ}$ C, VDD= -20V, VG= -10V, L= 0.5mH, RG= 25 $^{\circ}$ C, IAS= -17A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤2%



Typical Performance Characteristics

Figure1: Output Characteristics

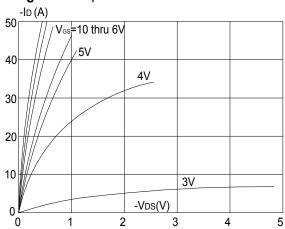


Figure 2: Typical Transfer Characteristics

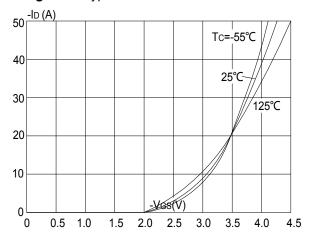


Figure 3:On-resistance vs. Drain Current

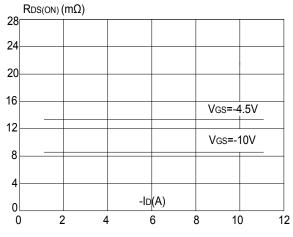


Figure 4: Body Diode Characteristics

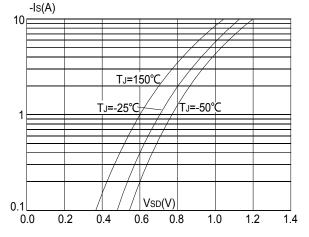


Figure 5: Gate Charge Characteristics

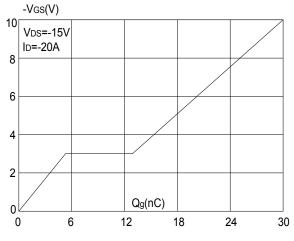


Figure 6: Capacitance Characteristics

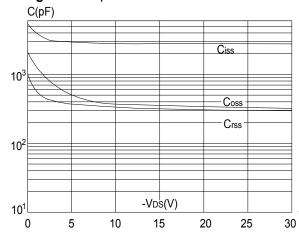




Figure 7: Normalized Breakdown Voltage vs. Junction Temperature

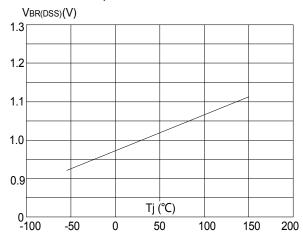


Figure 9: Maximum Safe Operating Area

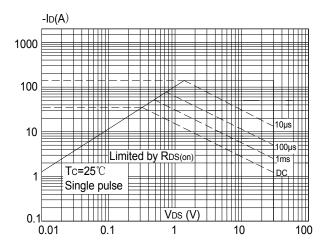


Figure.11: Maximum Effective Transient Thermal Impedance, Junction-to-Case

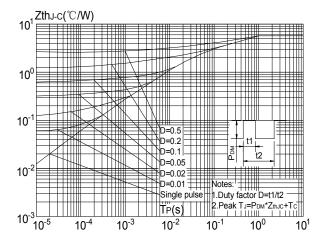


Figure 8: Normalized on Resistance vs. Junction Temperature

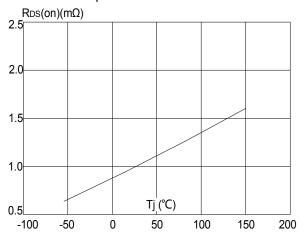
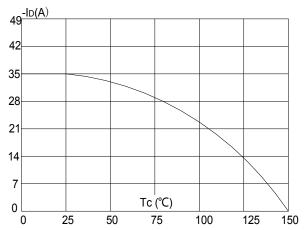
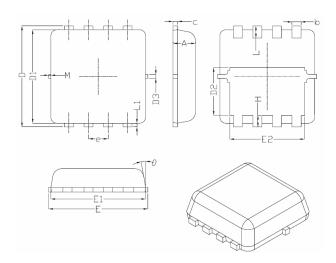


Figure 10: Maximum Continuous Drain Current vs. Case Temperature



DFN3X3-8L(PowerDI3333-8) Package Information



Symbol	Dimensions In Millimeters			
	Min.	Nom.	Max.	
A	0.70	0.75	0.80	
b	0.25	0.30	0.35	
С	0.10	0.15	0.25	
D	3.25	3.35	3.45	
D1	3.00	3.10	3.20	
D2	1.48	1.58	1.68	
D3	-	0.13	-	
E	3.20	3.30	3.40	
E1	3.00	3.15	3.20	
E2	2.39	2.49	2.59	
е	0.65BSC			
Н	0.30	0.39	0.50	
L	0.30	0.40	0.50	
L1	-	0.13	-	
М	*	*	0.15	
θ		10°	12 [°]	



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