

P-Channel 40-V (D-S) MOSFET

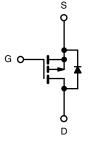
PRODUCT SUMMARY					
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A) ^a	Q _g (Typ.)		
- 40	0.0041 at V _{GS} = - 10 V	- 110	185 nC		

FEATURES

• TrenchFET[®] Power MOSFET







P-Channel MOSFET

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	- 40	V		
Gate-Source Voltage		V _{GS}	± 20	- v	
	T _C = 25 °C		- 110 ^a		
Continuous Droin Current (T 175 °C)	T _C = 70 °C		- 110 ^a		
Continuous Drain Current ($T_J = 175 \ ^{\circ}C$)	T _A = 25 °C	I _D	39 ^{b, c}		
	T _A = 70 °C		33 ^{b, c}		
Pulsed Drain Current	•	I _{DM}	240	A	
Continuous Courses Drain Diada Current	T _C = 25 °C	1	110	_	
Continuous Source-Drain Diode Current	T _A = 25 °C	I _S	10 ^{b, c}		
Avalanche Current		I _{AS}	75		
Single-Pulse Avalanche Energy	L = 0.1 mH	E _{AS}	281	mJ	
	T _C = 25 °C		375		
Maximum Davier Disaination	T _C = 70 °C	Б	262	w	
Maximum Power Dissipation	T _A = 25 °C	P _D	15 ^{b, c}		
	T _A = 70 °C		10.5 ^{b, c}	7	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	
Soldering Recommendations (Peak Temperature		260	°C		

THERMAL RESISTANCE RATINGS							
Parameter	Symbol	Typical	Maximum	Unit			
Maximum Junction-to-Ambient ^{b, d}	$t \le 10 s$	R _{thJA}	8	10	°C/W		
Maximum Junction-to-Case (Drain)	(Drain) Steady State		0.33	0.4	0/22		

Notes:

a. Package limited.
b. Surface Mounted on 1" x 1" FR4 board.
c. t = 10 s.
d. Maximum under Steady State conditions is 40 °C/W.

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Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit	
Static	Symbol	Test conditions	IVIII.	тур.	wax.	Unit	
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 40			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$		40	- 40			
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μA		- 5.5		mV/°0	
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 2	- 3	- 4	V	
Gate-Source Leakage		$V_{DS} = 0 V, V_{GS} = \pm 20 V$	- 2	0	± 100	nA	
Gale Gource Leakage	'655	$V_{DS} = -40 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			- 1	μA Α	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -40 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ T}_{J} = 55 \text{ °C}$			- 10		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = -10 \text{ V}$	- 120		10		
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = - 10 V, I _D = - 20 A		0.0041		Ω	
Forward Transconductance ^a	g _{fs}	V _{DS} = - 15 V, I _D = - 20 A		75		S	
Dynamic ^b	-10						
Input Capacitance	C _{iss}			11300			
Output Capacitance	C _{oss}	V _{DS} = - 25 V, V _{GS} = 0 V, f = 1 MHz		1510		pF	
Reverse Transfer Capacitance	C _{rss}			1000			
Total Gate Charge	Qg			185	280	nC	
Gate-Source Charge	Q _{gs}	V_{DS} = - 20 V, V_{GS} = - 10 V, I_{D} = - 110 A		48			
Gate-Drain Charge	Q _{gd}			42			
Gate Resistance	R _g	f = 1 MHz		4.0		Ω	
Turn-On Delay Time	t _{d(on)}			25	40		
Rise Time	t _r	V_{DD} = - 20 V, R_L = 0.18 Ω		290	440		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 110 A, V_GEN = - 10 V, R_g = 1 Ω		110	165	- ns	
Fall Time	t _f			35	55		
Drain-Source Body Diode Characteristics	S				1		
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			- 110	•	
Pulse Diode Forward Current ^a	I _{SM}				- 240	A	
Body Diode Voltage	V _{SD}	I _S = - 20 A		- 0.8	- 1.5	V	
Body Diode Reverse Recovery Time	t _{rr}			70	105	ns	
Body Diode Reverse Recovery Charge	Q _{rr}	$L = 20.4 di/dt = 100.4/\mu c T = 25.00$		130	200	nC	
Reverse Recovery Fall Time	t _a	I _F = - 20 A, di/dt = 100 A/μs, T _J = 25 °C		37			
Reverse Recovery Rise Time	t _b			33		ns	

Notes:

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

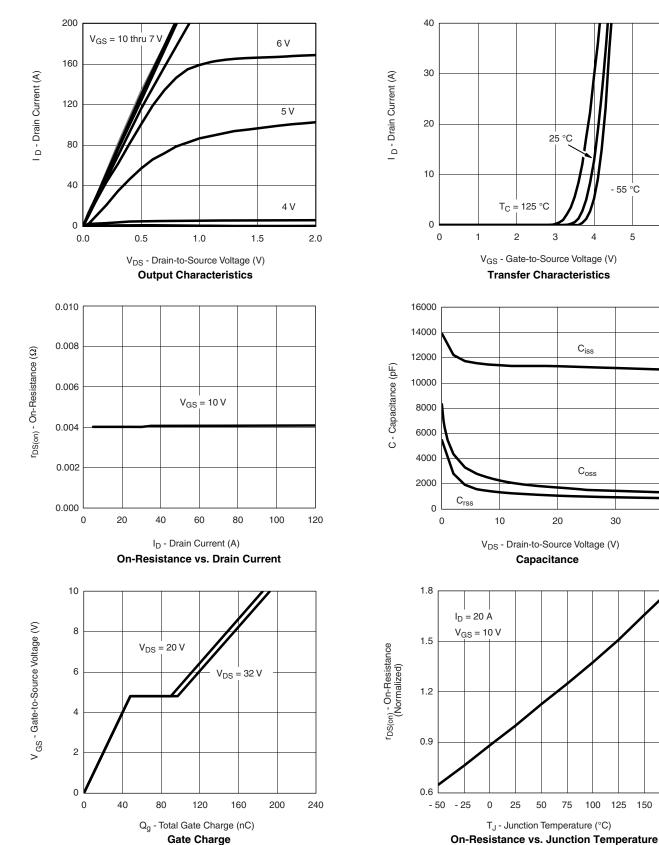
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



5

6

40

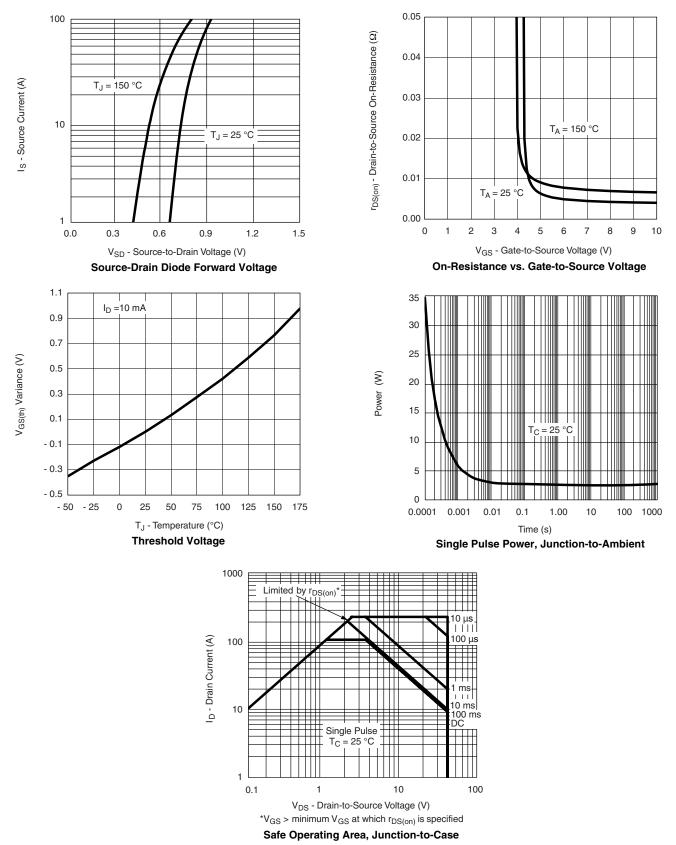


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

服务热线:400-655-8788

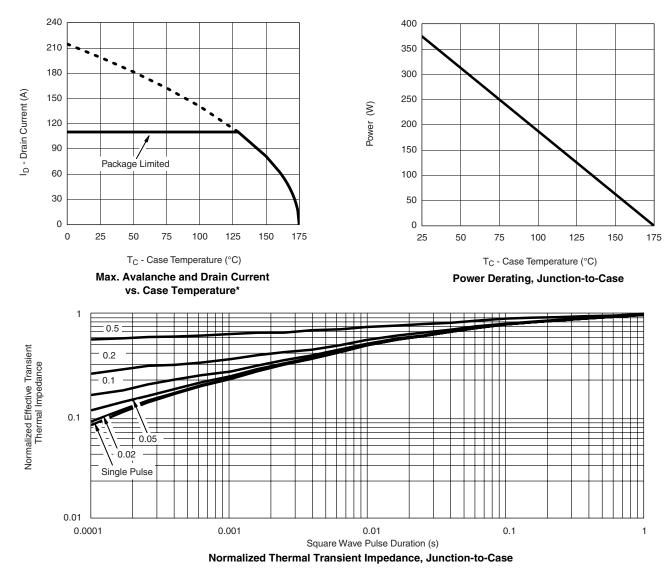
150 175





TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted





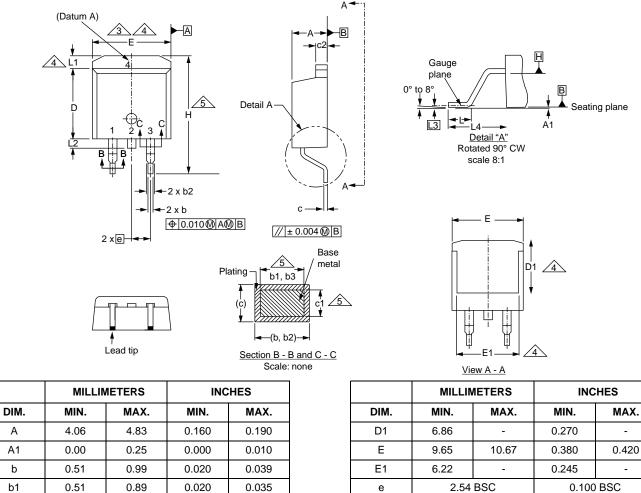
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* The power dissipation P_D is based on $T_{J(max)} = 175$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

NP50P04KDG



TO-263AB



A1	0.00	0.25	0.000	0.010		E	9.65	10.67	
b	0.51	0.99	0.020	0.039		E1	6.22	-	
b1	0.51	0.89	0.020	0.035		е	2.54 BSC		
b2	1.14	1.78	0.045	0.070		Н	14.61	15.88	
b3	1.14	1.73	0.045	0.068		L	1.78	2.79	
С	0.38	0.74	0.015	0.029		L1	-	1.65	
c1	0.38	0.58	0.015	0.023		L2	-	1.78	
c2	1.14	1.65	0.045	0.065		L3	0.25 BSC		
D	8.38	9.65	0.330	0.380		L4	4.78	5.28	
	ECN: S-82110-Rev. A, 15-Sep-08 DWG: 5970								

Notes

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1. Dimensioning and tolerancing per ASME Y14.5M-1994.

2. Dimensions are shown in millimeters (inches).

3. Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body at datum A.

4. Thermal PAD contour optional within dimension E, L1, D1 and E1.

5. Dimension b1 and c1 apply to base metal only.

6. Datum A and B to be determined at datum plane H.

7. Outline conforms to JEDEC outline to TO-263AB.

0.575

0.070

-

-

0.188

0.010 BSC

0.625

0.110

0.066 0.070

0.208



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