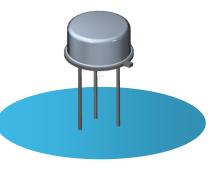
# **P-CHANNEL POWER MOSFET**



## IRFF9130 / 2N6849

- MOSFET Transistor in a Hermetic Metal TO-205AF Package
- Designed For Switching, Power Supply, Motor Control and Amplifier Applications
- High Reliability and Screening Options Available



### ABSOLUTE MAXIMUM RATINGS (T<sub>C</sub> = 25°C unless otherwise stated)

V <sub>DS</sub>	Drain - Source Voltage	-100V
V <sub>GS</sub>	Gate - Source Voltage	±20V
۱ <sub>D</sub>	Continuous Drain Current @ T <sub>C</sub> = 25°C	-5.8A
۱ <sub>D</sub>	Continuous Drain Current @ T <sub>C</sub> = 100°C	-3.7A
IDM <sup>(1)</sup>	Pulsed Drain Current	-25A
PD	Total Power Dissipation @ T <sub>C</sub> = 25°C	20.833W
	Derate Above 25°C	0.167W/°C
Тј	Junction Temperature Range	-55 to +150°C
T <sub>stg</sub>	Storage Temperature Range	-55 to +150°C

### **THERMAL PROPERTIES**

Symbols	Parameters	Max.	Units
<sup>R</sup> ӨJC	Thermal Resistance, Junction To Case	6	°C/W

### Notes

- (1) Repetitive Rating: Pulse width limited by maximum junction temperature
- (2) Pulse Width  $\leq$  380µs,  $\delta \leq$  2%
- (3) By Design Only, Not A Production Test.

Semelab Limited reserves the right to change test conditions, parameter limits and package dimensions without notice. Information furnished by Semelab is believed to be both accurate and reliable at the time of going to press. However Semelab assumes no responsibility for any errors or omissions discovered in its use. Semelab encourages customers to verify that datasheets are current before placing orders.



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### **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise stated)

Symbols	Parameters	Test Conditions		Min.	Тур.	Max.	Units
V <sub>(BR)DSS</sub>	Drain - Source Breakdown Voltage	V <sub>GS</sub> = 0V	I <sub>D</sub> = -0.25mA	-100			V
		V <sub>GS</sub> = -10V	I <sub>D</sub> = -3.7A			0.3	
R <sub>DS</sub> (on) <sup>(2)</sup>	Static Drain - Source On - State Resistance		T <sub>A</sub> = 125°C			0.54	Ω
		V <sub>GS</sub> = -10V	I <sub>D</sub> = -5.8A			0.32	
		V <sub>DS</sub> = V <sub>GS</sub>	I <sub>D</sub> = -250μA	-2		-4	
V <sub>GS(th)</sub>	Gate Threshold Voltage		T <sub>A</sub> = 125°C	-1.0			V
			T <sub>A</sub> = -55°C			-5	
gfs	Forward Transconductance	V <sub>DS</sub> ≥-5V	I <sub>DS</sub> = -3.7A	2.5		7.5	S(ʊ)
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>GS</sub> = 0	V <sub>DS</sub> = 0.8BV <sub>DSS</sub>			100	
			T <sub>A</sub> = 125°C			500	μΑ
	Gate - Source Leakage	$V_{GS} = \pm 20V$	·			±100	nA
IGSS			T <sub>A</sub> = 125°C			±200	ПA

### DYNAMIC CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise stated)

C <sub>iss</sub>	Input Capacitance	Vpc = -25V	845		
C <sub>OSS</sub>	Output Capacitance	V <sub>DS</sub> = -25V V <sub>GS</sub> = 0V	373		рF
C <sub>rss</sub>	Reverse Transfer Capacitance	f = 1.0MHz	92		Р.
<sup>t</sup> d(on)	Turn-On Delay Time			60	
t <sub>r</sub>	Rise Time	$V_{DD} = -50V$ $I_D = -5.8A$ $R_G = 7.5Ω$		140	nc
<sup>t</sup> d(off)	Turn-Off Delay Time	$R_{G} = 7.5\Omega$		140	ns
t <sub>f</sub>	Fall Time	-		140	

### SOURCE-DRAIN DIODE CHARACTERISTICS

t <sub>rr</sub> <sup>(3)</sup>	Reverse Recovery Time	$I_S = -5.8A$ $V_{DD} \le -50V$	T <sub>J</sub> = 25°C di/dt = 100A/μs		250	ns
V <sub>SD</sub>	Diode Forward Voltage	I <sub>S</sub> = -5.8A V <sub>GS</sub> = 0	Т <sub>Ј</sub> = 25°С		-4.3	V

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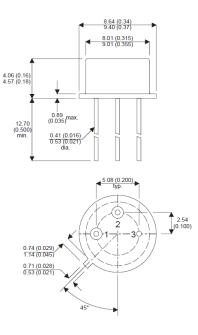
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### **MECHANICAL DATA**

Dimensions in mm (inches)



TO-39 (TO-205AF)

### PACKAGE PIN CONNECTIONS

Pin	Connection
1	Source
2	Gate
3	Drain

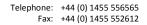
### PART NUMBER VARIANTS<sup>(4)(5)</sup>

Part Number Reference	Termination Finish	SML ROHS
IRFF9130 / 2N6849	Pre-tinned 63% Tin, 37% Lead	LD <sup>(6)</sup>

### Notes

(4) Specify lead finish option by part number at point of order.

- (5) All design variants contain Lead (Pb) within the construction of the device. The Lead content is fully RoHS compliant but using an exemption as currently understood from the EU directive 2011/65/EU (Annex III, exemption 7a).
- (6) LD = e0, as defined in J-STD-609 2nd Level Interconnect Category.



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