

# P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS}(V)$ $R_{DS(on)}(\Omega)$		Q <sub>g</sub> (Typ.)			
- 30	0.056at V <sub>GS</sub> = - 10 V	- 20	19 nC			
	0.072 at V <sub>GS</sub> = -4.5 V	- 15	19110			

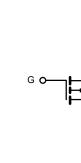
### **FEATURES** Halogen-free

- TrenchFET® Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested



#### **APPLICATIONS**

- Load Switch
- · Notebook Adaptor Switch



P-Channel MOSFET

TO-251	
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0	
	Drain Connected to Drain-Tab
G D S	
Top View	

ABSOLUTE MAXIMUM RATINGS T Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	V <sub>DS</sub>	- 30	Onit	
Gate-Source Voltage	V <sub>GS</sub>	± 20	V	
	T <sub>C</sub> = 25 °C	55	- 20	
Continuous Drain Current /T 450 °C)	T <sub>C</sub> = 70 °C		- 15	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	l <sub>D</sub>	-7.9 <sup>a, b</sup>	
	T <sub>A</sub> = 70 °C		- 5.6 <sup>a, b</sup>	
Pulsed Drain Current	I <sub>DM</sub>	- 60	A	
Ocationes Oceana Brain Binds Oceana	T <sub>C</sub> = 25 °C		- 20	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	ls –	- 7.9 <sup>a, b</sup>	
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	- 20	
Single-Pulse Avalanche Energy	L = 0.1 mn	E <sub>AS</sub>	20	mJ
	T <sub>C</sub> = 25 °C		20	
Maximum Power Dissipation	T <sub>C</sub> = 70 °C	P <sub>D</sub>	15	W
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	] '	2.7 <sup>a, b</sup>	VV
	T <sub>A</sub> = 70 °C		1.7 <sup>a, b</sup>	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>a, c</sup>	t ≤ 10 s	R <sub>thJA</sub>	38	46	°C/W	
Maximum Junction-to-Foot	Steady State	R <sub>thJF</sub>	20	25	5/ * *	

#### Notes:

- a. Surface mounted on 1" x 1" FR4 board.
- b. t = 10 s.
- c. Maximum under Steady State conditions is 85 °C/W.
- d. Based on  $T_C$  = 25 °C.



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	- 30			V
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$	I <sub>D</sub> = - 250 µA		- 34		mV/
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = - 250 μA		5.3		°C
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1.4		- 2.5	V
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 25 \text{ V}$			± 100	nA
Zana Oata Vallana Brain Oamani	I <sub>DSS</sub>	V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V			- 1	0
Zero Gate Voltage Drain Current		V <sub>DS</sub> = - 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			- 5	μA
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge -10 \text{ V}, V_{GS} = -10 \text{ V}$	- 20			Α
	, ,	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 6 A		0.056		
Drain-Source On-State Resistance <sup>a</sup>	$R_{DS(on)}$	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 4 A		0.072		Ω
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = - 10 V, I <sub>D</sub> = - 6 A		28		S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>			1150		pF
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		205		
Reverse Transfer Capacitance	C <sub>rss</sub>			140		
Total Gate Charge	Q <sub>g</sub> V	$V_{DS} = -15 \text{ V}, V_{GS} = -10 \text{ V}, I_{D} = -6 \text{ A}$		27	43	nC
				19	25	
Gate-Source Charge	Q <sub>qs</sub>	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -6 \text{ A}$		6		
Gate-Drain Charge	Q <sub>gd</sub>			12		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.5	2.2	4.4	Ω
Turn-On Delay Time	t <sub>d(on)</sub>			13	25	
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 1.5 \Omega$		12	24	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_{D} \cong -10 \text{ A}, V_{GEN} = -10 \text{ V}, R_{g} = 1 \Omega$		40	70	
Fall Time	t <sub>f</sub>	_		9	18	
Turn-On Delay Time	t <sub>d(on)</sub>			48	80	ns
Rise Time	t <sub>r</sub>	$V_{DD} = -15 \text{ V}, R_{L} = 1.5 \Omega$		92	160	- - -
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong -6 \text{ A}, \ V_{GEN} = -4.5 \text{ V}, \ R_g = 1 \Omega$		34	60	
Fall Time	t <sub>f</sub>			19	35	
<b>Drain-Source Body Diode Characteris</b>	tics					
Continous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			- 4.1	۸
Pulse Diode Forward Current	I <sub>SM</sub>				- 60	A
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 3 A, V <sub>GS</sub> = 0 V		- 0.75	- 1.2	V
Body Diode Reverse Recovery Time	t <sub>rr</sub>			27	45	ns
Body Diode Reverse Recovery Charge $Q_{rr}$ Reverse Recovery Fall Time $t_a$		L CA dl/dt 100 A/:- T 0500		16	27	nC
		$I_F = -6 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		12		ns
Reverse Recovery Rise Time	t <sub>b</sub>			15		

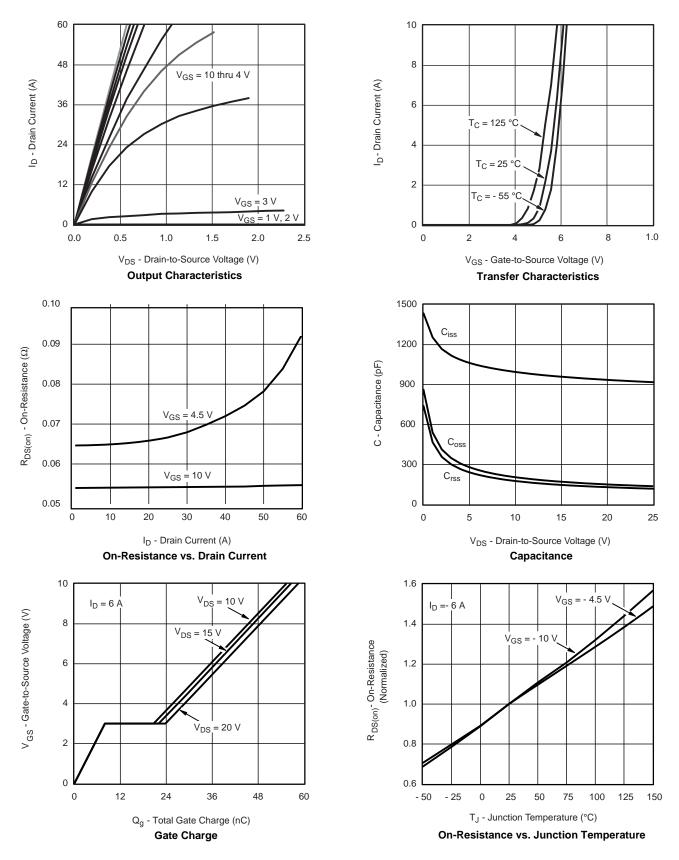
#### Notes:

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

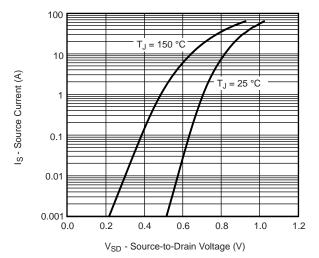


#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

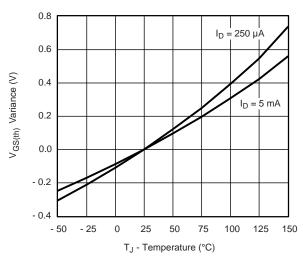




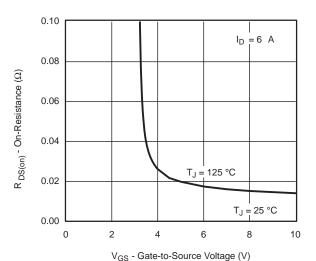
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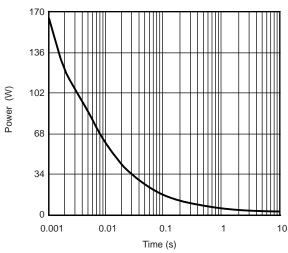
#### Source-Drain Diode Forward Voltage



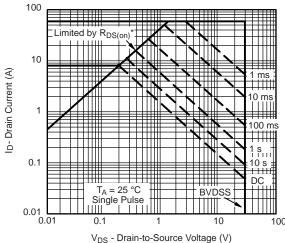
**Threshold Voltage** 



On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient

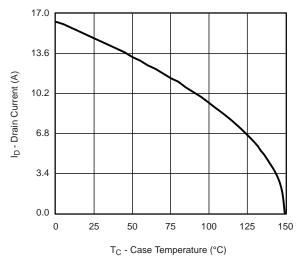


\* V<sub>GS</sub> > minimum V<sub>GS</sub> at which R<sub>DS(on)</sub> is specified

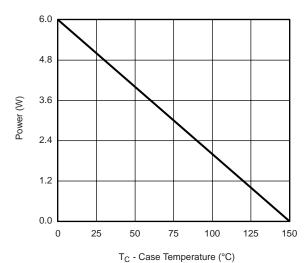
Safe Operating Area



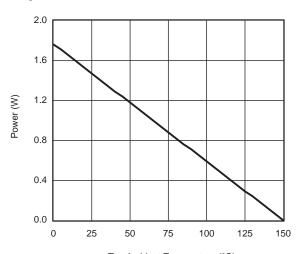
### MOSFET TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



#### **Current Derating\***



Power, Junction-to-Foot

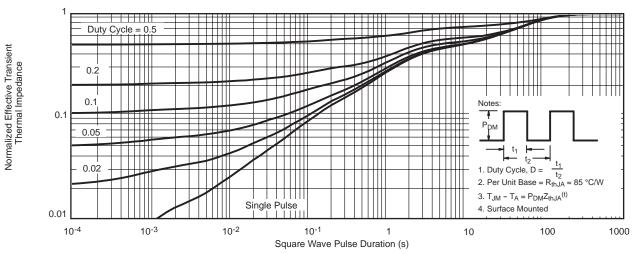


T<sub>A</sub> - Ambient Temperature (°C) Power Derating, Junction-to-Ambient

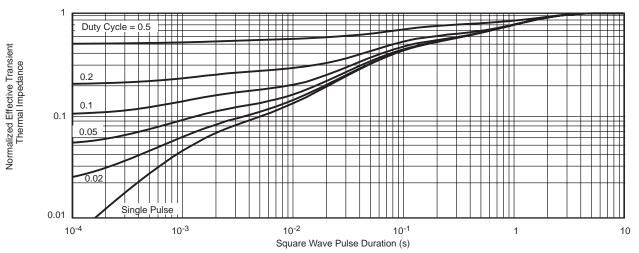
<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)}$  = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package



#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



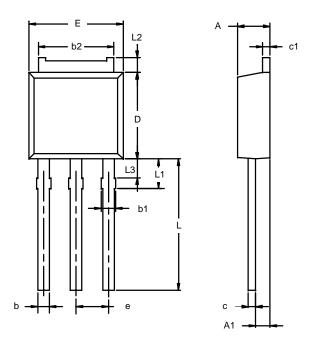
Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot



#### **TO-251AA**



Note: Dimension L3 is for reference only.

	MILLIM	IETERS	INC	HES
Dim	Min	Max	Min	Max
Α	2.21	2.38	0.087	0.094
<b>A</b> 1	0.89	1.14	0.035	0.045
b	0.71	0.89	0.028	0.035
b1	0.76	1.14	0.030	0.045
b2	5.23	5.43	0.206	0.214
С	0.46	0.58	0.018	0.023
с1	0.46	0.58	0.018	0.023
D	5.97	6.22	0.235	0.245
Е	6.48	6.73	0.255	0.265
е	2.28 BSC		0.090	BSC
L	3.89	9.53	0.153	0.375
L1	1.91	2.28	0.075	0.090
L2	0.89	1.27	0.035	0.050
L3	1.15	1.52	0.045	0.060



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