



Continental Device India Pvt. Limited

An IATF 16949, ISO9001 and ISO 14001 Certified Company



GLASS PASSIVATED BRIDGE RECTIFIERS

Reverse Voltage - 50 to 1000 Volts

Forward Current - 2.0 Amperes

KBP2005 ~ KBP210

**KBP Leaded
Plastic Package
RoHS compliant**



KBP

FEATURES:

1. Surge overload rating -60 amperes peak
2. Ideal for printed circuit board
3. Plastic material has Underwriters Laboratory flammability classification 94V-0
4. Mounting position :Any
5. Weight: 0.07ounces ,1.65grams
6. For AECQ compliant products, please suffix-AH in the part number while ordering.

APPLICATIONS: Single Phase AC to DC converter

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Ratings at Ta= 25°C unless specified otherwise, single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.)

DESCRIPTION	SYMBOLS	KBP 2005	KBP 201	KBP 202	KBP 204	KBP 206	KBP 208	KBP 210	UNITS
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Bridge Input Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Output Current	@ $T_A=50^{\circ}C$ $I_{(AV)}$	2.0							A
Peak Forward Surge Current,8.3ms Single Half Sine-Wave Super Imposed on Rated Load	I_{FSM}	60							A
Maximum Forward Voltage Drop Per Bridge Element at 2.0A Peak	V_F	1.1							V
Maximum Reverse Current at Rated DC Blocking Voltage Per Element	I_R	10.0							μA
Maximum Reverse Current at Rated DC Blocking Voltage Per Element	@ $T_A=100^{\circ}C$ I_R	1.0							mA
Operating Temperature Range T_J	T_J	-55 to +150							$^{\circ}C$
Storage Temperature Range T_A	T_{STG}	-55 to +150							$^{\circ}C$

KBP2005 TO KBP210
Rev1_17072020EEG



TYPICAL CHARACTERISTIC CURVES

Fig 1: DERATING CURVE
OUTPUT RECTIFIED CURRENT

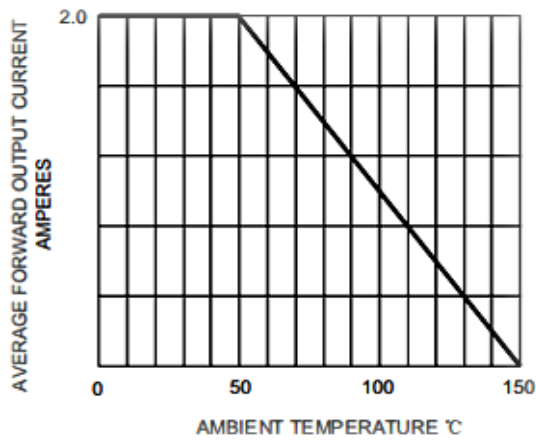


Fig 2: TYPICAL FORWARD
CHARACTERISTICS

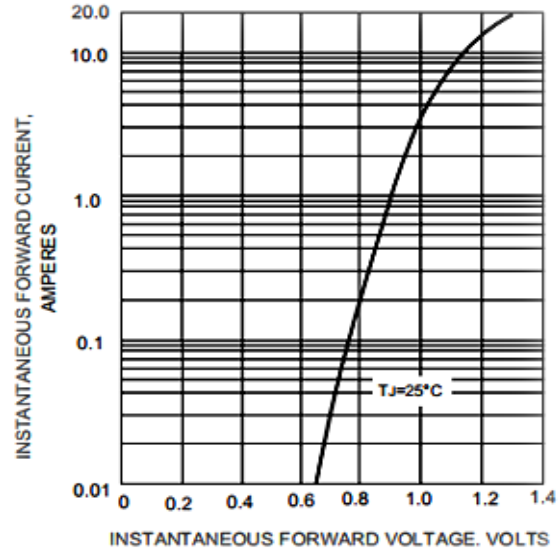


Fig 3: TYPIACL REVERSE CHARACTERISTICS

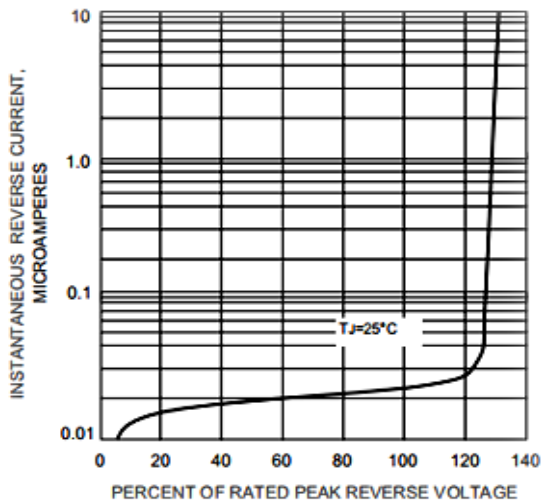
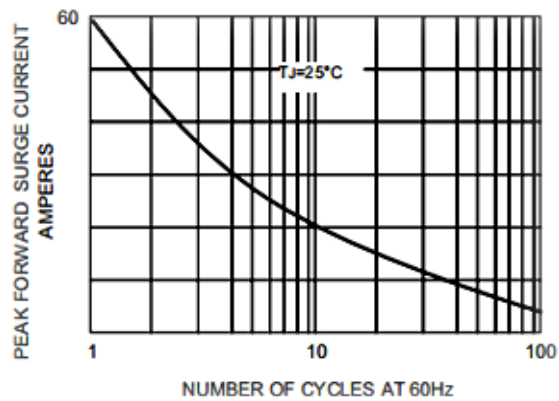


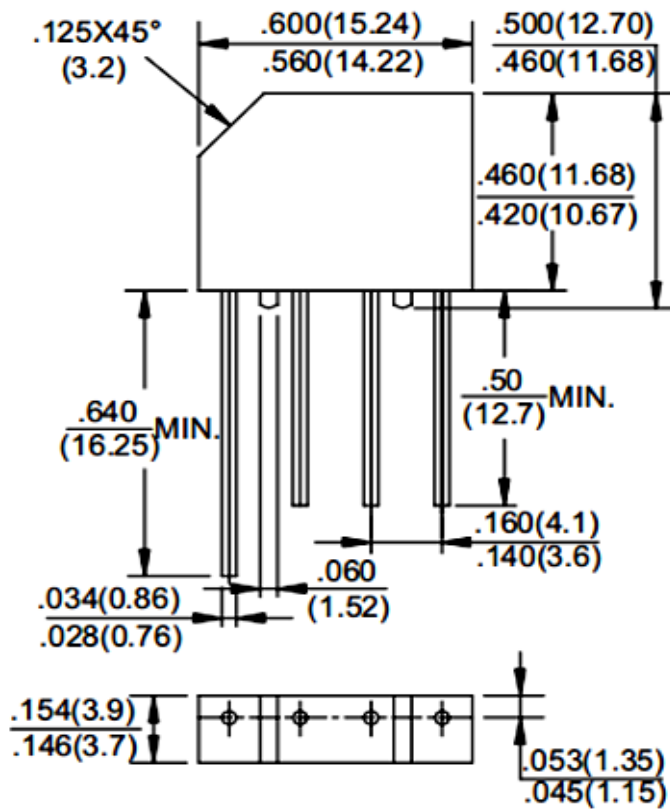
Fig 4: MAXIMUM FORWARD SURGE
CURRENT





PACKAGE DETAILS

KBP Leaded Plastic Package



All dimensions are in inches(mm)



Recommended Product Storage Environment for Discrete Semiconductor Devices

This storage environment assumes that the Diodes and transistors are packed properly inside the original packing supplied by CDIL.

- Temperature 5 °C to 30 °C
- Humidity between 40 to 70 %RH
- Air should be clean.
- Avoid harmful gas or dust.
- Avoid outdoor exposure or storage in areas subject to rain or water spraying .
- Avoid storage in areas subject to corrosive gas or dust. Product shall not be stored in areas exposed to direct sunlight.
- Avoid rapid change of temperature.
- Avoid condensation.
- Mechanical stress such as vibration and impact shall be avoided.
- The product shall not be placed directly on the floor.
- The product shall be stored on a plane area. They should not be turned upside down. They should not be placed against the wall.

Shelf Life of CDIL Products

The shelf life of products is the period from product manufacture to shipment to customers. The product can be unconditionally shipped within this period. The period is defined as 2 years.

If products are stored longer than the shelf life of 2 years the products shall be subjected to quality check as per CDIL quality procedure.

The products are further warranted for another one year after the date of shipment subject to the above conditions in CDIL original packing.

Floor Life of CDIL Products and MSL Level

When the products are opened from the original packing, the floor life will start.

For this, the following JEDEC table may be referred:

JEDEC MSL Level		
Level	Time	Condition
1	Unlimited	≤30 °C / 85% RH
2	1 Year	≤30 °C / 60% RH
2a	4 Weeks	≤30 °C / 60% RH
3	168 Hours	≤30 °C / 60% RH
4	72 Hours	≤30 °C / 60% RH
5	48 Hours	≤30 °C / 60% RH
5a	24 Hours	≤30 °C / 60% RH
6	Time on Label(TOL)	≤30 °C / 60% RH



Continental Device India Pvt. Limited

An IATF 16949, ISO9001 and ISO 14001 Certified Company



Customer Notes

Component Disposal Instructions

1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s). CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered trademark of

Continental Device India Pvt. Limited

C-120 Naraina Industrial Area, New Delhi 110 028, India.

Telephone +91-11-2579 6150, 4141 1112 Fax +91-11-2579 5290, 4141 1119

email@cdil.com www.cdil.com

CIN No. U32109DL1964PTC004291