



ZT13085E
Low Power 5V 2.5Mbps 256-Fanout RS485 Transceivers
Datasheet

Revision 2.20
Sep. 5th, 2017





IMPORTANT NOTICE

Copyright © 2012-2017 ASIX Electronics Corporation. All rights reserved.

DISCLAIMER

No part of this document may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, for any purpose, without the express written permission of ASIX. ASIX may make changes to the product specifications and descriptions in this document at any time, without notice.

ASIX provides this document “as is” without warranty of any kind, either expressed or implied, including without limitation warranties of merchantability, fitness for a particular purpose, and non-infringement.

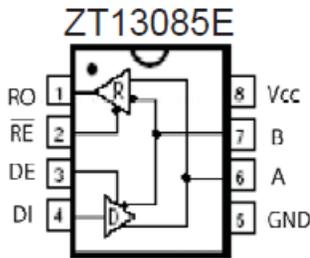
Designers must not rely on the absence or characteristics of any features or registers marked “reserved”, “undefined” or “NC”. ASIX reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. Always contact ASIX to get the latest document before starting a design of ASIX products.

TRADEMARKS

ASIX, the ASIX logo are registered trademarks of ASIX Electronics Corporation. All other trademarks are the property of their respective owners.

Features

- Meets or exceeds the requirements of ANSI Standard TIA/EIA-485-A and ISO 8482:1987(E) specifications for VCC at +5V ±10%
- Low quiescent current – 0.4mA typ., 0.9mA max.
- Low shutdown current (where applicable) - 1µA typical, 10µA max.
- Guaranteed standard data rate 2.5Mbps
- True Fail-Safe (Open, Short, Bus Idle) Receiver
- -7V to +12V common-mode input voltage range
- Half-Duplex mode configuration
- Allows up to 1/8 unit load (256 devices) on the same common bus
- Controlled driver output slew rate and receiver input filtering
- Active-high driver enable and active-low receiver enable
- ESD protection on bus terminals
±15KV Human Body Model (HBM)
- Alternative replacement for MAX3085E, MAX13082E, MAX13085E, SN75HVD3085, SN65HVD3085
- Standard fanout driving 1 unit load (32 devices) on the ZT485E Series are available options



General Description

The ZT13085E device is a 5V differential data line transceivers for RS485/RS422 communication that consist of one driver and one receiver with high level of ESD protection. They are designed for balanced transmission lines interface that meet ANSI standard TIA/EIA-485-A and ISO 8482:1987(E) specifications.

The ZT13085E series devices spans out with half duplex, data rate guaranteed at 2.5M bit per second allow one-eighth of an unit load that fan out 256 devices sharing a common bus. The I/Os are enhanced-electrostatic discharge (ESD) protected, exceeding ±15KV Human Body Model (HBM).

Applications

- RS422/RS485 communications
- Utility meters
- Industrial process control
- Building automation
- Level translators
- Transceivers for EMI-sensitive applications
- Routers and HUBs
- Industrial-controlled Local Area Networks
- Industrial PCs, embedded PCs and peripherals
- Industrial, security CATV and camera applications

Product Selection Guide and Cross Reference

Part Number	Duplex	# Of Tx/Rx	Data Rate (Mbps)	# of Tx/Rx on Bus	Slew Rate Limit	Rx Input Filtering	Low-Power Shutdown	Tx/Rx Enable	ESD on Tx/Rx	Package Types	Pin-to-Pin Cross Reference
ZT13085E	Half	1/1	2.5	256	No	No	Yes	Yes	± 15KV	8-PDIP, 8-nSOIC	MAX3085E, MAX13082E, MAX13085E, SN65HVD3085E



Absolute Maximum Ratings

These are stress ratings only and functional operation of the device at these ratings or any other above those indicated in the operation sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods of time may affect reliability.

Power Supply, (Vcc)-0.3V to +7.0V

Input Voltages

DI, DE, RE -0.3V to (Vcc +0.3V)
Differential Input Voltage, (VID) -12V to +12V
A, B (VI) -9V to +13V

Output Voltages

RO-0.3V to +6.0V
Y, Z (A & B on Half Duplex)-9V to +13.0V

Operating Temperature-40°C to +85°C
Storage Temperature-65°C to +150°C

Power Dissipation Per Package

8-pin PDIP (derate 9.09mW/°C above +70°C)722mW
8-pin nSOIC (derate 6.14mW/°C above +70°C)500mW

Storage Considerations

Storage in a low humidity environment is preferred. Large high density plastic packages are moisture sensitive and should be stored in Dry Vapor Barrier Bags. Prior to usage, the parts should remain bagged and stored below 40°C and 60%RH. If the parts are removed from the bag, they should be used within 168 hours or stored in an environment at or below 20%RH. If the above conditions cannot be followed, the parts should be baked for 12 hours at 125°C in order to remove moisture prior to soldering. ASIX ships product in Dry Vapor Barrier Bags with a humidity indicator card and desiccant pack. The humidity indicator should be below 30%RH. The MSL of this product is 3.

The information furnished by ASIX has been carefully reviewed for accuracy and reliability. Its application or use, however, is solely the responsibility of the user. No responsibility of the use of this information become part of the terms and conditions of any subsequent sales agreement with ASIX. Specifications are subject to change without the responsibility for any infringement of patents or other rights of third parties which may result from its use. No license or proprietary rights are granted by implication or otherwise under any patent or patent rights of ASIX Electronics Corporation.



1 DC Electrical Characteristics

Unless otherwise stated, $V_{CC} = +5.0V$, $T_A = T_{min}$ to T_{max} , typical values apply at $V_{CC} = +5.0V$ and $T_A = 25^\circ C$.

Parameter	Condition	Min	Typ	Max	Units
Operating Voltage & Temperature					
Temperature	Industrial Grade	-40	25	85	°C
V _{CC} Voltage Range	V _{CC} = +5.0V	4.5	5	5.5	V
Supply Current					
I _{CC} , Tx and Rx active	DI = V _{CC} /GND, DE = V _{CC} , \overline{RE} = GND, RS485 I/O = Open		400	900	μA
I _{CC} , Tx active	DI = V _{CC} /GND, DE = V _{CC} , \overline{RE} = V _{CC} , RS485 I/O = Open		400	900	μA
I _{CC} , Rx active	DI = V _{CC} /GND, DE = GND, \overline{RE} = GND, RS485 I/O = Open		400	900	μA
I _{SD} , Shutdown Current	DI = V _{CC} /GND, DE = GND, \overline{RE} = V _{CC} , RS485 I/O = Open		1	10	μA
TTL LOGIC Input, Driver					
Input Threshold Low, V _{IL}	V _{CC} = +5.0V, DE, DI, and \overline{RE}			0.8	V
Input Threshold High, V _{IH}	V _{CC} = +5.0V, DE, DI, and \overline{RE}	2			V
TTL LOGIC Output, Receiver					
Output Voltage Low, V _{OL}	I _{OUT} = +4mA, Input Differential Voltage = 200mV			0.4	V
Output Voltage High, V _{OH}	I _{OUT} = -4mA, Input Differential Voltage = 200mV	3.5			V
Output Leakage Current	Receiver Outputs Disabled, V _{OUT} = 0.4V to 2.4V			±1	μA
Short Circuit Current	V _{OUT} = 0V to V _{CC}			±95	mA
Receiver Input					
Input Current	DE = 0V, V _{CC} = 0V to 5.5V, V _{IN} = +12V			125	μA
	DE = 0V, V _{CC} = 0V to 5.5V, V _{IN} = -7V			-75	μA
Differential Threshold Voltage, V _{TH}	V _{CM} = 0V, V _{CC} = +5.0V, T _A = 25°C	-0.2		0.2	V
Input Hysteresis	V _{CM} = 0V		20		mV
Input Resistance, R _{IN}	V _{CM} = -7V to +12V	96			KΩ
Transmitter Output					
Differential Output Voltage, V _{OD1}	No Load			5	V
Differential Output Voltage, V _{OD2}	With R _L = 50Ω, Refer to Figure 1. (RS422)	2			V
	With R _L = 27Ω, Refer to Figure 1. (RS485)	1.5		5	V
Driver Common Mode Output, V _{OC}	With R _L = 27Ω or 50Ω. C _L = 50pF. Refer to Figure 3.			3	V
Change in Voltage Magnitude for Differential States, ΔV _{OD}	Differential Output Voltage, with R _L = 27Ω or 50Ω, C _L = 50pF Refer to Figure 1			0.2	V
Change in Voltage Magnitude for Common Mode States, ΔV _{OC}	Common-Mode Output Voltage, with R _L = 60Ω/375Ω/375Ω. Refer to Figure 2.			0.2	V
Transmitter Short-Circuit Current	V _{OUT} = -7V to +12V. Refer to Figure 7.	-250		250	mA

2 AC Electrical Characteristics

Unless otherwise stated, VCC = +5.0V, TA = T_{min} to T_{max}, typical values apply at VCC = +5.0V and TA = 25°C.

Parameter	Condition	Min	Typ	Max	Units
Transmitter Timing					
Transmitter Propagation t _{PLH}	R _{DIFF} = 54Ω, C _L = 50pF. Refer to Figure 4.		150	250	ns
Transmitter Propagation t _{PHL}	R _{DIFF} = 54Ω, C _L = 50pF. Refer to Figure 4.		150	250	ns
Transmitter Output Skew t _{SK}	t _{PLH} - t _{PHL}			40	ns
Transmitter Rise/Fall Time	t _r , t _f , R _{DIFF} = 54Ω, C _L = 50pF. Refer to Figure 4.		60	100	ns
Transmitter Output Enable	To Output HIGH, C _L = 50pF, R _L = 110Ω. Refer to Figure 5.		200		ns
	To Output LOW, C _L = 50pF, R _L = 110Ω. Refer to Figure 6.		200		ns
Transmitter Output Disable	From Output HIGH, C _L = 50pF, R _L = 110Ω. Refer to Figure 5.		200		ns
	From Output LOW, C _L = 50pF, R _L = 110Ω. Refer to Figure 6.		200		ns
Receiver Timing					
Receiver Propagation t _{PLH}	C _L = 15pF, Refer to Figure 9.			350	ns
Receiver Propagation t _{PHL}	C _L = 15pF, Refer to Figure 9.			350	ns
Differential Receiver Skew t _{SK}	t _{PLH} - t _{PHL}		10		ns
Receiver Output Enable	To Output HIGH, C _L = 15pF. Refer to Figure 10.		50		ns
	To Output LOW, C _L = 15pF. Refer to Figure 11.		50		ns
Receiver Output Disable	From Output HIGH, C _L = 15pF. Refer to Figure 10.		50		ns
	From Output LOW, C _L = 15pF. Refer to Figure 11.		50		ns
Shutdown Timing					
Time to Shutdown, t _{SHDN}			50	600	ns
Transmitter Enable from SHUTDOWN to Output HIGH	C _L = 50pF, R _L = 110Ω. Refer to Figure 5.		200		ns
Transmitter Enable from SHUTDOWN to Output LOW	C _L = 50pF, R _L = 110Ω. Refer to Figure 6.		200		ns
Receiver Enable from SHUTDOWN to Output HIGH	C _L = 15pF, R _L = 1KΩ. Refer to Figure 11.		200		ns
Receiver Enable from SHUTDOWN to Output LOW	C _L = 15pF, R _L = 1KΩ. Refer to Figure 11.		200		ns
Transceiver Throughput					
Maximum Data Rate	R _L = 54Ω, C _L = 50pF, T _A = 25°C.	2.5			Mbps
ESD Tolerance					
ESD HBM	RS485 Inputs and Outputs		±15		KV
ESD HBM ⁽¹⁾	All Pins		±4		KV

(1) Tested in accordance with JEDEC Standard 22, Test Method A114-A and IEC 60749-26

3 Pin Description

Pin Number	Pin Name	Pin Description
1	RO	Receiver Output. If A>B by 200mV, then RO = HIGH; If A<B by 200mV, then RO = LOW
2	\overline{RE}	Receiver Output Enable. Low active input. RO is high-Z when \overline{RE} = HIGH
3	DE	Driver Output Enable. The transmitter outputs, A and B, are enabled when DE = HIGH. The outputs are high-Z when DE = LOW
4	DI	Driver Input. A low on DI forces output A low and output B high. A high on DI will bring output A high and output B low
5	GND	Analog Ground
6	A	Non-inverting transmitter output and non-inverting receiver input.
7	B	Inverting transmitter output and inverting receiver input.
8	VCC	Power Supply Input, 5V \pm 10%

4 Circuit Description

The ZT13085E are low-power transceivers for RS-485 and RS-422 communications. The RS-485 standard is ideal for multi-drop applications and for long-distance interfaces. The TIA/EIA-485 specification allows up to 256 drivers and 256 receivers to be connected to a data bus, making it an ideal choice for multi-drop utility meter applications. RS-485 transceivers are equipped with a wide (-7V to +12V) common mode range to accommodate ground potential differences since the cabling can be as long as 4,000 feet. As RS-485 is a differential interface, data is virtually immune to noise in the transmission line.

The ZT13085E is slew-rate limited, minimizing EMI and reducing reflections caused by improperly terminated cables.

RS-485 Transmitters

The ZT13085E contains a differential output line transmitter that can drive voltage into multiple loads on a terminated two-wire pair, and a receiver that accepts a differential voltage down to 200mV. The transmitter's differential output can comply with RS-485 and also RS-422 standards. The typical voltage output swing with no load is 0V to VCC. With worst case loading of 54 ohms across the differential outputs, the drivers can maintain greater than 1.5V voltage levels, which is more than adequate for a differential receiver to acknowledge a logic state. The 54 ohms is the equivalent of two 120 ohm termination resistors placed on each side of the transmission line and the input impedance of 256 receivers on the line.

The ZT13085E transmitter has an enable control line which is active HIGH. A logic HIGH on DE (pin 3) will enable the differential outputs. A logic LOW on DE (pin 3) will disable the transmitter outputs. While disabled, the transmitter outputs are in high impedance.

RS-485 Receivers

Each transceiver contains one differential receiver that has an input sensitivity of 200mV. The input impedance of the receivers is typically 96K ohms. A wide common mode range of -7V to +12V allows for large ground potential differences between systems.

The ZT13085E receivers have an enable control input. A logic LOW on \overline{RE} will enable the receiver, a logic HIGH on \overline{RE} will disable the receiver. The receivers are equipped with the true fail-safe feature, which guarantees that the receiver output to be in a HIGH-IMPEDANCE state when the input is left unconnected. When the receiver inputs are either open or short circuit, the receiver output will be in a HIGH state when \overline{RE} enable is LOW.

The ZT13085E transmits and receives at data rates up to 2.5Mbps.

Bus Configuration

The ZT13085E is configured as half-duplex RS485/RS422 transceivers.

Half duplex is a configuration where the transmitter outputs are connected to its receiver inputs. This application is common for two-wire interfaces where either the transmitter is active or the receiver is active. It is common to connect the enable inputs for the transmitter and receiver together so that a logic HIGH will enable the transmitter and disable the receiver. Conversely, a logic LOW will disable the transmitter and enable the receiver. Half-duplex configurations and these devices are designed for bidirectional data transmission on multipoint twisted-pair cables for applications, such as utility meters, digital motor controllers, remote sensors and terminals, industrial process control, security stations and environmental control systems.

ESD Immunity

Electro-Static Discharge (ESD) is an important factor when implementing a serial port into a system, especially in harsh environmental conditions. These industrial strength devices provide extra protection against ESD and are intended for harsh environments where high-speed data communication is important.

All of the ZT13085E family of transceivers incorporates internal protection structures on all pins to protect against ESD charges encountered during handling and assembly. The driver outputs and receiver inputs have extra protection against static electricity as they are directly interfacing to the outside environment. As such, these pins are protected against ESD of $\pm 15\text{KV}$ without damage in all states of the transceiver's operation in the static state. After multiple ESD events, ASIX's ZT13085E family of transceivers keep working without latchup. These devices eliminate the need for external transient suppressor diodes and the associated high capacitance loading, allowing reliable high-speed data communications.

The Human Body Model has been the generally accepted ESD testing method for semiconductors. This test is intended to simulate the human body's potential to store electrostatic energy and discharge it to an integrated circuit upon close proximity or contact. This method will test the IC's capability to withstand an ESD transient during normal handling such as in manufacturing areas where the ICs tend to be handled frequently.

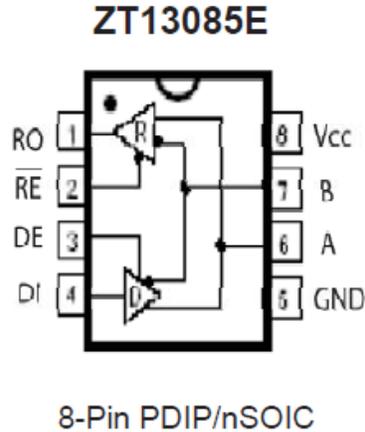
5 Function Table

DRIVER				RECEIVER		
Input DI	Enable DE	Outputs		Differential Inputs $V_{ID} = V_A - V_B$	Enable \overline{RE}	Output RO
		A	B			
H	H	H	L	$V_{ID} \leq -0.2V$	L	L
L	H	L	H	$-0.2V < V_{ID} < +0.2V$	L	U
X	L	Z	Z	$+0.2V \leq V_{ID}$	L	H
X	L	Z	Z	X	H	Z*
Open	H	H	L	X	H	Z
X	Open	Z	Z	Open circuit	L	H
				Short circuit	L	H
				X	Open	Z

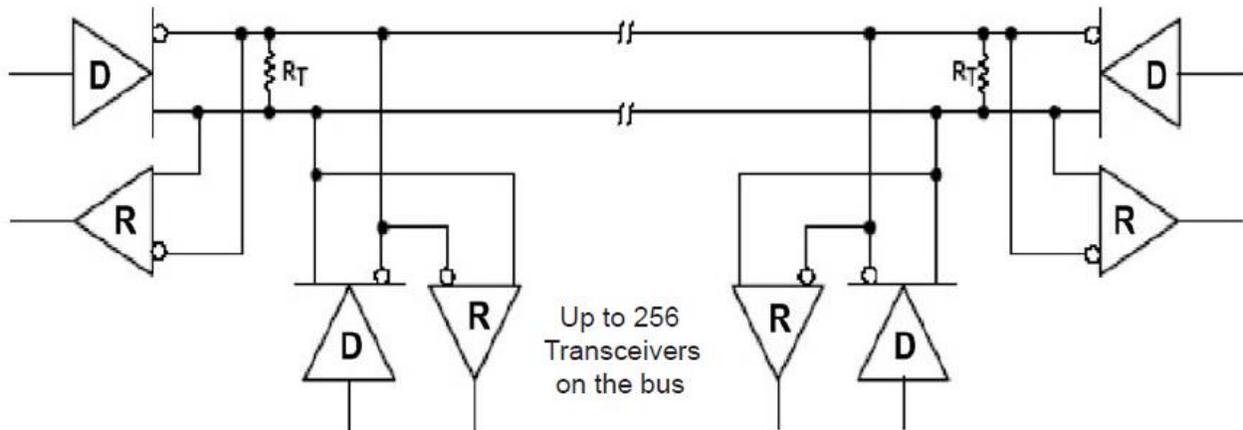
Note:

H = High Level; L = Low Level; Z = High Impedance; X = Irrelevant; U = Undetermined State; Z* = Shutdown.

6 Pin Configuration



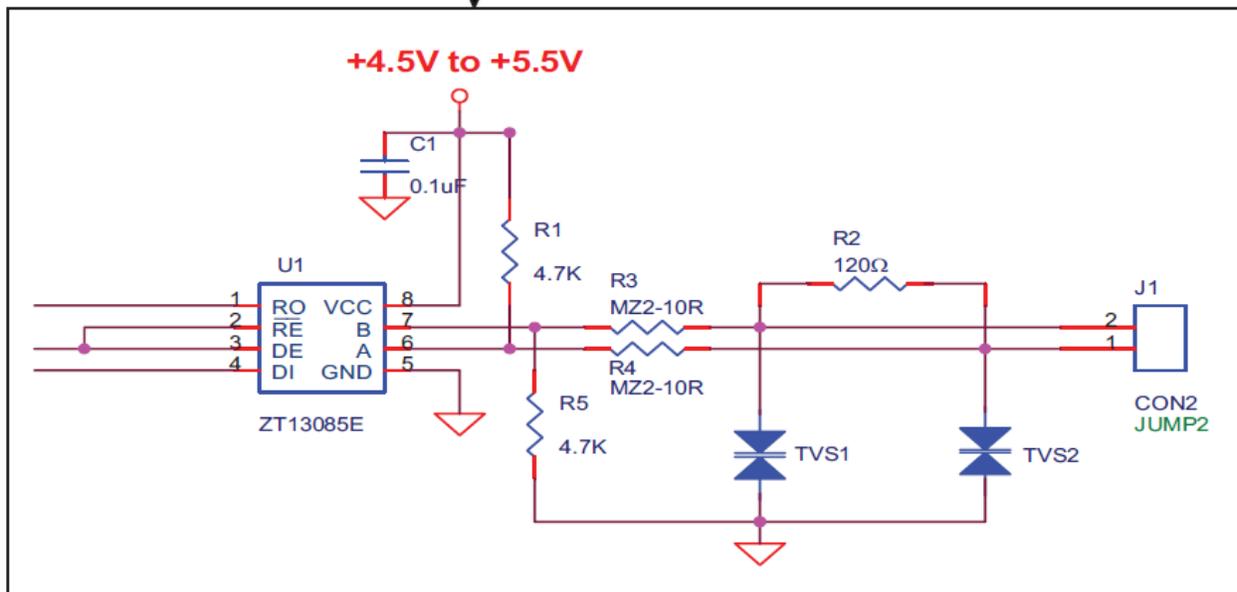
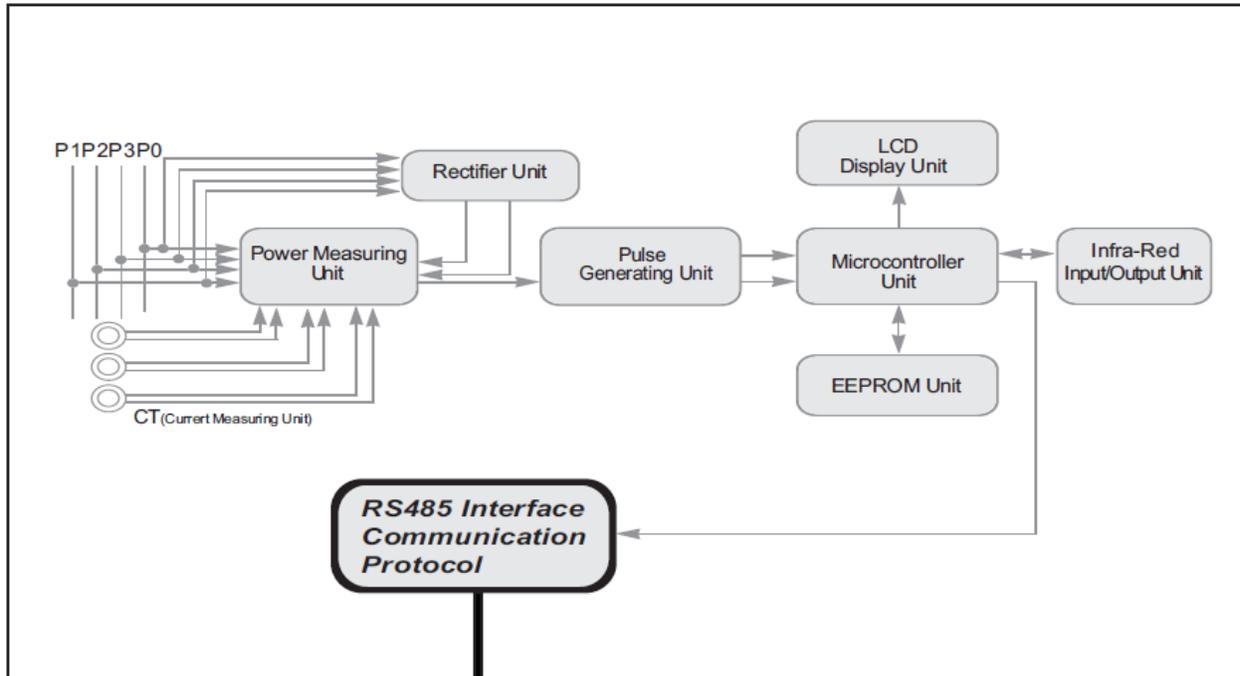
7 Typical Application Circuits



Notes:

- A. The bus should be terminated at both ends in its characteristic impedance of $R_T = Z_0$.
- B. Stub lengths off the main bus should be kept as short as possible.
- C. Can connect up to 256 devices on the same common bus.

Example Circuit Schematics for Utility Meter



8 Typical Test Circuits

Notes:

- A. The test load capacitance includes probe and test jig capacitance, unless otherwise specified.
- B. The signal generator had the following characteristics:
 Pulse rate = 1000 KHz, 50% duty cycle, $Z_0 = 50\ \Omega$, t_r & $t_f < 6\text{ns}$, unless otherwise specified.

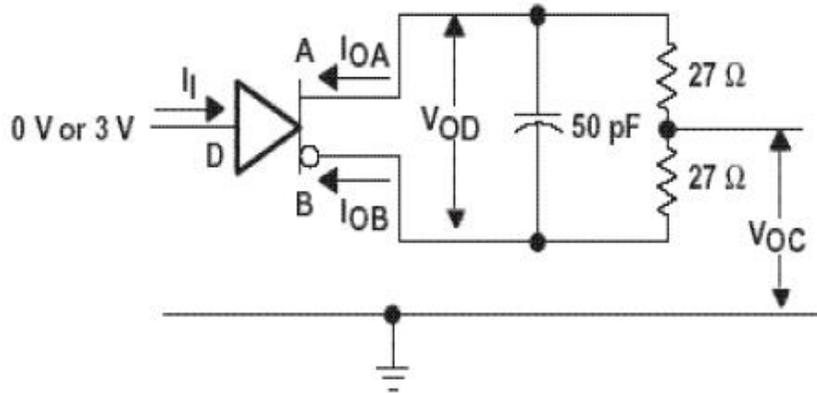


Figure 1. Driver Test Circuit, VOD and VOC Without Common-Mode Loading

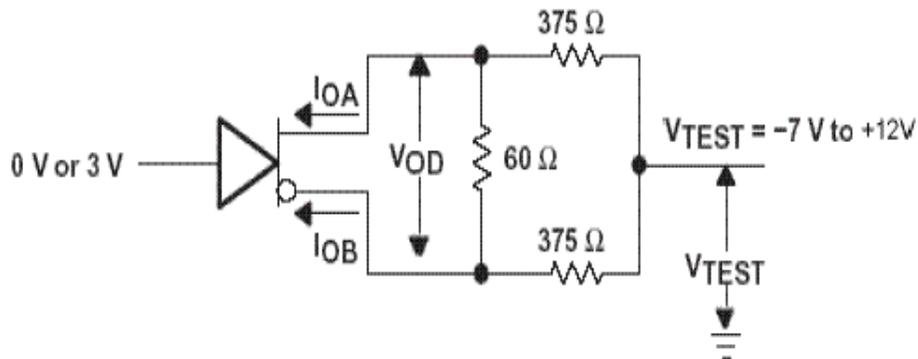


Figure 2. Driver Test Circuit, VOD With Common-Mode Loading

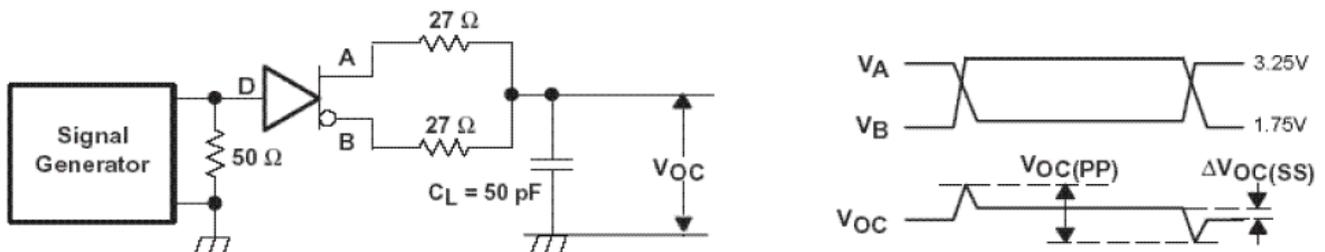


Figure 3. Driver Common-Mode Output Voltage (VOC) Test Circuit and Waveforms

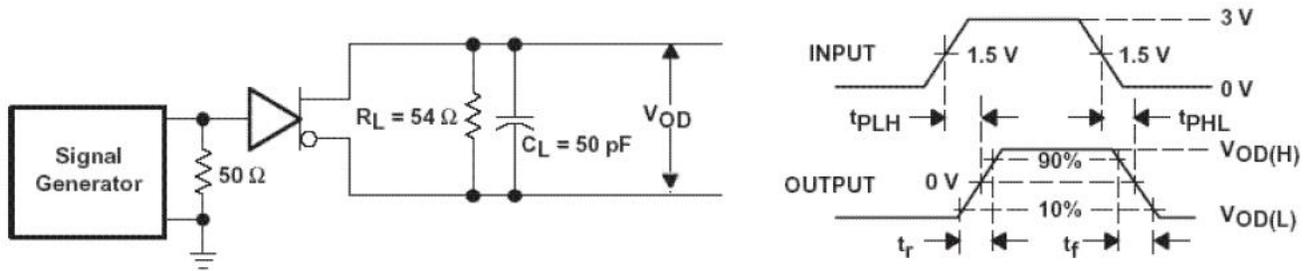


Figure 4. Driver Differential Output Voltage (VOD) Switching Test Circuit and Waveforms

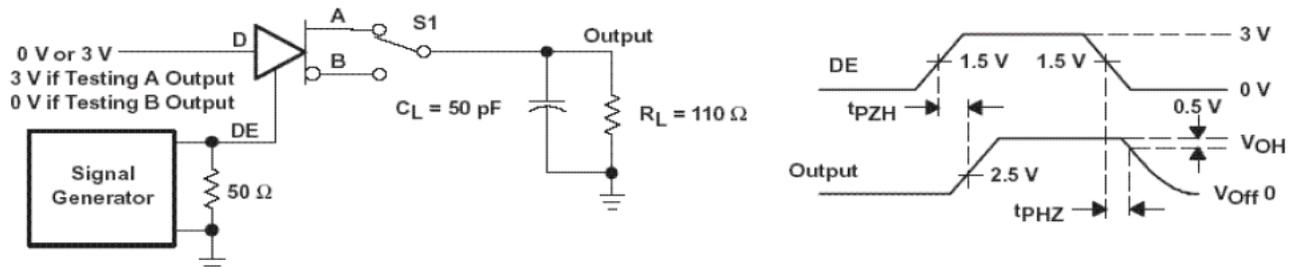


Figure 5. Driver Enable/Disable Test Circuit and Waveforms, High Output

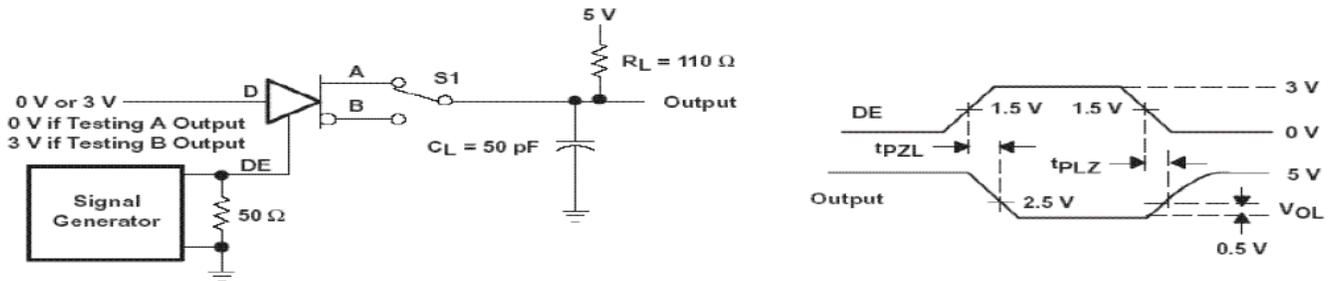


Figure 6. Driver Enable/Disable Test Circuit and Waveforms, Low Output

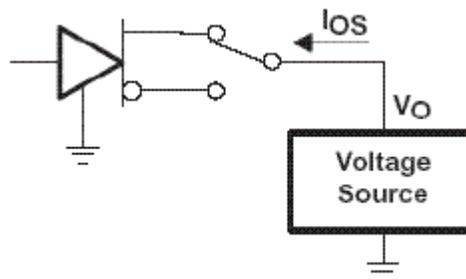


Figure 7. Driver Short-Circuit Test Configuration

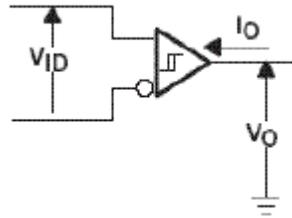


Figure 8. Receiver Parameter Definitions

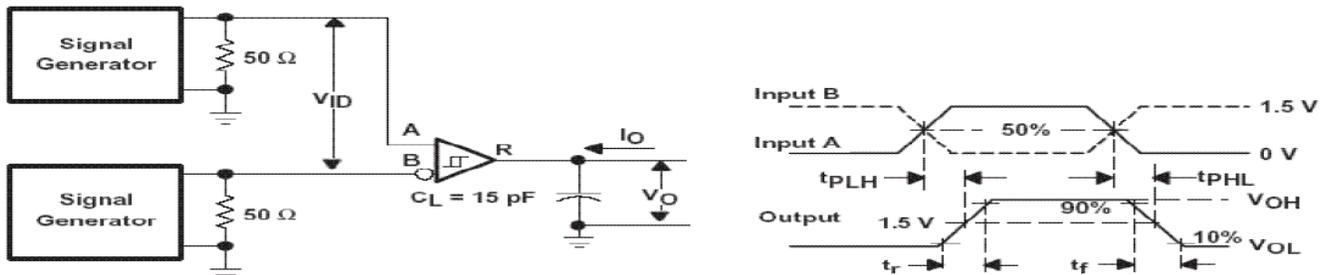


Figure 9. Receiver Propagation (t_{PLH} and t_{PHL}) Test Circuit and Waveforms

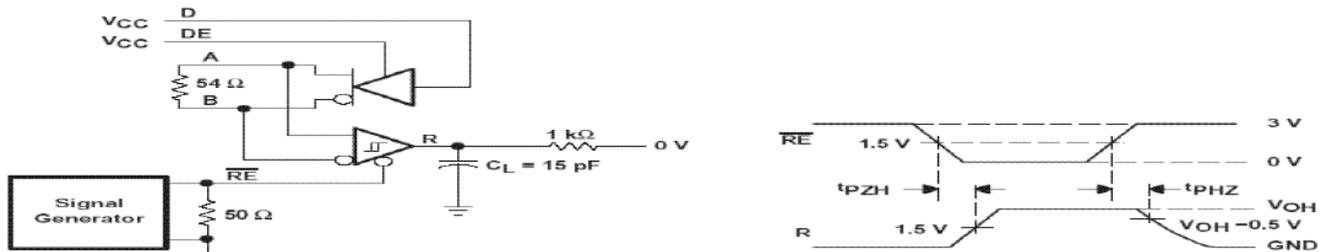


Figure 10. Receiver Output Enable/Disable Test Circuit and Waveforms, Data Output High

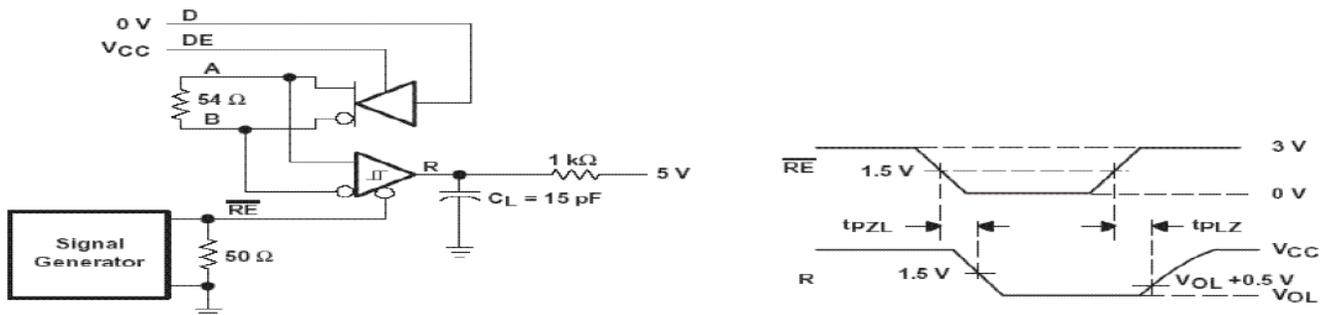
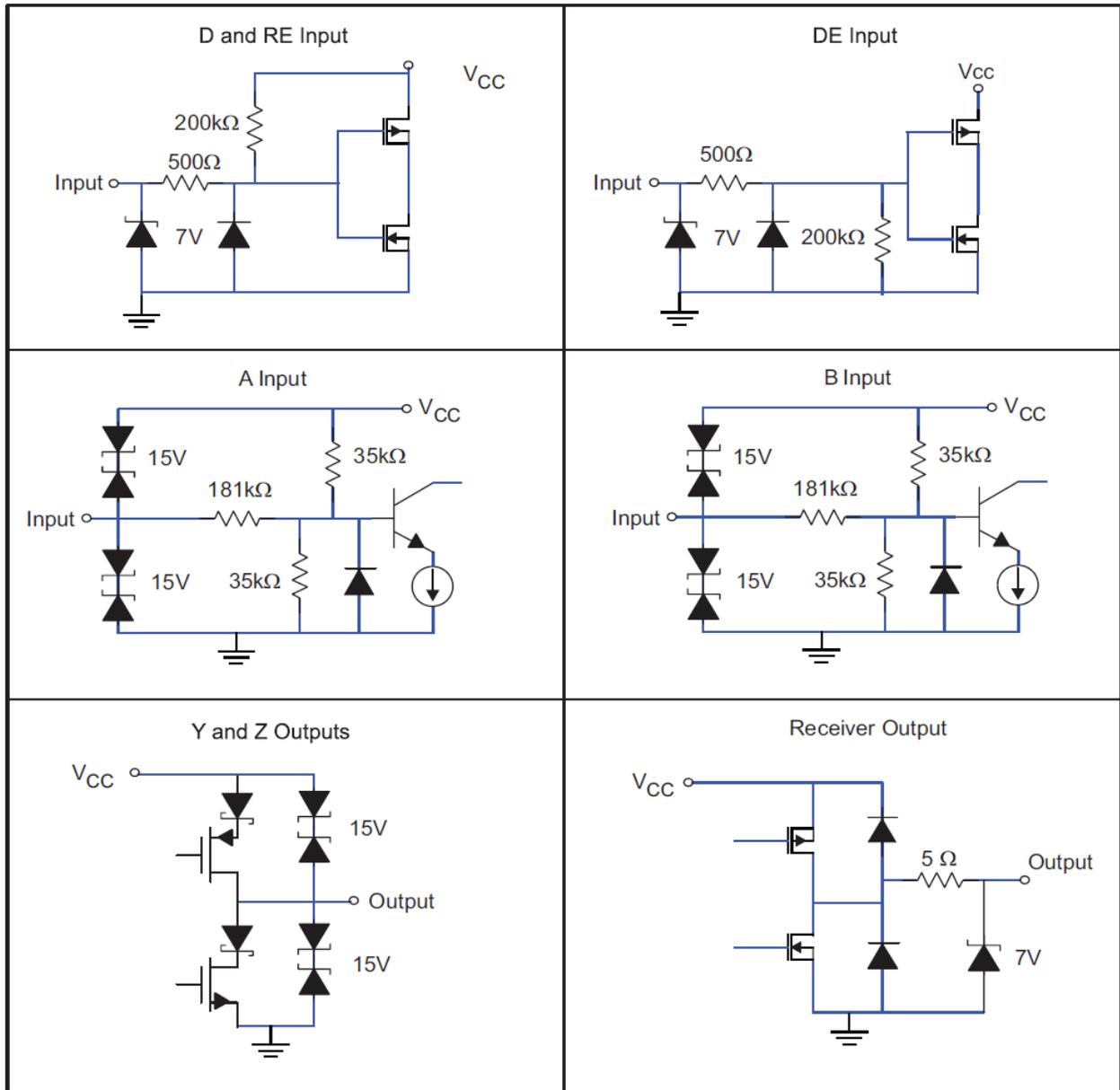
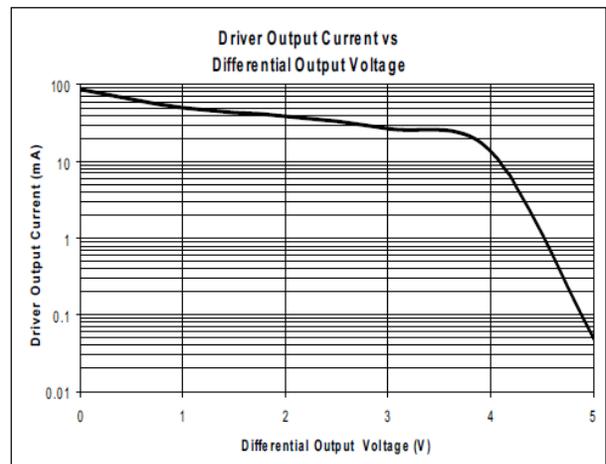
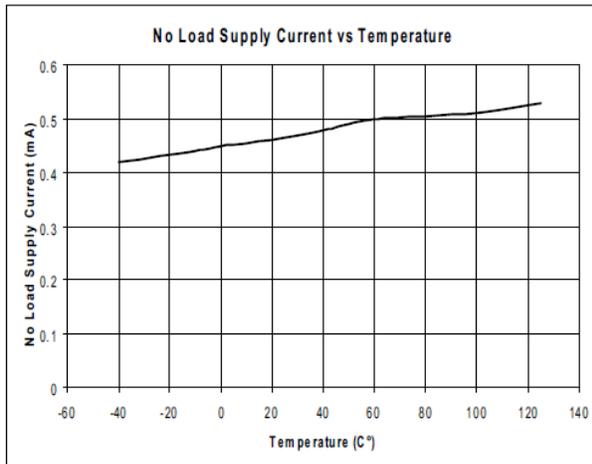
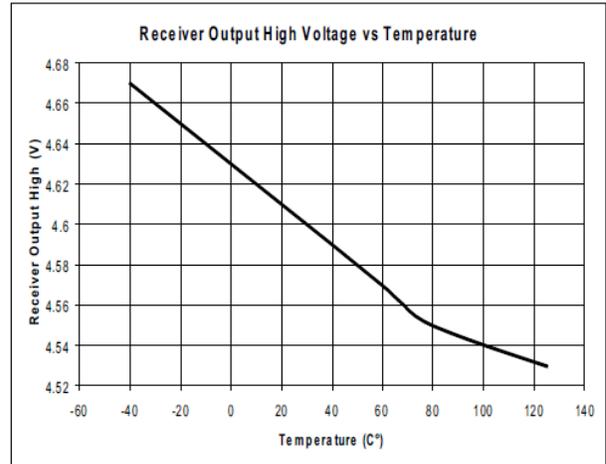
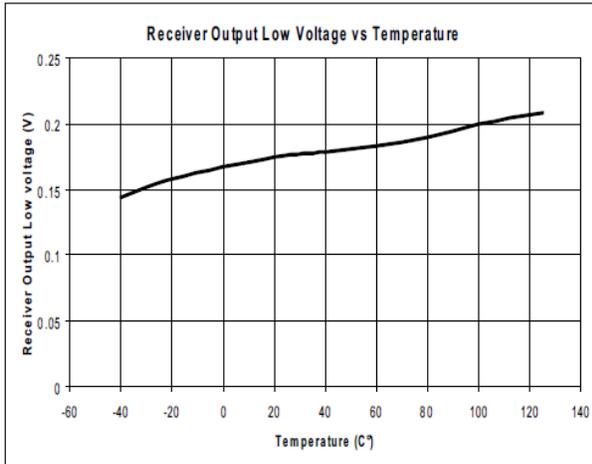
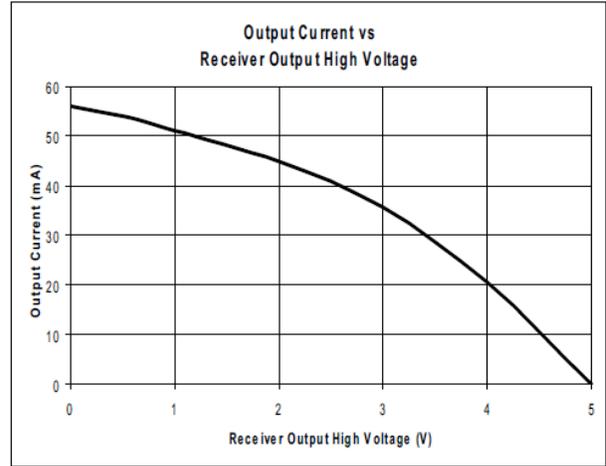
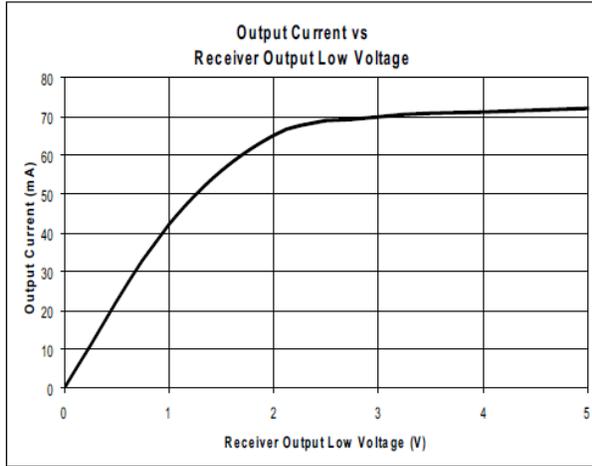


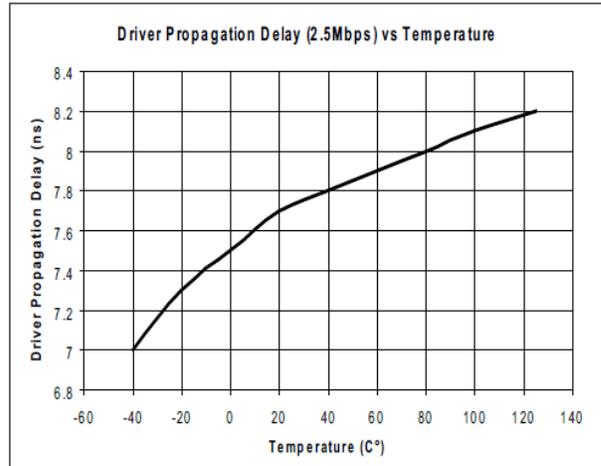
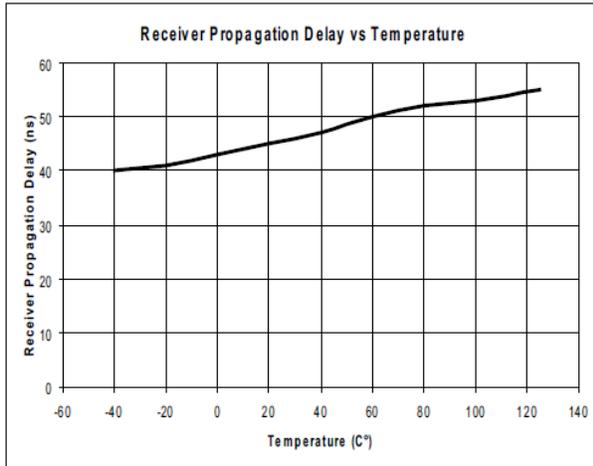
Figure 11. Receiver Output Enable/Disable Test Circuit and Waveforms, Data Output Low

9 Equivalent Input and Output Schematic Diagrams

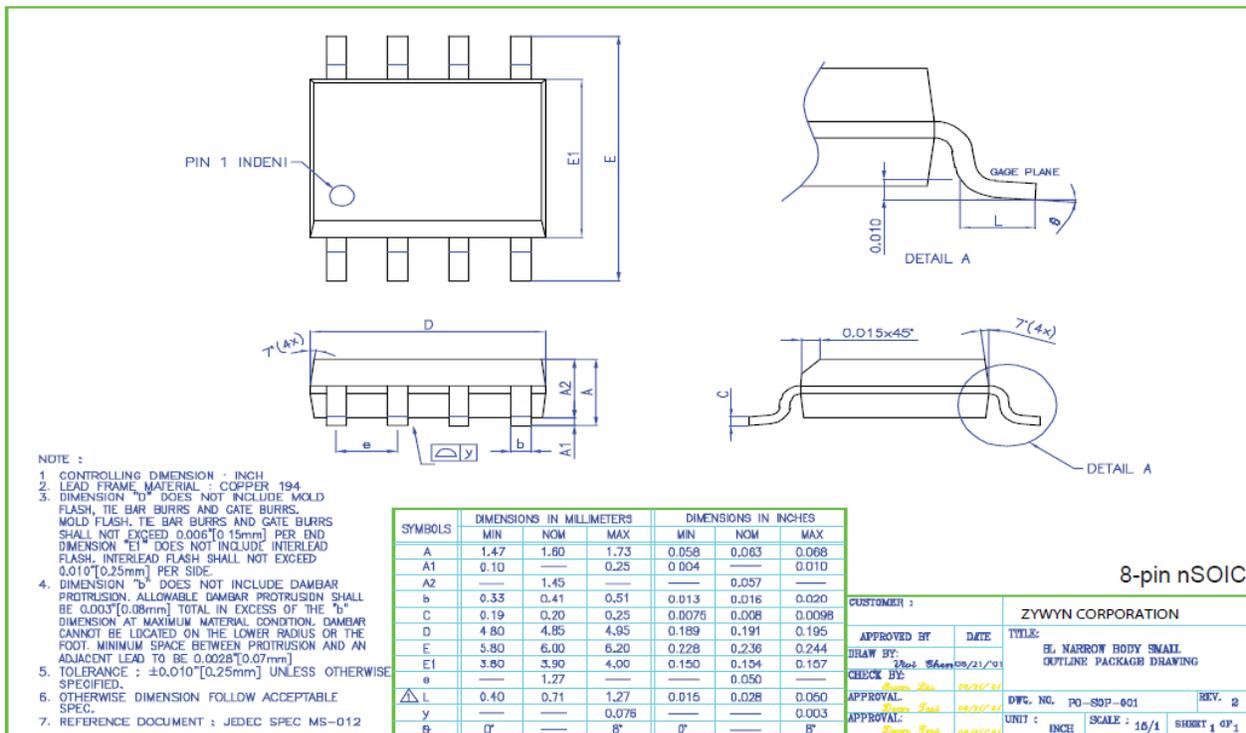
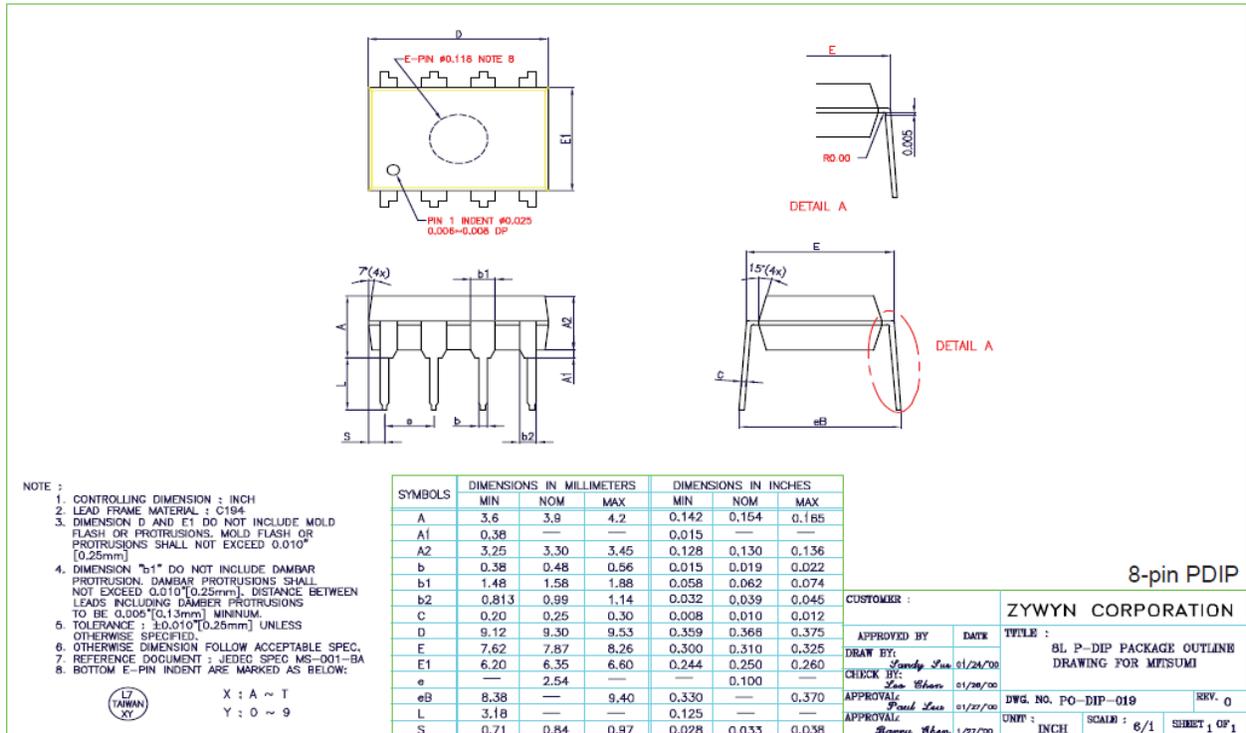


10 Typical Performance Characteristics

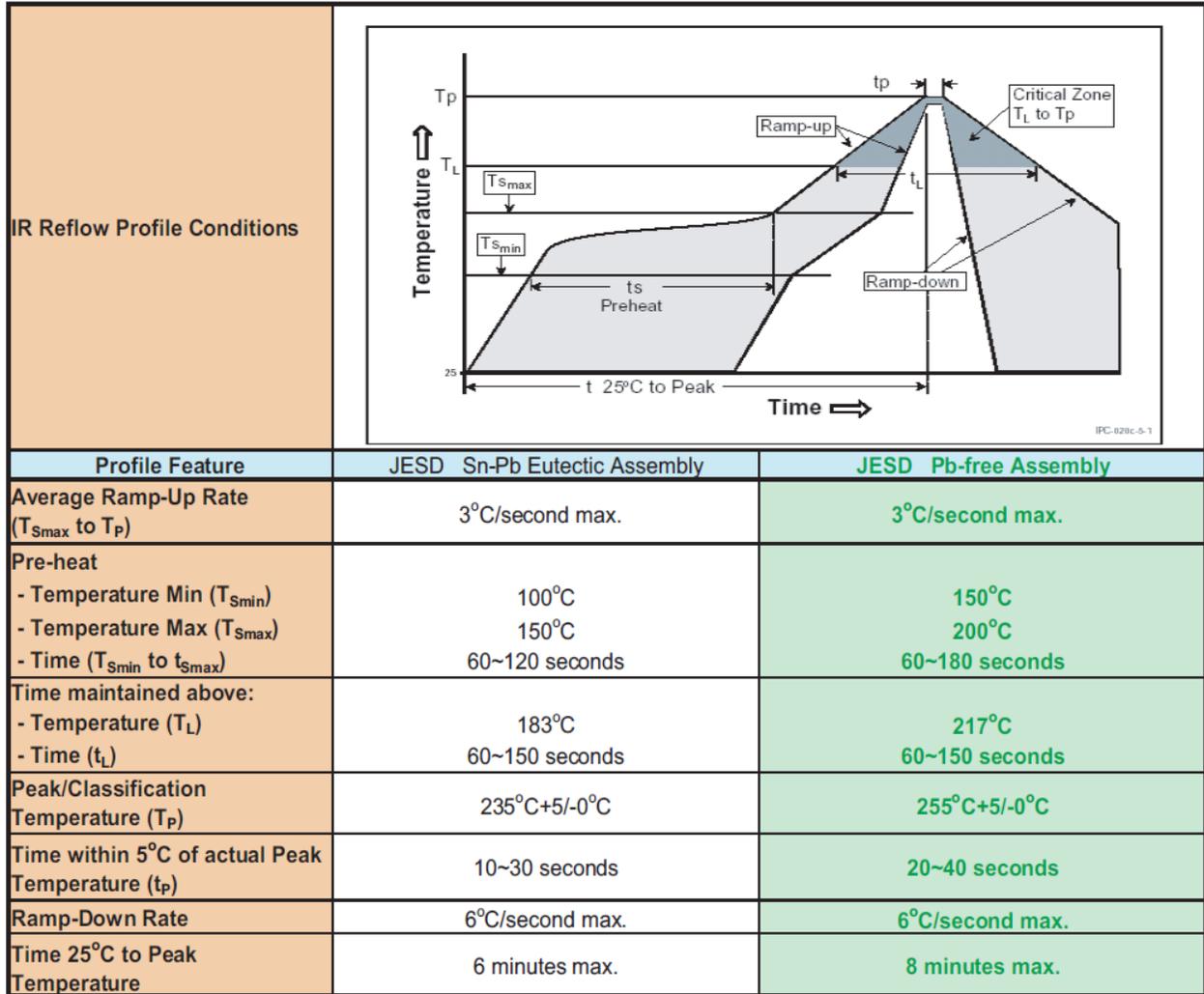




11 Package Information



12 Green Package SMD IR Reflow Profile Information



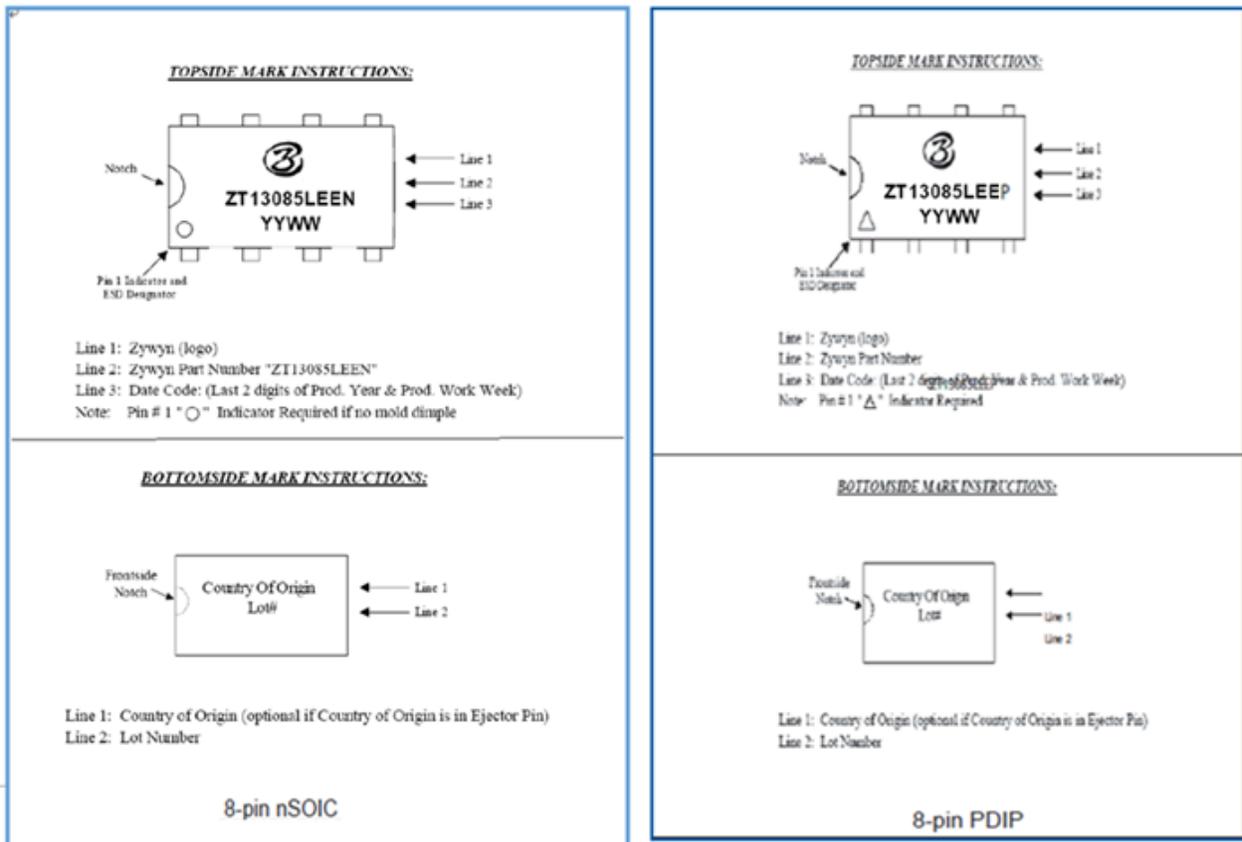
ASIX Green Packages are Pb-free and RoHS compliance.

13 Ordering Information

Part Number	Temperature Range	Package Type	Green Package (Pb-Free & RoHS)	MOQ /Tube	MOQ /T&R
ZT13085LEEN	-40°C to +85°C	8-pin nSOIC		100	2500
ZT13085LEEP	-40°C to +85°C	8-pin PDIP		60	N/A

Please contact the factory for pricing, availability on Tape-and-Reel and Die Sales options.

14 Part Marking Information



Revision History

Revision	Date	Comment
V1.4	2012/06/01	Initial release to customers
V2.00	2015/04/28	<ol style="list-style-type: none">1. Changed to ASIX Electronics Corp. logo, strings and contact information.2. Added ASIX copyright legal header information.3. Modified the Revision History table format.
V2.10	2015/05/13	<ol style="list-style-type: none">1. Modified some information in Feature, Section 1 and 8.
V2.20	2017/09/05	<ol style="list-style-type: none">1. Updated the Marking Information in Section 14.



ZT13085E

Low Power 5V 2.5Mbps 256-Fanout RS485 Transceivers



4F, No. 8, Hsin Ann Rd., HsinChu Science Park,
HsinChu, Taiwan, R.O.C.

TEL: 886-3-5799500

FAX: 886-3-5799558

Sales Email: sales@asix.com.tw

Support Email: support@asix.com.tw

Web: <http://www.asix.com.tw>