

General Description

The FDMS0312AS use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics. This device is specially designed to get better ruggedness and suitable to use in

General Features

V_{DS} =30V l_D =60A

 $R_{DS(ON)} < 5.8m\Omega @ V_{GS}=10V$

Applications

Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications

Package Marking and Ordering Information

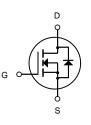
Product ID	Pack	Brand	Qty(PCS)
FDMS0312AS	DFN5X6-8L(DFN-8L(5x6))	HXY MOSFET	5000

Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
Vds	Drain-Source Voltage	V	
Vgs	Gate-Source Voltage	±20	V
I₀@Tc=25°C	Continuous Drain Current, V _{GS} @ 10V	60	A
I _D @T _C =100°C	Continuous Drain Current, V _{GS} @ 10V	38	А
Ідм	Pulsed Drain Current ²	135	А
EAS	Single Pulse Avalanche Energy ³	29.8	mJ
P₀@Tc=25°C	Total Power Dissipation ⁴	30	W
Тятд	Storage Temperature Range -55 to 150		°C
TJ	Operating Junction Temperature Range	-55 to 150	°C
R _θ JC	Thermal Resistance from Junction-to-Ambient ³ 4.6		°C/W
R _{θJA}	Thermal Resistance Junction-Ambient ¹	50	°C/W



DFN5X6-8L (DFN-8L(5x6))



N-Channel MOSFET



Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit	
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	30			V	
Rds(on)	Static Drain-Source On-Resistance ²	V _{GS} =10V , I _D =20A		4.4	5.8	mΩ	
		V _{GS} =4.5V , I _D =15A		6.9	9		
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2		2.5	V	
1	Drain-Source Leakage Current	V _{DS} =24V , V _{GS} =0V , T _J =25℃			1	uA	
IDSS		V _{DS} =24V , V _{GS} =0V , T _J =55℃			5		
lgss	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA	
gfs	Forward Transconductance	V _{DS} =5V , I _D =20A		67		S	
Rg	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.7		Ω	
Qg	Total Gate Charge (4.5V)			8			
Qgs	Gate-Source Charge	V _{DS} =15V , V _{GS} =4.5V , I _D =15A		2.4		nC	
Q_{gd}	Gate-Drain Charge			3.2			
T _{d(on)}	Turn-On Delay Time			7.1			
Tr	Rise Time	V_{DD} =15V , V_{GS} =10V , R_G =3.3 Ω		40		ns	
T _{d(off)}	Turn-Off Delay Time	I _D =15A		15			
Tf	Fall Time			6			
Ciss	Input Capacitance			814			
Coss	Output Capacitance	V _{DS} =15V , V _{GS} =0V , f=1MHz		498		pF	
Crss	Reverse Transfer Capacitance			41		I	
ls	Continuous Source Current ^{1,6}	$V_G=V_D=0V$, Force Current			60	А	
V _{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25℃			1	V	
trr	Reverse Recovery Time	IF=20A , di/dt=100A/μs ,		15		nS	
Qrr	Reverse Recovery Charge	TJ=25℃		25		nC	

Electrical Characteristics (T_J = 25°C, unless otherwise noted)

Note :

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width \leq 300us , duty cycle \leq 2%

3. The EAS data shows Max. rating . The test condition is $V_{\text{DD}}\text{=}25V, V_{\text{GS}}\text{=}10V, L\text{=}0.1\text{mH}, I_{\text{AS}}\text{=}24\text{A}$

4. The power dissipation is limited by 150°C junction temperature 5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.



Typical Characteristics

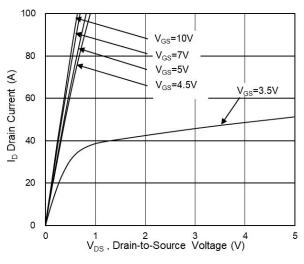


Fig.1 Typical Output Characteristics

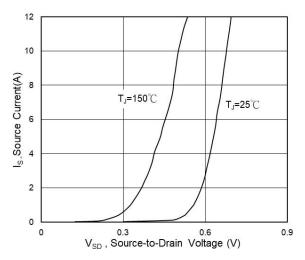
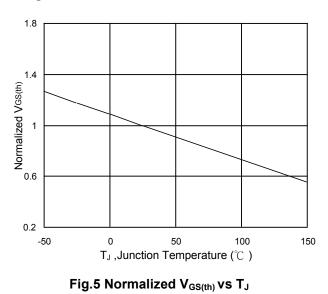


Fig.3 Source Drain Forward Characteristics



 $\begin{array}{c}
12 \\
10 \\
\hline
CB \\
\hline
Cg \\
4 \\
2 \\
4 \\
6 \\
V_{GS}(V)
\end{array}$ $l_{D}=20A$ $l_{$

Fig.2 On-Resistance vs G-S Voltage

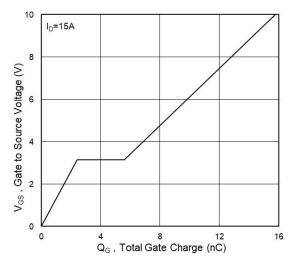
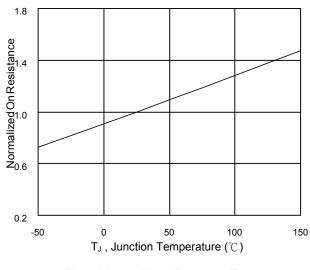
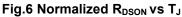
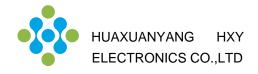
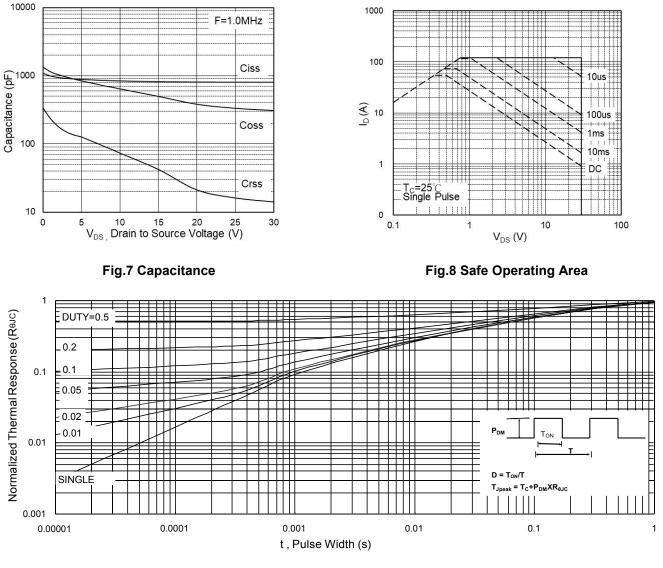


Fig.4 Gate-Charge Characteristics











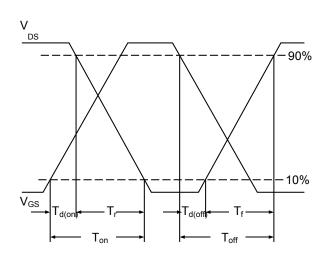


Fig.10 Switching Time Waveform

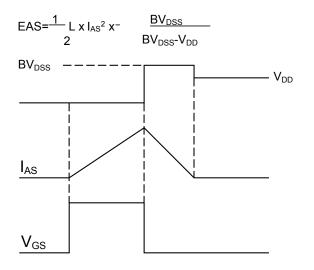
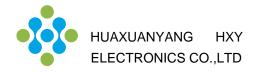
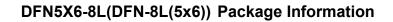
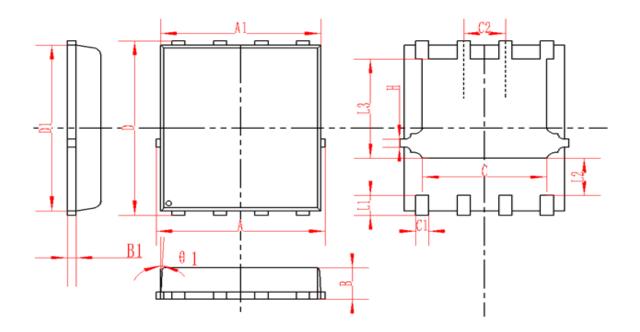


Fig.11 Unclamped Inductive Switching Waveform







SYMBOL		MM			INCH		
	MIN	NOM	MAX	MIN	NOM	MAX	
А	4.95	5	5.05	0.195	0.197	0.199	
A1	4.82	4.9	4.98	0.190	0.193	0.196	
D	5.98	6	6.02	0.235	0.236	0.237	
D1	5.67	5.75	5.83	0.223	0.226	0.230	
В	0.9	0.95	1	0.035	0.037	0.039	
B1	0.254REF		0.010REF				
С	3.95	4	4.05	0.156	0.157	0.159	
C1	0.35	0.4	0.45	0.014	0.016	0.018	
C2		1.27TYP			0.5TYP		
θ1	8°	10°	12°	8°	10°	12°	
L1	0.63	0.64	0.65	0.025	0.025	0.026	
L2	1.2	1.3	1.4	0.047	0.051	0.055	
L3	3.415	3.42	3.425	0.134	0.135	0.135	
Н	0.24	0.25	0.26	0.009	0.010	0.010	



Attention

Any and all HUA XUAN YANG ELECTRONICS products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your HUA XUAN YANG ELECTRONICS representative nearest you before using any HUA XUAN YANG ELECTRONICS products described or contained herein in such applications.

• HUA XUAN YANG ELECTRONICS assumes no responsibility for equipment failures that result from using products at values that exceed, even momentarily, rated values (such as maximum ratings, operating condition ranges, or other parameters) listed in products specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein.

• Specifications of any and all HUA XUAN YANG ELECTRONICS products described or contained herein stipulate the performance, characteristics, and functions of the described products in the independent state, and are not guarantees of the performance, characteristics, and functions of the described products as mounted in the customer's products or equipment. To verify symptoms and states that cannot be evaluated in an independent device, the customer should always evaluate and test devices mounted in the customer's products or equipment.

■ HUA XUAN YANG ELECTRONICS CO.,LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could

give rise to accidents or events that could endanger human lives, that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

• In the event that any or all HUA XUAN YANG ELECTRONICS products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

• No part of this publication may be reproduced or transmitted in any form or by any means, electronic or mechanical, including photocopying and recording, or any information storage or retrieval system, or otherwise, without the prior written permission of HUA XUAN YANG ELECTRONICS CO.,LTD.

Information (including circuit diagrams and circuit parameters) herein is for example only ; it is not guaranteed for volume production.
HUA XUAN YANG ELECTRONICS believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.

Any and all information described or contained herein are subject to change without notice due to product/technology improvement, etc. When designing equipment, refer to the "Delivery Specification" for the HUA XUAN YANG ELECTRONICS product that you intend to use.