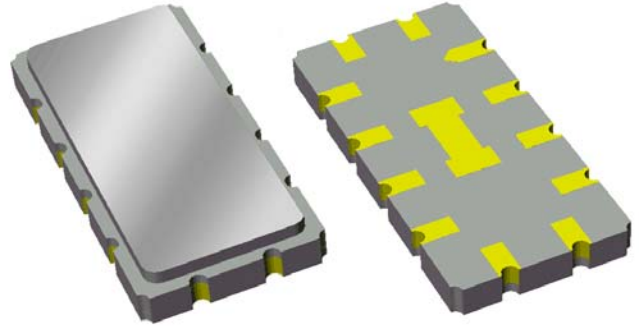


Data Sheet

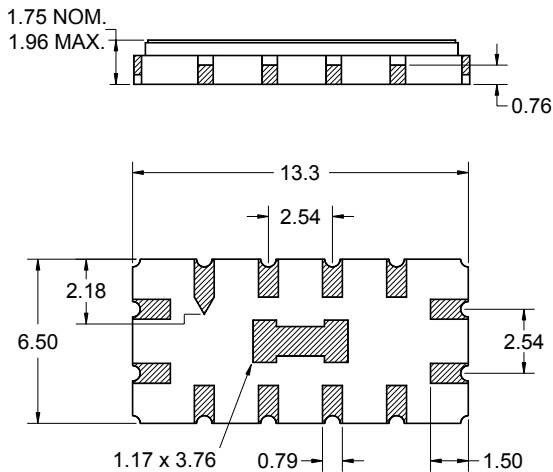
Features

- Standard 70 MHz family – low loss
- Typical 3 dB bandwidth of 20 MHz
- Low loss
- High attenuation
- Single-ended operation
- Ceramic Surface Mount Package (SMP)
- Small size



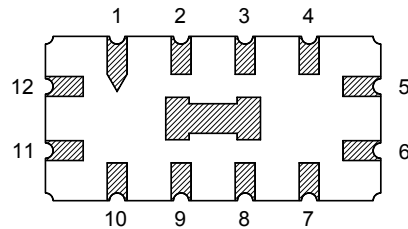
Package

Surface Mount 13.30 x 6.50 x 1.75 mm



Pin Configuration

Bottom View



Pin No.	Description
5	Output
6	Output return
11	Input
12	Input return
1,2,3,4	Ground
7,8,9,10	Ground

Dimensions shown are nominal in millimeters
 All tolerances are ± 0.15 mm except overall
 length and width ± 0.10 mm

Body: Al_2O_3 ceramic
 Lid: Kovar, Ni plated
 Terminations: Au plating 0.5 - 1.0 μ m,
 over a 2 - 6 μ m Ni plating

Data Sheet

Electrical Specifications ⁽¹⁾

Operating Temperature: ⁽²⁾ +25 °C

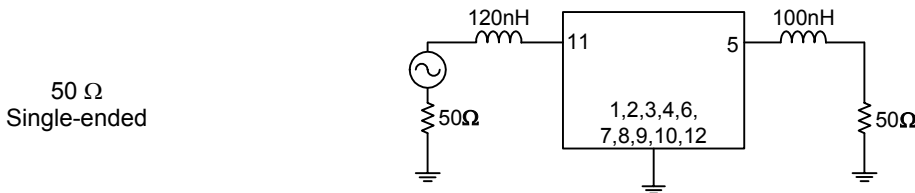
Parameter	Minimum	Typical	Maximum	Unit
Center Frequency	69.8	70	70.2	MHz
Insertion Loss at F _o	-	14.5	15	dB
1 dB Bandwidth	18.9	19.3	-	MHz
3 dB Bandwidth	20	20.35	-	MHz
40 dB Bandwidth	-	25.4	26.1	MHz
Passband Ripple	-	0.7	1	dB p-p
Phase Linearity (90% of 3dB Bandwidth)	-	8.25	11.25	deg
Group Delay Variation (90% of 3dB Bandwidth)	-	65	90	nsec
Absolute Delay	-	1.12	-	μsec
Substrate Material	-	YZ LiNbO ₃	-	-
Temperature Coefficient ⁽²⁾	-	-94	-	ppm/°C
Source Impedance ⁽³⁾	-	50	-	Ω
Load Impedance ⁽³⁾	-	50	-	Ω

Notes:

1. All specifications are based on the test circuit shown below
2. Device will function over the storage temperature range (-40 to +85 deg C). However, specifications are only guaranteed at the operating temperature of 25 deg C. The temperature coefficient will define the frequency shift over temperature.
3. This is the optimum impedance in order to achieve the performance shown

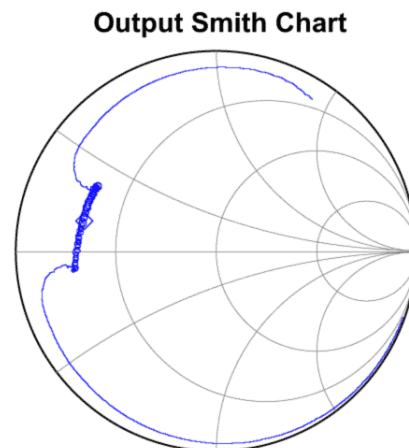
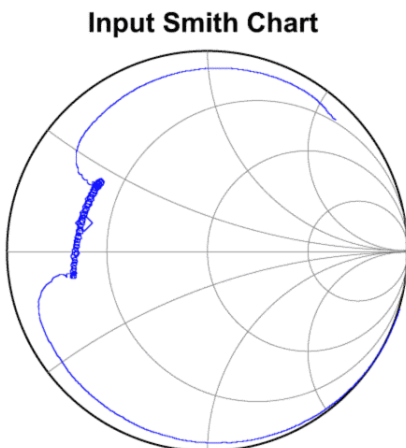
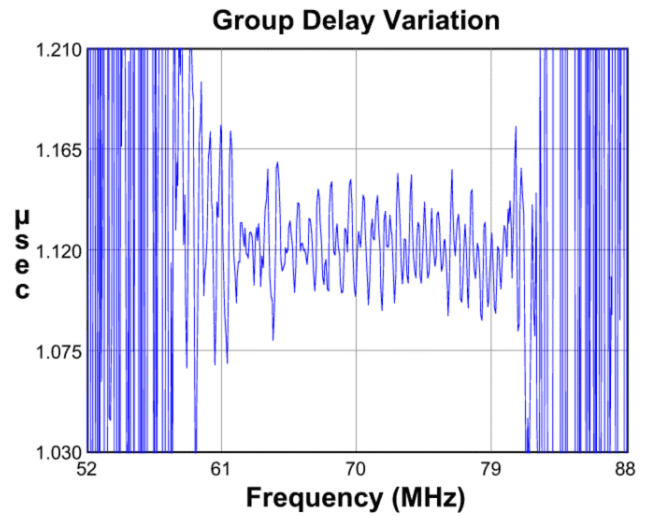
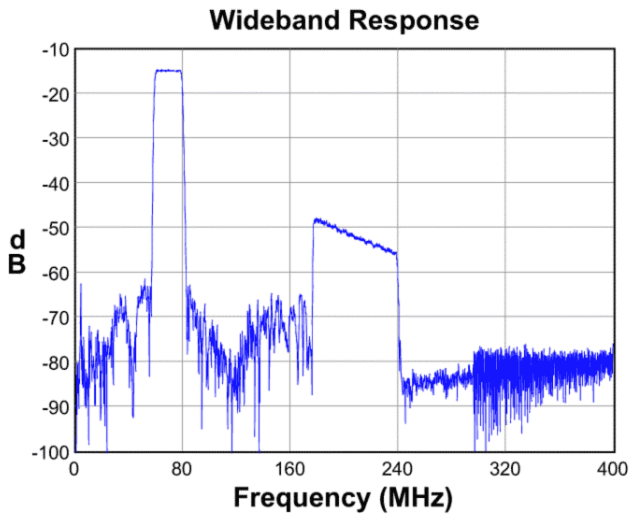
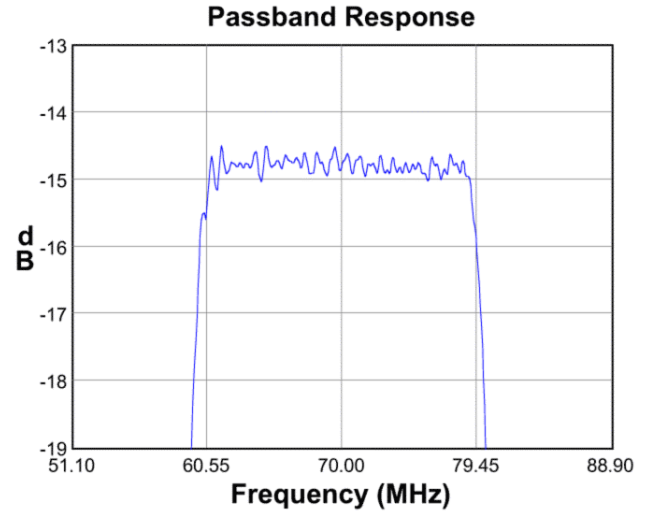
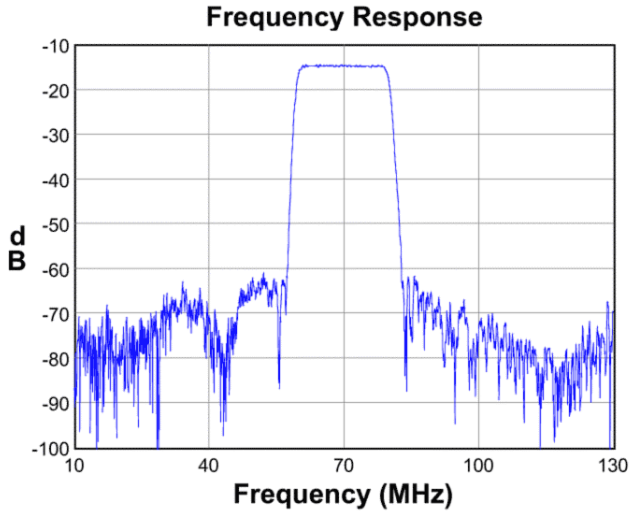
Test Circuit:

Actual matching values may vary due to PCB layout and parasitics



Data Sheet

Typical Performance (at +25°C)




Data Sheet

Maximum Ratings

Parameter	Symbol	Minimum	Typical	Maximum	Unit
Operating Temperature Range	T	-	+25	-	°C
Storage Temperature Range	T _{stg}	-40	-	+85	°C
RF Power	P _{in}	-	-	+10	dBm

Warnings

- Electrostatic Sensitive Device (ESD) 
- Avoid ultrasonic exposure

Links to Additional Technical Information

[PCB Layout Tips](#)

[Qualification Flowchart](#)

[Soldering Profile](#)

[S-Parameters](#)

[Other Technical Information](#)

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Contact Information



PO Box 609501
 Orlando, FL 32860-9501
 USA

Phone: +1 (407) 886-8860
 Fax: +1 (407) 886-7061
 Email: custservice@sawtek.com
 Web: www.sawtek.com

Or contact one of our worldwide network of [sales offices](#), [representatives](#) or [distributors](#)

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