TinyLogic ULP-A Dual Buffer (Open-Drain Output)

NC7WV07

The NC7WV07 is a dual buffer with open−drain output in tiny footprint packages. The device is designed to operate for V_{CC} = 0.9 V to 3.6 V.

Features

- Designed for 0.9 V to 3.6 V V_{CC} Operation
- 1.6 ns t_{PD} at 3.3 V (Typ)
- Inputs/Outputs Over−Voltage Tolerant up to 3.6 V
- I_{OFF} Supports Partial Power Down Protection
- Source/Sink 24 mA at 3.3 V
- Available in SC−88 and MicroPak™ Packages
- These Devices are Pb−Free, Halogen Free/BFR Free and are RoHS Compliant

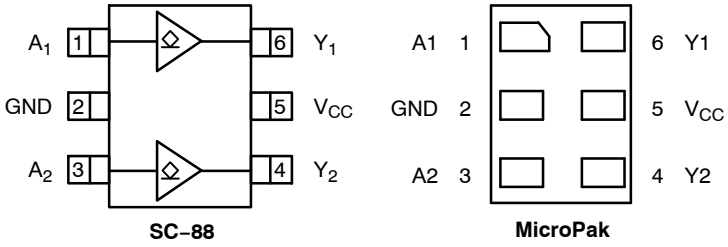


Figure 1. Pinout Diagrams (Top Views)

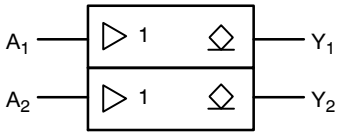
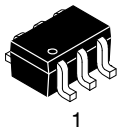
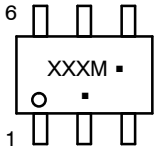


Figure 2. Logic Symbol

MARKING DIAGRAMS



SC−88
CASE 419AD

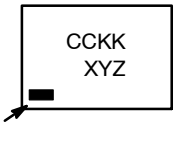


XXX = Specific Devic Code
M = Date Code*
▪ = Pb−Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or position may vary depending upon manufacturing location.

SIP6 1.45X1.0
MicroPak
CASE 127EB



CC = Specific Device Code
KK = 2−Digit Lot Run Traceability Code
XY = 2−Digit Date Code
Z = Assembly Plant Code

ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 6 of this data sheet.

NOTE: Some of the devices on this data sheet have been **DISCONTINUED**. Please refer to the table on page 6.

PIN ASSIGNMENT

Pin	Function
1	A1
2	GND
3	A2
4	Y2
5	V_{CC}
6	Y1

FUNCTION TABLE

A Input	Y Output
L	L
H	Z

Z = High Impedance

MAXIMUM RATINGS

Symbol	Characteristics	Value	Unit
V_{CC}	DC Supply Voltage	−0.5 to +4.3	V
V_{IN}	DC Input Voltage	−0.5 to +4.3	V
V_{OUT}	DC Output Voltage Active−Mode (High or Low State) Tri−State Mode (Note 1) Power−Down Mode (V_{CC} = 0 V)	−0.5 to V_{CC} + 0.5 −0.5 to +4.3 −0.5 to +4.3	V
I_{IK}	DC Input Diode Current V_{IN} < GND	−50	mA
I_{OK}	DC Output Diode Current V_{OUT} < GND	−50	mA
I_{OUT}	DC Output Source/Sink Current	±50	mA
I_{CC} or I_{GND}	DC Supply Current per Supply Pin or Ground Pin	±50	mA
T_{STG}	Storage Temperature Range	−65 to +150	°C
T_L	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
T_J	Junction Temperature Under Bias	+150	°C
θ_{JA}	Thermal Resistance (Note 2) SC−88 MicroPak	377 154	°C/W
P_D	Power Dissipation in Still Air SC−88 MicroPak	332 812	mW
MSL	Moisture Sensitivity	Level 1	−
F_R	Flammability Rating Oxygen Index: 28 to 34	UL 94 V−0 @ 0.125 in	−
V_{ESD}	ESD Withstand Voltage (Note 3) Human Body Model Charged Device Model	4000 2000	V
$I_{Latchup}$	Latchup Performance (Note 4)	±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Applicable to devices with outputs that may be tri−stated.
2. Measured with minimum pad spacing on an FR4 board, using 10 mm−by−1 inch, 2 ounce copper trace no air flow per JESD51−7.
3. HBM tested to EIA / JESD22−A114−A. CDM tested to JESD22−C101−A. JEDEC recommends that ESD qualification to EIA/JESD22−A115A (Machine Model) be discontinued.
4. Tested to EIA/JESD78 Class II.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V_{CC}	Positive DC Supply Voltage	0.9	3.6	V
V_{IN}	DC Input Voltage	0	3.6	V
V_{OUT}	DC Output Voltage Active−Mode (High or Low State) Tri−State Mode (Note 5) Power−Down Mode (V_{CC} = 0 V)	0 0 0	V_{CC} 3.6 3.6	
T_A	Operating Temperature Range	−40	+85	°C
t_r , t_f	Input Transition Rise and Fall Time V_{CC} = 3.3 V ± 0.3 V	0	10	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

5. Applicable to devices with outputs that may be tri−stated.

DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V_{CC} (V)	T_A = 25°C			T_A = −40°C to +85°C		Unit
				Min	Typ	Max	Min	Max	
V_{IH}	High−Level Input Voltage		0.9	−	0.5	−	−	−	V
			1.1 to 1.3	0.65 x V_{CC}	−	−	0.65 x V_{CC}	−	
			1.4 to 1.6	0.65 x V_{CC}	−	−	0.65 x V_{CC}	−	
			1.65 to 1.95	0.65 x V_{CC}	−	−	0.65 x V_{CC}	−	
			2.3 to <2.7	1.6	−	−	1.6	−	
			2.7 to 3.6	2.0	−	−	2.0	−	
V_{IL}	Low−Level Input Voltage		0.9	−	0.5	−	−	−	V
			1.1 to 1.3	−	−	0.35 x V_{CC}	−	0.35 x V_{CC}	
			1.4 to 1.6	−	−	0.35 x V_{CC}	−	0.35 x V_{CC}	
			1.65 to 1.95	−	−	0.35 x V_{CC}	−	0.35 x V_{CC}	
			2.3 to <2.7	−	−	0.7	−	0.7	
			2.7 to 3.6	−	−	0.8	−	0.8	

DC ELECTRICAL CHARACTERISTICS (continued)

Symbol	Parameter	Condition	V_{CC} (V)	T_A = 25°C			T_A = −40°C to +85°C		Unit
				Min	Typ	Max	Min	Max	
V_{OL}	Low−Level Output Voltage	V_{IN} = V_{IH} or V_{IL}							V
		I_{OL} = 100 μA	0.9	–	0.1	–	–	–	
			1.1 to 1.3	–	–	0.1	–	0.1	
			1.4 to 1.6	–	–	0.1	–	0.1	
			1.65 to 1.95	–	–	0.2	–	0.2	
			2.3 to < 2.7	–	–	0.2	–	0.2	
			2.7 to 3.6	–	–	0.2	–	0.2	
		I_{OL} = 2 mA	1.1 o 1.3	–	–	0.25 x V_{CC}	–	0.25 x V_{CC}	
		I_{OL} = 4 mA	1.4 to 1.6	–	–	0.25 x V_{CC}	–	0.25 x V_{CC}	
		I_{OL} = 6 mA	1.65 to 1.95	–	–	0.3	–	0.3	
		I_{OL} = 12 mA	2.3 to <2.7	–	–	0.4	–	0.4	
			2.7 to 3.6	–	–	0.4	–	0.4	
		I_{OL} = 18 mA	2.3 to <2.7	–	–	0.6	–	0.6	
			2.7 to 3.6	–	–	0.4	–	0.4	
		I_{OL} = 24 mA	2.7 to 3.6	–	–	0.55	–	0.55	
I_{IN}	Input Leakage Current	V_{IN} = 0 V to 3.6 V	0.9 to 3.6	–	–	±0.1	–	±0.5	μA
I_{OFF}	Power Off Leakage Current	V_{IN} = 0 V to 3.6 V or V_{OUT} = 0 V to 3.6 V	0	–	–	0.5	–	0.5	μA
I_{CC}	Quiescent Supply Current	V_{IN} = V_{CC} or GND	0.9 to 3.6	–	–	0.9	–	0.9	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

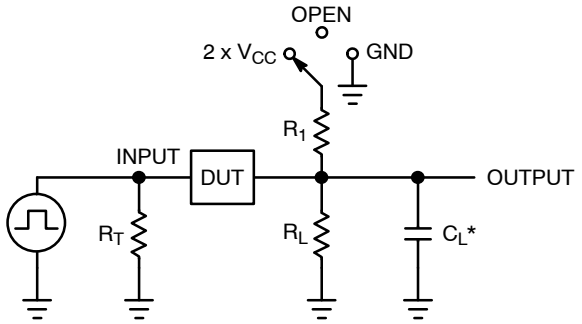
AC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V_{CC} (V)	T_A = 25°C			T_A = −40°C to +85°C		Unit
				Min	Typ	Max	Min	Max	
t_{PZL}, t_{PLZ}	Propagation Delay, A to Y (Figures 3 and 4)	R1 = R_L = 1 kΩ, C_L = 30 pF	0.9	–	15.9	–	–	–	ns
			1.1 to 1.3	–	5.4	15.6	–	18.6	
			1.4 to 1.6	–	3.6	8.7	–	9.7	
			1.65 to 1.95	–	2.4	6.0	–	6.8	
			2.3 to 2.7	–	1.9	3.6	–	4.7	
			2.7 to 3.6	–	1.6	3.3	–	4.0	

CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Test Condition	Typ (T_A = 25°C)	Unit
C_{IN}	Input Capacitance	V_{CC} = 0 V	2.0	pF
C_{OUT}	Output Capacitance	V_{CC} = 0 V	6.5	pF
C_{PD}	Power Dissipation Capacitance (Note 6)	f = 10 MHz, V_{CC} = 0.9 to 3.6 V, V_{IN} = 0 V or V_{CC}	10.0	pF

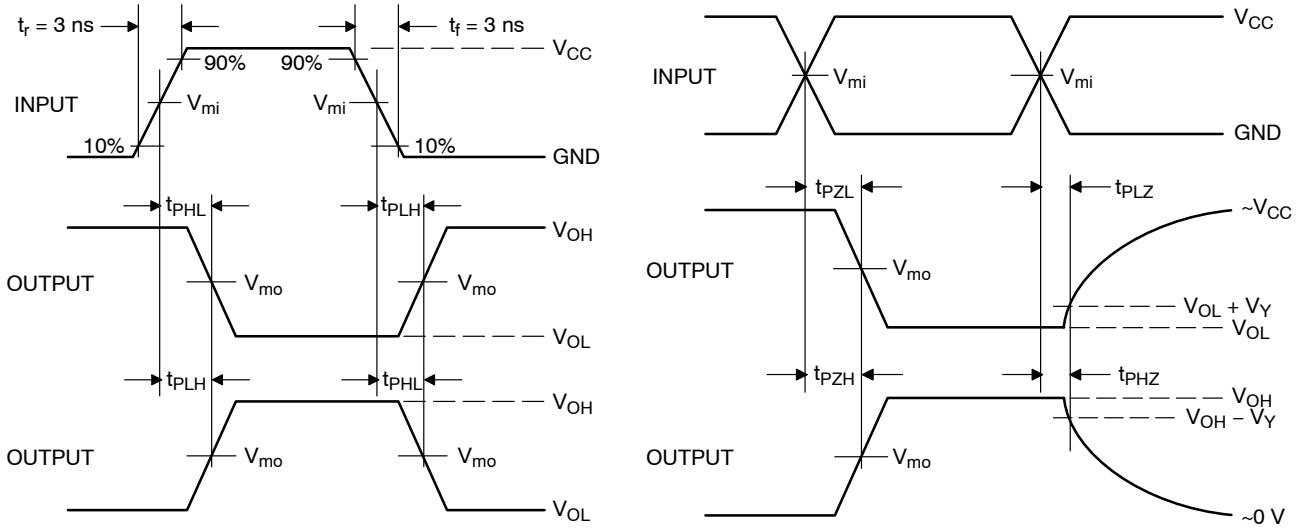
6. C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation $I_{CC(OPR)} = C_{PD} \bullet V_{CC} \bullet f_{in} + I_{CC}$. C_{PD} is used to determine the no−load dynamic power consumption: $P_D = C_{PD} \bullet V_{CC}^2 \bullet f_{in} + I_{CC} \bullet V_{CC}$.



C_L includes probe and jig capacitance
R_T is Z_{OUT} of pulse generator (typically 50 Ω)
f = 1 MHz

Test	Switch Position
t_{PLH} / t_{PHL}	Open
t_{PLZ} / t_{PZL}	6 V @ Vcc = 3.0–3.6 V; 2 x Vcc @ Vcc = 0.9–2.7 V
t_{PHZ} / t_{PZH}	GND

Figure 3. Test Circuit



V_{CC}, V	V_{mi}, V	V_{mo}, V	V_Y, V
0.9	V_{CC} / 2	V_{CC} / 2	0.1
1.1 to 1.3	V_{CC} / 2	V_{CC} / 2	0.1
1.4 to 1.6	V_{CC} / 2	V_{CC} / 2	0.1
1.65 to 1.95	V_{CC} / 2	V_{CC} / 2	0.15
2.3 to 2.7	V_{CC} / 2	V_{CC} / 2	0.15
3.0 to 3.6	1.5	1.5	0.3

Figure 4. Switching Waveforms

ORDERING INFORMATION

Device	Package	Marking	Pin 1 Orientation (See below)	Shipping†
NC7WV07P6X	SC−88	V07	Q4	3000 / Tape & Reel

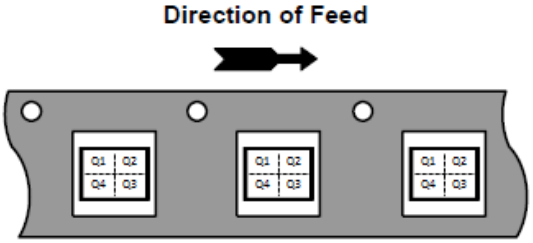
DISCONTINUED (Note 7)

NC7WV07L6X	MicroPak	BC	Q4	5000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

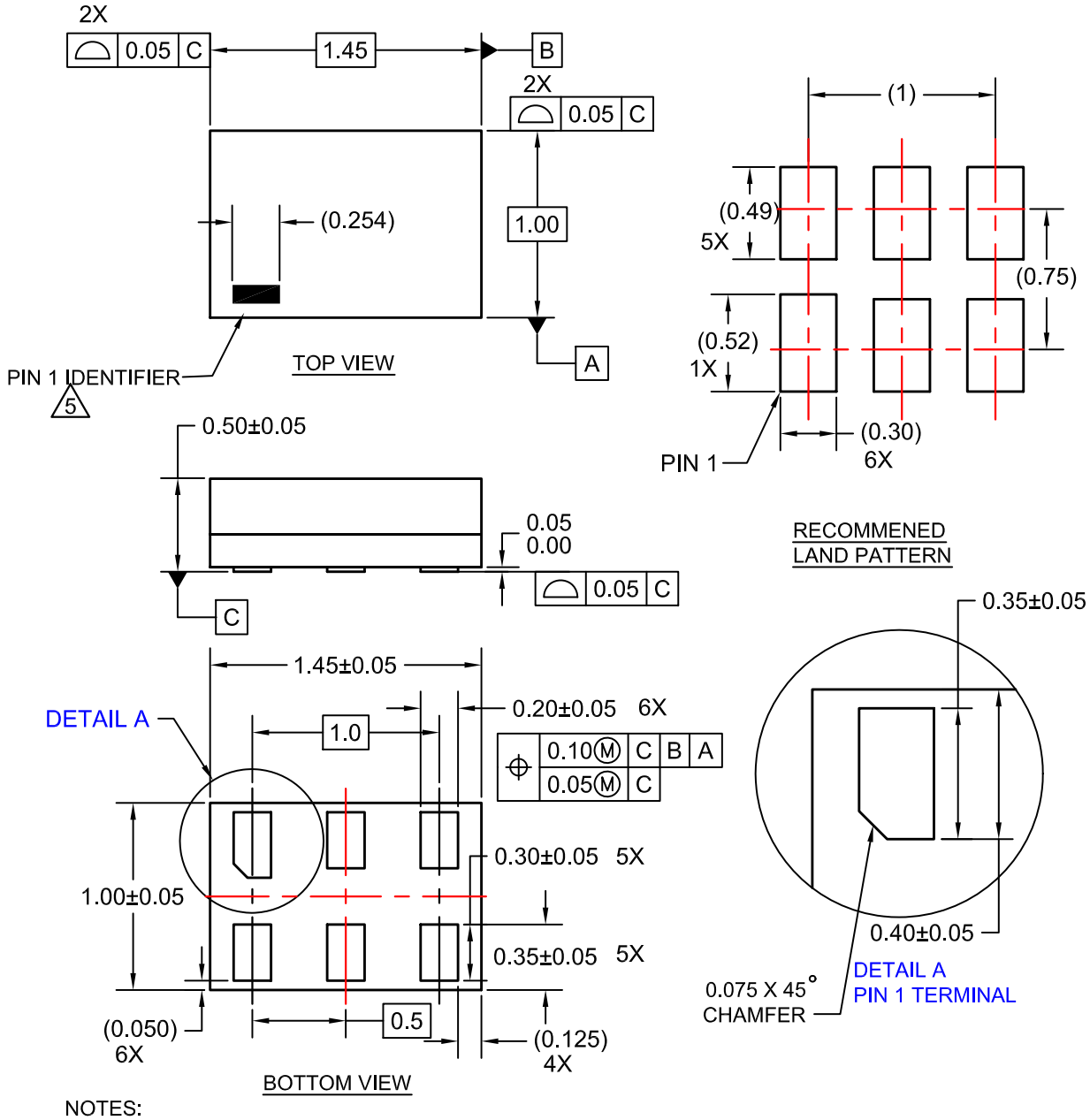
7. **DISCONTINUED:** This device is not recommended for new design. Please contact your **onsemi** representative for information. The most current information on this device may be available on www.onsemi.com.

PIN 1 ORIENTATION IN TAPE AND REEL



SIP6 1.45X1.0
CASE 127EB
ISSUE O

DATE 31 AUG 2016



NOTES:

1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
2. DIMENSIONS ARE IN MILLIMETERS
3. DRAWING CONFORMS TO ASME Y14.5M-2009
4. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY OTHER LINE IN THE MARK CODE LAYOUT.

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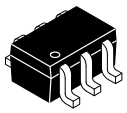
MECHANICAL CASE OUTLINE
PACKAGE DIMENSIONS



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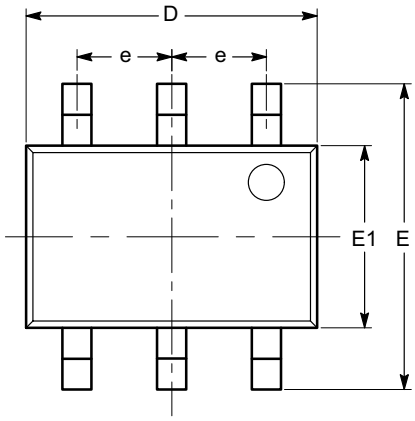
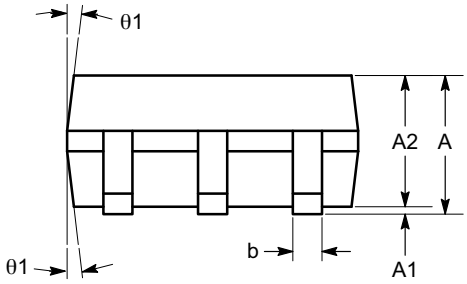
SC−88 (SC−70 6 Lead), 1.25x2
CASE 419AD
ISSUE A

DATE 07 JUL 2010

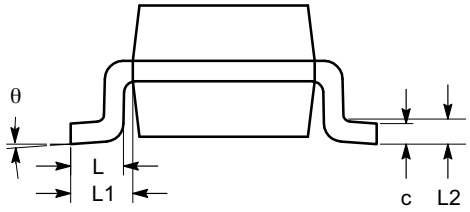


TOP VIEW

SYMBOL	MIN	NOM	MAX
A	0.80		1.10
A1	0.00		0.10
A2	0.80		1.00
b	0.15		0.30
c	0.10		0.18
D	1.80	2.00	2.20
E	1.80	2.10	2.40
E1	1.15	1.25	1.35
e		0.65 BSC	
L	0.26	0.36	0.46
L1		0.42 REF	
L2		0.15 BSC	
θ	0°		8°
θ1	4°		10°



SIDE VIEW



END VIEW

Notes:
(1) All dimensions are in millimeters. Angles in degrees.
(2) Complies with JEDEC MO-203.

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DESCRIPTION:	SC−88 (SC−70 6 LEAD), 1.25X2	PAGE 1 OF 1

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