

### **General Description**

The HXYG50H06NF use advanced SGT MOSFET technology to provide low RDS(ON), low gate charge, fast switching and excellent avalanche characteristics.

This device is specially designed to get better ruggedness and suitable to use in

#### **General Features**

V<sub>DS</sub> =60V I<sub>D</sub> =50 A

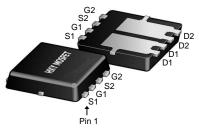
 $R_{DS(ON)}$  < 14m $\Omega$  @  $V_{GS}$ =10V

### **Applications**

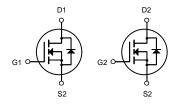
Consumer electronic power supply Motor control

Synchronous-rectification Isolated DC

Synchronous-rectification applications



DFN5X6-8L



**Dual N-Channel MOSFET** 

## **Package Marking and Ordering Information**

Product ID	Pack	Marking	Qty(PCS)
HXYG50H06NF	DFN5X6-8L		5000

# Absolute Maximum Ratings ( $T_C$ =25 $^{\circ}$ C unless otherwise specified)

Symbol	Parameter		Max.	Units
V <sub>DSS</sub>	Drain-Source Voltage		60	V
V <sub>GSS</sub>	Gate-Source Voltage		±20	V
	Continuous Dusin Comment	T <sub>C</sub> = 25°C	50	Α
l <sub>D</sub>	Continuous Drain Current	T <sub>C</sub> = 100°C	29	Α
I <sub>DM</sub>	Pulsed Drain Current note1		180	Α
Eas	Single Pulsed Avalanche Energy no	te2	36	mJ
P <sub>D</sub>	Power Dissipation	T <sub>C</sub> = 25℃	60	W
R <sub>0</sub> JC	Thermal Resistance, Junction	to Case	2.5	°C/W
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Tempe	rature Range	-55 to +175	$^{\circ}$ C



### **Electrical Characteristics** (T<sub>J</sub>=25°C unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Тур.	Max.	Units	
Off Characteristic							
V <sub>(BR)DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =250µA	60	-	-	V	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> =60V, V <sub>GS</sub> =0V,	-	-	1.0	μΑ	
I <sub>GSS</sub>	Gate to Body Leakage Current	V <sub>DS</sub> =0V, V <sub>GS</sub> = ±20V	-	-	±100	nA	
On Charac	teristics						
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250µA	1.0	1.6	2.5	V	
П	Static Drain-Source on-Resistance	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	11	14	mΩ	
R <sub>DS(on)</sub>	note3	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	14	20		
Dynamic C	Characteristics						
C <sub>iss</sub>	Input Capacitance	\/ -25\/ \/ -0\/	-	930	-	pF	
Coss	Output Capacitance	V <sub>DS</sub> =25V, V <sub>GS</sub> =0V, f=1.0MHz	-	230	-	pF	
C <sub>rss</sub>	Reverse Transfer Capacitance	1-1.0101112	-	8	-	pF	
$Q_g$	Total Gate Charge	\/ 00\/ L 00A	-	22	-	nC	
Q <sub>gs</sub>	Gate-Source Charge	$V_{DS}$ =30V, $I_{D}$ =20A, $V_{GS}$ =10V	-	4.5	-	nC	
$Q_{gd}$	Gate-Drain("Miller") Charge	VGS-10V	-	3.5	-	nC	
Switching	Characteristics						
t <sub>d(on)</sub>	Turn-on Delay Time		-	4.5	-	ns	
t <sub>r</sub>	Turn-on Rise Time	V <sub>DD</sub> =30V, I <sub>D</sub> =20A,	-	2.7	-	ns	
t <sub>d(off)</sub>	Turn-off Delay Time	$R_G=1.6\Omega$ , $V_{GS}=10V$	-	13.8	-	ns	
t <sub>f</sub>	Turn-off Fall Time		-	2.7	-	ns	
Drain-Soul	rce Diode Characteristics and Maxim	um Ratings					
1.	Is Maximum Continuous Drain to Source Diode Forward Current				45	_	
IS			-	-	45	Α	
I <sub>SM</sub>	Maximum Pulsed Drain to Source Diode Forward Current			-	180	Α	
V <sub>SD</sub>	Drain to Source Diode Forward	V <sub>GS</sub> =0V, I <sub>S</sub> =30A	_	_	1.2	V	
v SD	Voltage	VGS-UV, IS-JUA	-	-	1.2	V	
t <sub>rr</sub>	Body Diode Reverse Recovery Time	T <sub>J</sub> =25℃,	-	18	-	ns	
Qrr	Body Diode Reverse Recovery	I <sub>F</sub> =20A,dI/dt=100A/μs	_	12	_	nC	
- Sii	Charge	- 1- 20/1,4//41-100/1/μο		12	_	110	

Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

- 2. EAS condition: TJ=25  $^{\circ}\text{C}$  , VDD=30V, VG=10V, RG=25 $\Omega$ , L=0.5mH, IAS=12A
- 3. Pulse Test: Pulse Width≤300µs, Duty Cycle≤0.5%



# **Typical Performance Characteristics**

Figure1: Output Characteristics

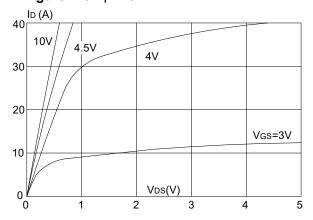


Figure 3:On-resistance vs. Drain Current

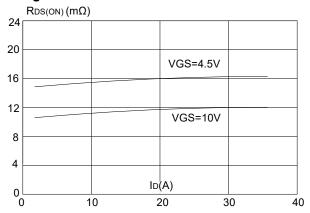


Figure 5: Gate Charge Characteristics

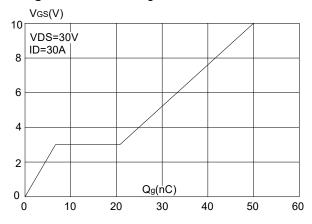


Figure 2: Typical Transfer Characteristics

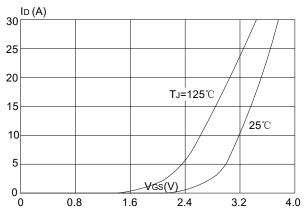


Figure 4: Body Diode Characteristics

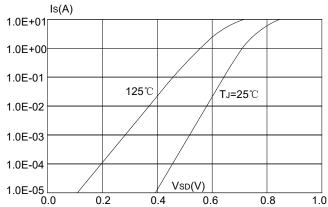
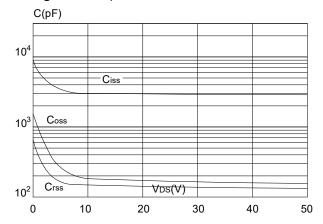


Figure 6: Capacitance Characteristics



**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature

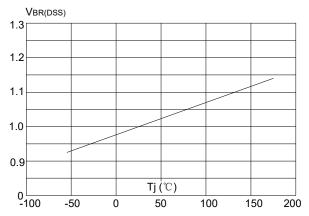
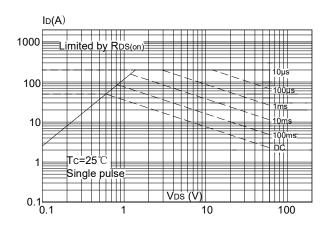
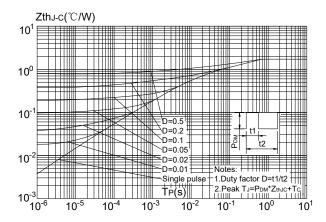


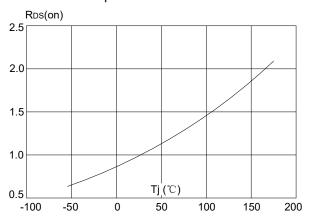
Figure 9: Maximum Safe Operating Area



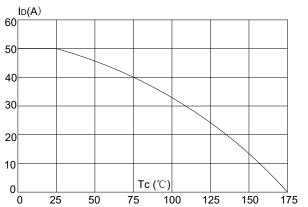
**Figure.11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case



**Figure 8:** Normalized on Resistance vs. Junction Temperature

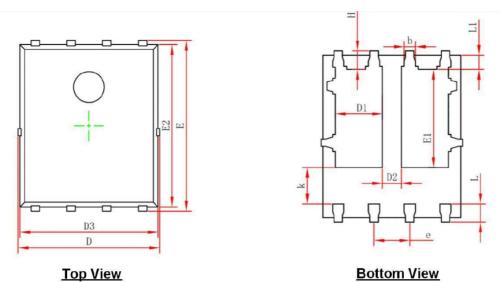


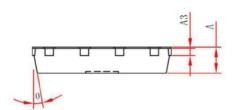
**Figure 10:** Maximum Continuous Drain Current vs. Case Temperature





# Package Mechanical Data- PDFN5X6-8L





Side View

Symbol	Dimensions In Millimeters		Dimensions In Inches		
	Min.	Max.	Min.	Max.	
Α	0.900	1.000	0.035	0.039	
A3	0.154	AREF.	0.006REF.		
D	4.944	5.096	0.195	0.201	
E	5.974	6.126	0.235	0.241	
D1	1.470	1.870	0.058	0.074	
D2	0.470	0.870	0.019	0.034	
E1	3.375	3.575	0.133	0.141	
D3	4.824	4.976	0.190	0.196	
E2	5.674	5.826	0.223	0.229	
k	1.190	1.390	0.047	0.055	
b	0.350	0.450	0.014	0.018	
е	1.270TYP.		0.050TYP.		
L	0.559	0.711	0.022	0.028	
L1	0.424	0.576	0.017	0.023	
Н	0.574	0.726	0.023	0.029	
θ	10°	12°	10°	12°	



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