

SEMITRANS<sup>®</sup> 3

**Ultra Fast IGBT Modules** 

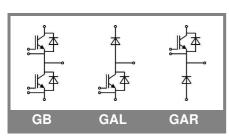
## SKM 200GB125D **SKM 200GAL125D SKM 200GAR125D**

## **Features**

- N channel, homogeneous Si •
- Low inductance case
- Short tail current with low temperature dependence
- High short circuit capability, self limiting to 6 x I<sub>cnom</sub> • Fast & soft inverse CAL diodes
- Isolated copper baseplate using . DCB Direct Copper Bonding Technology
- Large clearance (13 mm) and creepage distance (20 mm)

## **Typical Applications\***

- Switched mode power supplies at  $f_{sw} > 20 \text{ kHz}$
- Resonant inverters up to 100 kHz
- Inductive heating
- Electronic welders at f<sub>sw</sub> > 20 kHz



Absolu	te Maximum Ratings	$T_c = 2$	25 °C, unless otherwise	e specified
Symbo	I Conditions		Values	Units
IGBT				
V <sub>CES</sub>	T <sub>j</sub> = 25 °C T <sub>i</sub> = 150 °C		1200	V
I <sub>C</sub>	T <sub>j</sub> = 150 °C	T <sub>case</sub> = 25 °C	200	А
		T <sub>case</sub> = 80 °C	160	А
I <sub>CRM</sub>	I <sub>CRM</sub> =2xI <sub>Cnom</sub>		300	А
$V_{GES}$			± 20	V
t <sub>psc</sub>	$\label{eq:V_CC} \begin{array}{l} V_{CC} \mbox{=} 600 \mbox{ V};  V_{GE} \leq 20 \mbox{ V}; \\  V_{CES} \mbox{=} 1200 \mbox{ V} \end{array}$	T <sub>j</sub> = 125 °C	10	μs
Inverse	Diode			
I <sub>F</sub>	T <sub>j</sub> = 150 °C	T <sub>case</sub> = 25 °C	200	А
		T <sub>case</sub> = 80 °C	130	А
I <sub>FRM</sub>	I <sub>FRM</sub> =2xI <sub>Fnom</sub>		300	А
I <sub>FSM</sub>	t <sub>p</sub> = 10 ms; sin.	T <sub>j</sub> = 150 °C	1440	А
Freewh	eeling Diode			
I <sub>F</sub>	T <sub>j</sub> = °C	T <sub>c</sub> = 25 °C	200	A
		T <sub>c</sub> = 80 °C	130	А
I <sub>FRM</sub>	I <sub>FRM</sub> =2xI <sub>Fnom</sub>		300	А
I <sub>FSM</sub>	t <sub>p</sub> = 10 ms;	T <sub>j</sub> = 150 °C	1440	А
Module	)			
I <sub>t(RMS)</sub>			500	А
T <sub>vj</sub>			- 40+ 150	°C
T <sub>stg</sub>			- 40+ 125	°C
V <sub>isol</sub>	AC, 1 min.		4000	V

Characteristics		T <sub>c</sub> =	= 25 °C, unless otherwise specified			
Symbol	Conditions		min.	typ.	max.	Units
IGBT						
V <sub>GE(th)</sub>	$V_{GE} = V_{CE}, I_C = 6 \text{ mA}$		4,5	5,5	6,5	V
I <sub>CES</sub>	$V_{GE}$ = 0 V, $V_{CE}$ = $V_{CES}$	T <sub>j</sub> = 25 °C		0,15	0,45	mA
V <sub>CE0</sub>		T <sub>j</sub> = 25 °C		1,5	1,75	V
		T <sub>j</sub> = 125 °C				V
r <sub>CE</sub>	V <sub>GE</sub> = 15 V	T <sub>j</sub> = 25°C		12	14	mΩ
		T <sub>j</sub> = 125°C				mΩ
V <sub>CE(sat)</sub>	I <sub>Cnom</sub> = 150 A, V <sub>GE</sub> = 15 V	T <sub>j</sub> = °C <sub>chiplev.</sub>		3,3	3,85	V
C <sub>ies</sub>				10	13	nF
C <sub>oes</sub>	$V_{CE}$ = 25, $V_{GE}$ = 0 V	f = 1 MHz		1,5	2	nF
C <sub>res</sub>				0,8	1,2	nF
Q <sub>G</sub>	V <sub>GE</sub> = 0V - +20V			1300		nC
R <sub>Gint</sub>	T <sub>j</sub> = °C			2,5		Ω
t <sub>d(on)</sub>				75		ns
t <sub>r</sub>	$R_{Gon} = 4 \Omega$	V <sub>CC</sub> = 600V		36		ns
E <sub>on</sub>		I <sub>C</sub> = 150A		14		mJ
t <sub>d(off)</sub>	$R_{Goff}$ = 4 $\Omega$	T <sub>j</sub> = 125 °C		420		ns
t <sub>f</sub>		$V_{GE} = \pm 15V$		25		ns
E <sub>off</sub>						mJ
R <sub>th(j-c)</sub>	per IGBT				0,09	K/W



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#### Features

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- Isolated copper baseplate using DCB Direct Copper Bonding Technology
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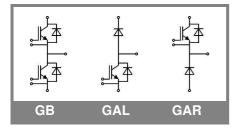
## **Typical Applications\***

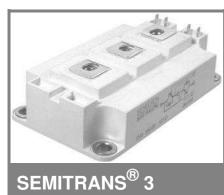
- Switched mode power supplies at  $f_{sw} > 20 \text{ kHz}$
- Resonant inverters up to 100 kHz Inductive heating
- Electronic welders at f<sub>sw</sub> > 20 kHz

Characteristics							
Symbol	Conditions		min.	typ.	max.	Units	
Inverse Diode							
$V_F = V_{EC}$	$I_{Fnom}$ = 150 A; $V_{GE}$ = 0 V	T <sub>j</sub> = 25 °C <sub>chiplev.</sub>		2	2,5	V	
		$T_j = 125 \ ^\circ C_{chiplev.}$ $T_j = 25 \ ^\circ C$		1,8		V	
V <sub>F0</sub>				1,1	1,2	V	
		T <sub>j</sub> = 125 °C				V	
r <sub>F</sub>		T <sub>j</sub> = 25 °C		6	8,7	mΩ	
		T <sub>j</sub> = 125 °C T <sub>j</sub> = 125 °C				mΩ	
I <sub>RRM</sub>	I <sub>F</sub> = 150 A	T <sub>j</sub> = 125 °C		230		А	
Q <sub>rr</sub>	di/dt = 5500 A/µs			24		μC	
E <sub>rr</sub>	V <sub>GE</sub> = 0 V; V <sub>CC</sub> = 600 V					mJ	
R <sub>th(j-c)D</sub>	per diode				0,25	K/W	
Freewhe	eling Diode						
$V_F = V_{EC}$	$I_{Fnom}$ = 150 A; $V_{GE}$ = 0 V			2	2,5	V	
		$T_j = 125 \ ^\circ C_{chiplev.}$ $T_j = 25 \ ^\circ C$		1,8		V	
V <sub>F0</sub>		T <sub>j</sub> = 25 °C		1,1	1,2	V	
		T <sub>j</sub> = 125 °C				V	
r <sub>F</sub>		T <sub>j</sub> = 25 °C		6	8,7	V	
		T <sub>j</sub> = 125 °C				V	
I <sub>RRM</sub>	I <sub>F</sub> = 150 A	T <sub>j</sub> = 125 °C		230		Α	
Q <sub>rr</sub>	di/dt = 5500 A/µs			24		μC	
E <sub>rr</sub>	V <sub>GE</sub> = 0 V; V <sub>CC</sub> = 600 V					mJ	
R <sub>th(j-c)FD</sub>	per diode				0,25	K/W	
Module							
L <sub>CE</sub>				15	20	nH	
R <sub>CC'+EE'</sub>	res., terminal-chip	T <sub>case</sub> = 25 °C		0,35		mΩ	
		T <sub>case</sub> = 125 °C		0,5		mΩ	
R <sub>th(c-s)</sub>	per module				0,038	K/W	
M <sub>s</sub>	to heat sink M6		3		5	Nm	
M <sub>t</sub>	to terminals M6		2,5		5	Nm	
w					325	g	

This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

\* The specifications of our components may not be considered as an assurance of component characteristics. Components have to be tested for the respective application. Adjustments may be necessary. The use of SEMIKRON products in life support appliances and systems is subject to prior specification and written approval by SEMIKRON. We therefore strongly recommend prior consultation of our personal.





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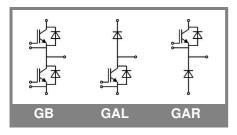
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#### **Features**

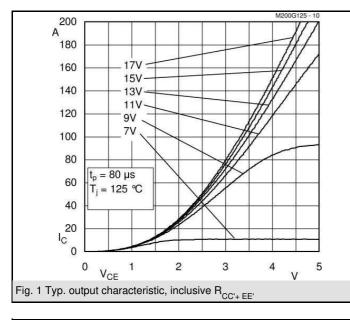
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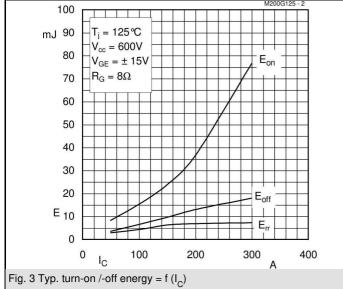
#### **Typical Applications\***

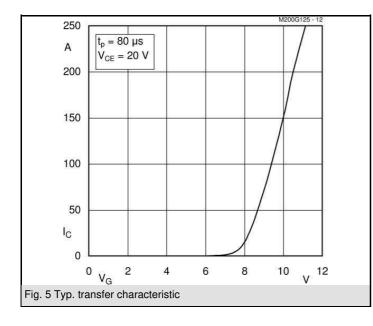
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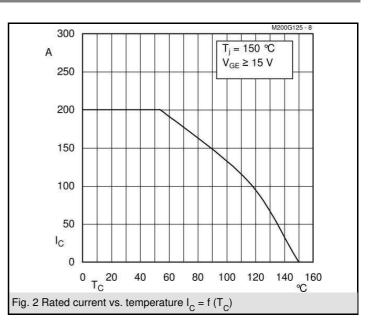


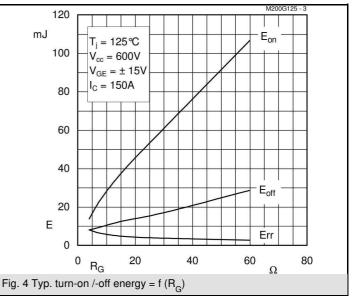
Z <sub>th</sub> Symbol	Conditions	Values	Units
	Conditions	Values	Onits
Z R <sub>i</sub>		20	1
Ri	i = 1	60	mk/W
R <sub>i</sub>	i = 2	23	mk/W
R <sub>i</sub>	i = 3	5,9	mk/W
R <sub>i</sub>	i = 4	1,1	mk/W
tau <sub>i</sub>	i = 1	0,0744	s
tau	i = 2	0,0087	s
tau <sub>i</sub>	i = 3	0,002	s
tau <sub>i</sub>	i = 4	0,0015	s
Z <sub>Ri</sub> th(j-c)D			
R <sub>i</sub>	i = 1	160	mk/W
R <sub>i</sub>	i = 2	67	mk/W
R <sub>i</sub>	i = 3	20	mk/W
R <sub>i</sub>	i = 4	3	mk/W
tau <sub>i</sub>	i = 1	0,0536	S
tau <sub>i</sub>	i = 2	0,0034	S
tau <sub>i</sub>	i = 3	0,077	s
tau <sub>i</sub>	i = 4	0,0003	s

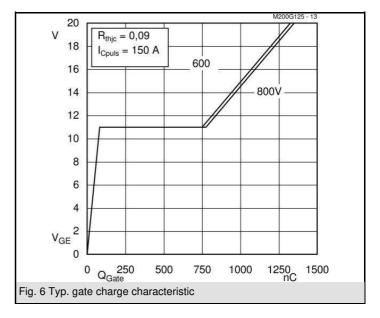


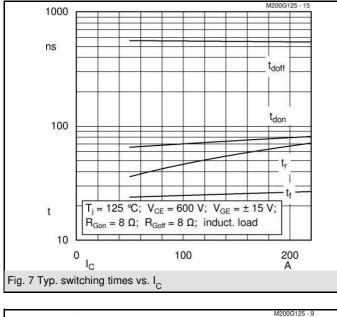


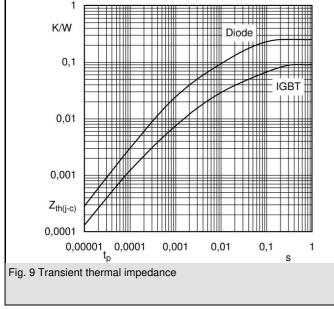


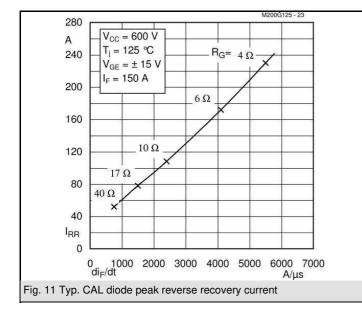


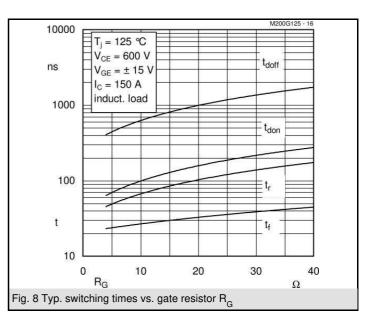


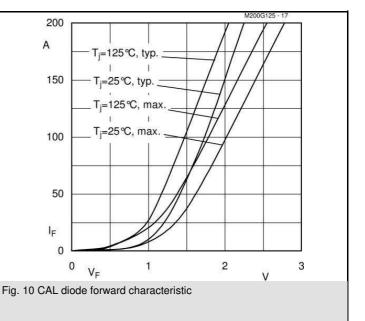


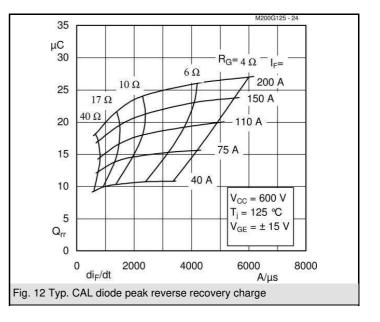












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